

ARM Instructions Worksheet #3

Addressing Modes

Prerequisite Reading: Chapter 4

Revised: March 26, 2020

Objectives: To use the web-based simulator ("CPULator") to better understand the four addressing modes:

1. Immediate Offset Addressing: [R1] and [R1,4]

2. Register Offset Addressing: [R1,R2] and [R1,R2,LSL 2]

3. Post-Indexed Addressing: [R1],44. Pre-Indexed Addressing: [R1,4]!

To do offline: Answer the questions that follow the listing below. (Numbers at far left are memory addresses.)

```
.syntax
                                 unified
                    .global
                                 _start
                    .skip
                                 0x100
00000100
         Array32: .word 0xBEEFBEEF
                                              // uint32 t Array[4];
00000104
                    .word 0xC0DEC0DE
00000108
                    .word 0xF00DF00D
                    .word 0xFACEFACE
0000010C
                          R1,=Array32
                                              // *** EXECUTION STARTS HERE ***
00000110
          start:
                   LDR
                          R0,[R1]
                                              // Address provided by R1
00000114
                   LDR
00000118
                   LDR
                          R0,[R1,4]
                                              // Address = R1 + 4
0000011C
                    LDR
                          R2,=8
                                              // R2 = Offset = 8
00000120
                    LDR
                          R0,[R1,R2]
                                              // Address = R1 + R2
00000124
                   LDR
                          R2.=3
                                              // R2 = Subscript = 3
00000128
                    LDR
                          R0,[R1,R2,LSL 2]
                                              // Address = R1 + 4*R2
                    LDR
0000012C
                          R0,[R1],4
                                              // Address = R1; Post-Increment
                                              // Address = R1 + 4; Pre-Increment
00000130
                    LDR
                          R0,[R1,4]!
00000134 done:
                          done
                                              // infinite loop
                    .end
```

What hex <u>address</u> is copied into R1 by the LDR instruction at address 00000110_{16} ?

What hex <u>data</u> is copied from the address in R1 by the LDR at address 00000114₁₆?

What hex <u>data</u> is copied into R0 by the LDR instruction at address 00000118_{16} ?

What hex <u>address</u> did that value come from?

What hex *data* is copied into R0 by the LDR instruction at address 00000120₁₆?

What hex <u>address</u> did that value come from?

The address of Array32[0] or 00000100

The data of Array32[0] or BEEFBEEF

The data of Array32[1] or C0DEC0DE

The address of Array32[1] or 00000104

The data of Array32[2] or F00DF00D

he address of Array32[2] or 00000108

What hex $\underline{\textit{data}}$ is copied into R0 by the LDR instruction at address 00000128	The data of Array32[3] or FACEFAC
What hex <u>address</u> did that value come from?	The address of Array32[3] or 0000010
What hex \underline{data} is copied into R0 by the LDR instruction at address 0000012C	The data of Array32[0] or BEEFBEE
What hex <u>address</u> did that value come from?	The address of Array32[0] or 0000010
What hex $\underline{\it address}$ is left in R1 by the LDR instruction at address $0000012C_{16}$? 00000100+4 or 00000104
What hex <u>data</u> is copied into R0 by the LDR instruction at address 00000130	The data of Array32[2] or F00DF00E
What hex <u>address</u> did that value come from?	00000104+4 or 00000108
What hex <u>address</u> is left in R1 by the LDR instruction at address 00000130_{16} ?	00000104+4 or 00000108
1. Click here to open a browser for the ARM instruction simulator with process. Press Ctrl-M to open the memory display window and drag-n-drop it also. In the "Memory" window, enter 0x100 into the search box and press Estep 1: Press F2 exactly 2 times to execute the first two LDR instructions. (The 3step 1) and the search box are pressed in the search box and press Estep 1: Press F2 exactly 2 times to execute the first two LDR instructions.	re-loaded code. bout halfway to the right. Enter to highlight that address for easy reference.
What hex <u>address</u> is copied into R1 by the LDR instruction at address 00000	0110 ₁₆ ? 00000100
What hex <u>data</u> is copied from the address in R1 by the LDR at address 0000	00114 ₁₆ ? beefbeef
Step 2: Press F2 exactly once to execute the LDR R0, [R1,#4]	
What hex <u>data</u> is copied into R0 by the LDR instruction at address 0000011	8 ₁₆ ? c0dec0de
What hex <u>address</u> did that value come from?	00000104
Step 3: Press F2 exactly 2 times to execute the LDR R2,=8 (MOV R2,#8) and	d the LDR R0,[R1,R2]
What hex <u>data</u> is copied into R0 by the LDR instruction at address 00000120	0 ₁₆ ? f00df00d
What hex <u>address</u> did that value come from?	00000108
Step 4: Press F2 exactly 2 times to execute the LDR R2,=3 (MOV R2,#3) and	the LDR R0,[R1,R2,LSL #2]
What hex <u>data</u> is copied into R0 by the LDR instruction at address 0000012	3 ₁₆ ? faceface
What hex <u>address</u> did that value come from?	0000010c
Step 5: Press F2 exactly once to execute the LDR R0, [R1],#4	
What hex <u>data</u> is copied into R0 by the LDR instruction at address 0000012	C ₁₆ ? beefbeef
What hex <u>address</u> did that value come from?	00000100
What hex <u>address</u> is left in R1 by the LDR instruction at address 0000012C ₁	6? 00000104
Step 6: Press F2 exactly once to execute the LDR R0, [R1,#4]!.	
What hex \underline{data} is copied into R0 by the LDR instruction at address 00000130	O ₁₆ ? f00df00d
What hex <u>address</u> did that value come from?	00000108
What hex <u>address</u> is left in R1 by the LDR instruction at address 00000130_1	00000108