

C)

Address Mnemonics Hexcode Bytes M-cycle T-State

0000	MVI A, 30	3E	2	2	7
0001		30			4
0002	MOV B, A	47	1	1	4
0003	MOV C, A	4F	1	1	4
0004	MOV H, A	67	1	1	4
0005	MOV L, A	6F	1	1	4

0005 MOV L, A CF 1 1 4

B) Address Memory's Hexades Byte n-cycles 7-stages

0000 LXI H, 3245 21 3 3 10

0001 45

0002 32

0003 LXI D, 6578 11 3 3 10

0004 78

0005 65

0006 XCHG EB 1 1 4

A.]

Address	Mnemonic	Hexcode	Bytes	M-cycles	T-stages
0000	MVJ A, 35	35	2	2	7
0001		35			
0002	MVJ B, 76	76	2	2	7
0003		76			
0004	ADD B	80	4	1	4