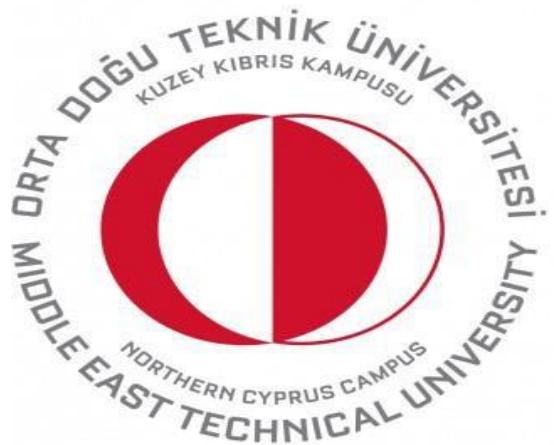


# **EXPERIMENT IV BASIC APPLICATIONS OF OPERATIONAL AMPLIFIERS**



**Fall - 2023/2024**

**EEE 281 – Electrical**

**Circuits Lab 4 Report**

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**DATE:**

**Objective:**

In this experiment we will study the basic applications of operational amplifiers.

1. We will learn how to control transfer characteristics of circuits using different resistor configurations.
2. We will learn how to apply node analysis to op-amp circuits.
3. We will learn to use feedback techniques to deliver op-amp circuits.
4. We will observe the fundamental specifications and limitations of practical op-amps.
5. We will use a function generator and oscilloscope during our experiments.

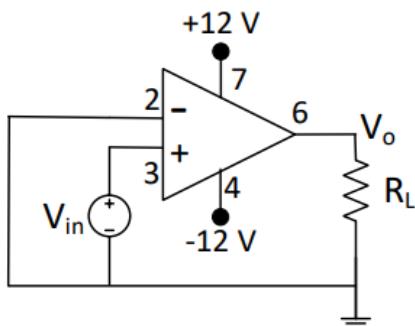
**Equipment & Component List:**

DC Power Supply

Function/Arbitrary Waveform Generator

Digital Oscilloscope Op-Amp ( $\mu$ A741)

Resistors (2x100  $\Omega$ , 4 x 1k $\Omega$ )

**Results:****1) Open-loop operation (without feedback)**

We created the above circuit in the lab environment by making the essential connections. And measure the value of output voltage ( $V_o$ ) by applying the values of  $V_{in}$  and  $R_L$ .

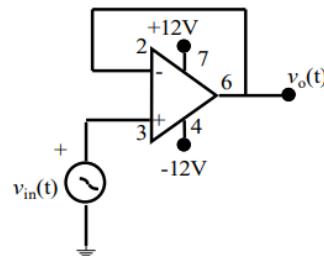
$V_{in}(V)$	$R_L(\Omega)$	$V_o(V)$	Input current, $i_+(mA)$	Is output voltage saturated? (Yes/No)	Is output voltage limited by op-amp output current limit? (Yes/No)
0.002	3000	11.06	1	Yes	Yes
0.002	200	4.32	1	Yes	Yes
2	200	4.20	1	Yes	Yes

Table 1

While measuring the values of  $V_o$  by the given  $V_{in}$  values we skipped the  $V_{in} = 10 \mu V$  value since the value is too low for the equipment.

Comparing the measured values of the output voltage from Table 1 and the calculated values from the pre-work, we see that the values are nearly same with each other. The discrepancies between the values can be caused by the temperature variations, material aging and the resistance of the cables in the experiment part.

## 2) Voltage follower (buffer) characterization



**Figure 4.2** Voltage follower (buffer)

We created the above circuit with the given value of  $V_{in}(t) = 3\sin 1000\pi t$ . Setting the function generator in HZ mode, implying the values of frequency as 500 Hz and the amplitude as 6 Vpp in function generator our circuit is completed.

A)

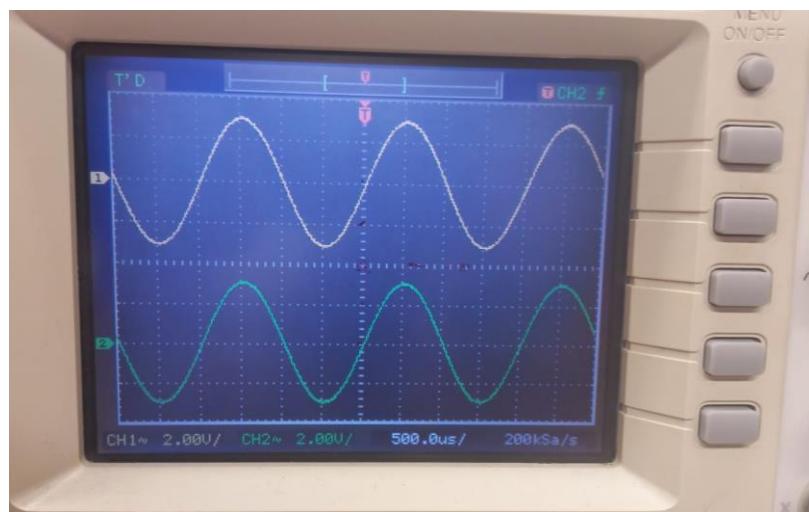


Figure 1: Non saturated

In figure 1, arranging the amplitude as 6 Vpp and connecting channel 2 to the output voltage, we don't observe saturation effects on the oscilloscope.

We increased the amplitude until the output saturates and observed that the maximum amplitude of  $V_{in}(t)$  is 20 Vpp.

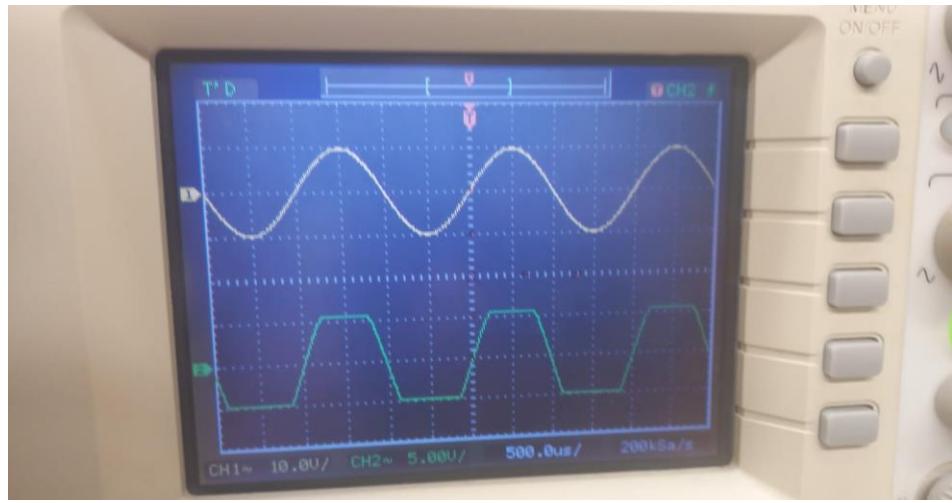


Figure 2: Saturated

In figure 2, arranging the amplitude as 20 Vpp, we observe the graph of the output voltage in the oscilloscope. We observe that this value is accurate with the value in the op-amp data sheet which is 20 V/mV.

**B)**

We obtain the graph of  $V_o$  versus  $V_{in}$  using the oscilloscope by connecting channel 1 to the  $V_{in}$  and connecting channel 2 to  $V_o$ .

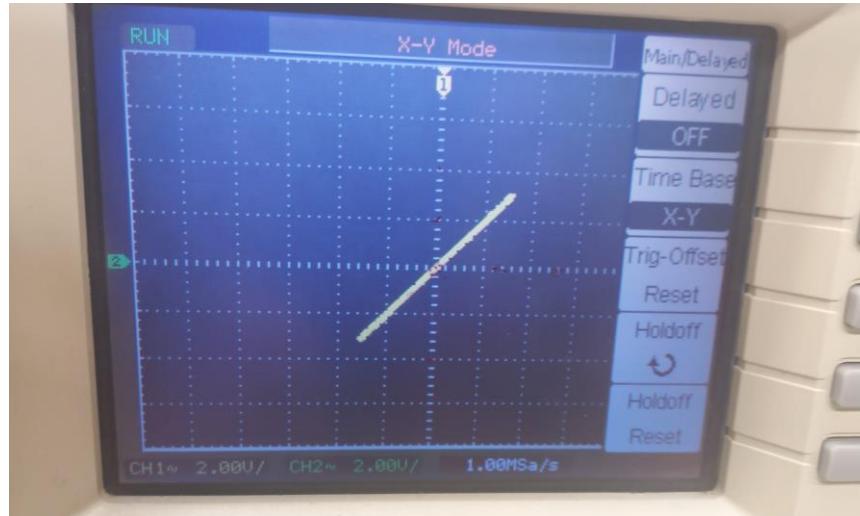


Figure 3: Non saturated

In figure 3, we can observe that  $V_o$  has a linearly increasing graph between  $-V_{cc}$  and  $+V_{cc}$  until the amplitude is 20 Vpp.

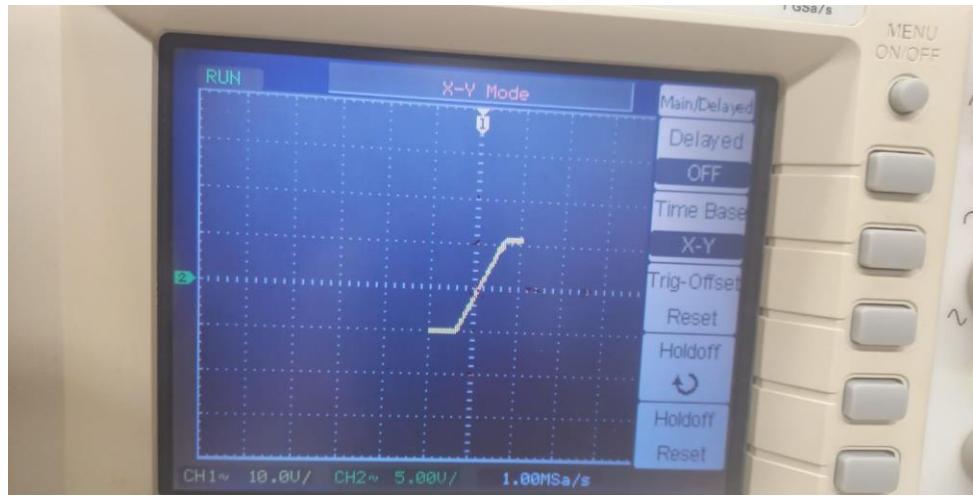
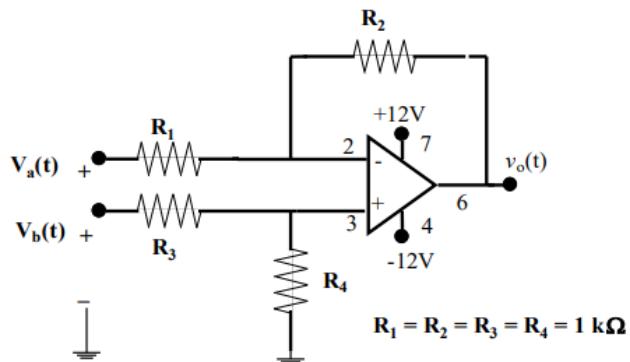


Figure 4: Saturated

In figure 4, we can observe that  $V_o$  is saturated when the amplitude is greater and equal to 20 Vpp.  $V_o$  is saturated if it is less than  $-V_{cc}$  and greater than  $+V_{cc}$ . We viewed the graph as fixed when these conditions are provided.

### 3) Summer and Subtractor (Difference) Circuits



We created the above circuit with the given values  $V_a(t) = 2\sin 1000\pi t$  V,  $V_b(t) = 2$  V and  $R_1=R_2=R_3=R_4=1\text{k}\Omega$ .

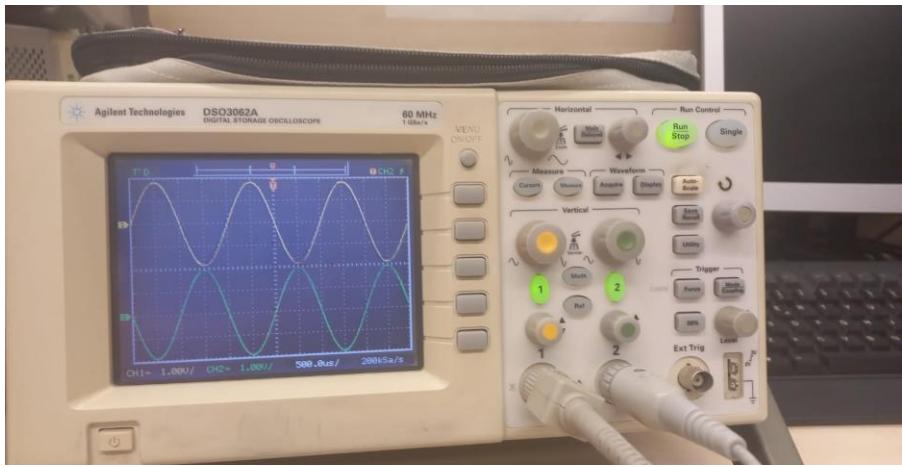


Figure 5

In figure 5, we observed the signals by connecting channel 1 to the  $V_a(t)$  and connecting channel 2 to  $V_o(t)$ . We saw that the calculations and the simulated signals are the same as our experimental set up values.

Unfortunately, we didn't reduce the  $V_b(t)$  slowly and observe what happens and we couldn't set up the summer circuit due to the time limit of the lab hours.

**Conclusion:**