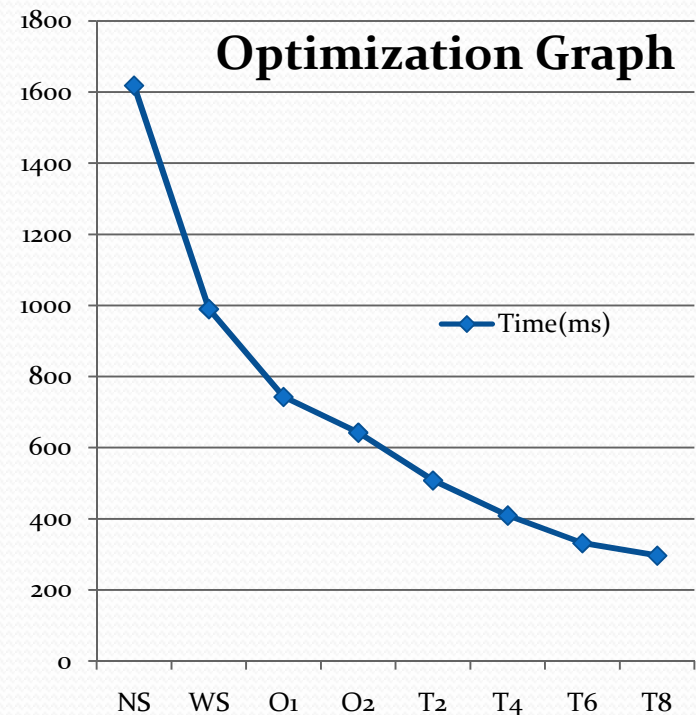# Siwir2 Seminar

## - Karnajit Sen , Ramyar Biniazan

### What we have done:

- Aligned Memory Allocation
- Padding and anti-aliasing
- Loop unrolling
- Loop fusion
- AVX intrinsic
- Modularized Code
- Exploited Symmetry of the domain w.r.t X-axis
- OpenMP Parallelization
- Some more optimizations.

**Optimization Graph**

Time(ms)

# Hmm!!! We are not the fastest group. What else can we do?

- It seems there is some symmetry about Y-axis too.

- More use of spatial locality

- North and South Grid points are bad!!!

- Can we change Grid object to calculate future data?

- Move to MPI from OpenMP.