

Consensus Form

Date: 3/11/2020

Members Present:

Felicia F

Jacques C

Segev Adler

Siddhanta Darda

Karn Watcharasupat

Time meeting began: 4:30 pm

Time meeting ended: 6:05 pm

◆ This form is to be completed after each group meeting (both in and out of lab). Therefore, each group should complete a minimum of two consensus forms per week.

You may use the back of this form if additional space is needed.

Today We Discussed:

(Describe the main topics discussed, worked on, or completed during the meeting.)

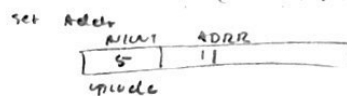
- settle admin matters

- summarized tasks

- planning out approach

- create read-cycle & write-cycle

1) set CTRL to READ ($\overline{WE} \leftarrow '1', \overline{CE} \leftarrow '0'$)



2) read-cycle

3) write-cycle

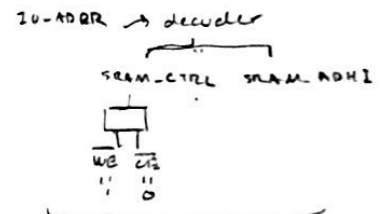
IO-cycle & Addr

0: R00	4: W00
1: R01	5: W01
2: R10	6: W10
3: R11	7: W11

B - P

LOAD addr-low
OUT SRAM-
I/O DATA

data to be decoded



all = $\overline{stomp-clk}$, $\overline{IO-DATA}$, $\overline{IO-WRITE}$
in to SRAM

EQV SRAM - ROW k110

CLK @ 50 MHz
3 cycles ✓

Action Items:

(List the tasks being worked on for the next meeting. In parenthesis next to each action item, write the name of the team member responsible for completing the task)

◆ IO-DECODER = Felicia & Segev

◆ SRAM-READ = Jacques & Siddhanta
+ UML

◆ SRAM-SKELETON = Karn

◆

Consensus Form

Date: 03/10/20

Members Present:

Felicia E

Karn Wathanasuput

Segev Apter

Jacques C

Siddhanta Panda

Time meeting began: 6:15 pm

Time meeting ended: 7:45 pm

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Today We Discussed:

(Describe the main topics discussed, worked on, or completed during the meeting.)

- Created group
- Completed lab exercise to familiarize ourselves with the SRAM and SCOMP (modified)
- Established first external meeting, 03/11/20, to discuss implementation, deadlines, divide work, and to try working on the VHDL & asm code.
- Current ideas
 - SRAM interface with SCOMP → aim to skip the decoder if possible and implement assembly I/O directly to SRAM
 - bulk of work will likely be in VHDL or asm
 - how to execute for longer
 - how to control read vs write?

Action Items:

(List the tasks being worked on for the next meeting. In parenthesis next to each action item, write the name of the team member responsible for completing the task)

- ◆ Devise a set method of approach / proposal for the project - All members
- ◆ Determine meeting dates and deadlines - All members
- ◆ Divide work / create shared folders / git project / methods of communication - All members
- ◆ Begin code portion of project - All members

Consensus Form

Date: 17 Mar 2020

Members Present: Karn Watcharasupat

Time meeting began: 18:00 EDT

Felicia E

Siddhanta Panda

Segev Apter

Time meeting ended: 20:30 EDT

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Today We Discussed:

(Describe the main topics discussed, worked on, or completed during the meeting.)

- Review of previous week's work on IO_DECODER and read cycle for SRAM
- Address allocation for IO_DECODER
- Added tristate bus to SRAM controller
- Write cycle timing, states, and assembly instructions
- Delegate work for write cycle

Action Items:

(List the tasks being worked on for the next meeting. In parenthesis next to each action item, write the name of the team member responsible for completing the task)

◆ IO_DECODER control signal and address allocation in VHDL (Karn)

◆ Write cycle UML and synchronization diagram (Felicia & Segev)

◆ Add write cycle to SRAM Controller VHDL (Siddhanta & Jacques)

◆

Consensus Form

Date: 04/01/2020

Members Present: Siddhanta Panda

Time meeting began: 18:00

Felicia E

Karn Watcharasupat

Jacques Crawford

Time meeting ended: 20:00

Segev Apter

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Today We Discussed:

(Describe the main topics discussed, worked on, or completed during the meeting.)

Proposal

Introduction

- Problem statement
- Requirements

-Minor fix to UML

-Assembly code for verification of read cycle

Technical Approach

- Assembly and User Interaction.
- Read Cycle
- Write Cycle

Project Timeline

- Current progress
- Future work
- Contingency plan

Action Items:

(List the tasks being worked on for the next meeting. In parenthesis next to each action item, write the name of the team member responsible for completing the task)

◆ Proposal slides: Introduction -Felicia

Assembly and User interaction - Felicia and Karn
Read Cycle - Siddhanta and Jacques

◆ Write Cycle - Siddhanta and Segev

◆ Minor fix to UML – Felicia

◆ Simulation