

The diagram illustrates the internal logic of the control unit for a 68000 microprocessor. It features a 74LS04N inverter, a 74LS161N counter, a 74LS154N decoder, and four AT28C16-2 memory chips. The control unit is connected to the 68000 microprocessor via a 32-bit data bus and a 16-bit address bus. The control unit also controls the 68000's internal logic via a 16-bit control bus. The control unit is labeled 'CONTROL UNIT' and 'VER 1.1'.

VER 1.1