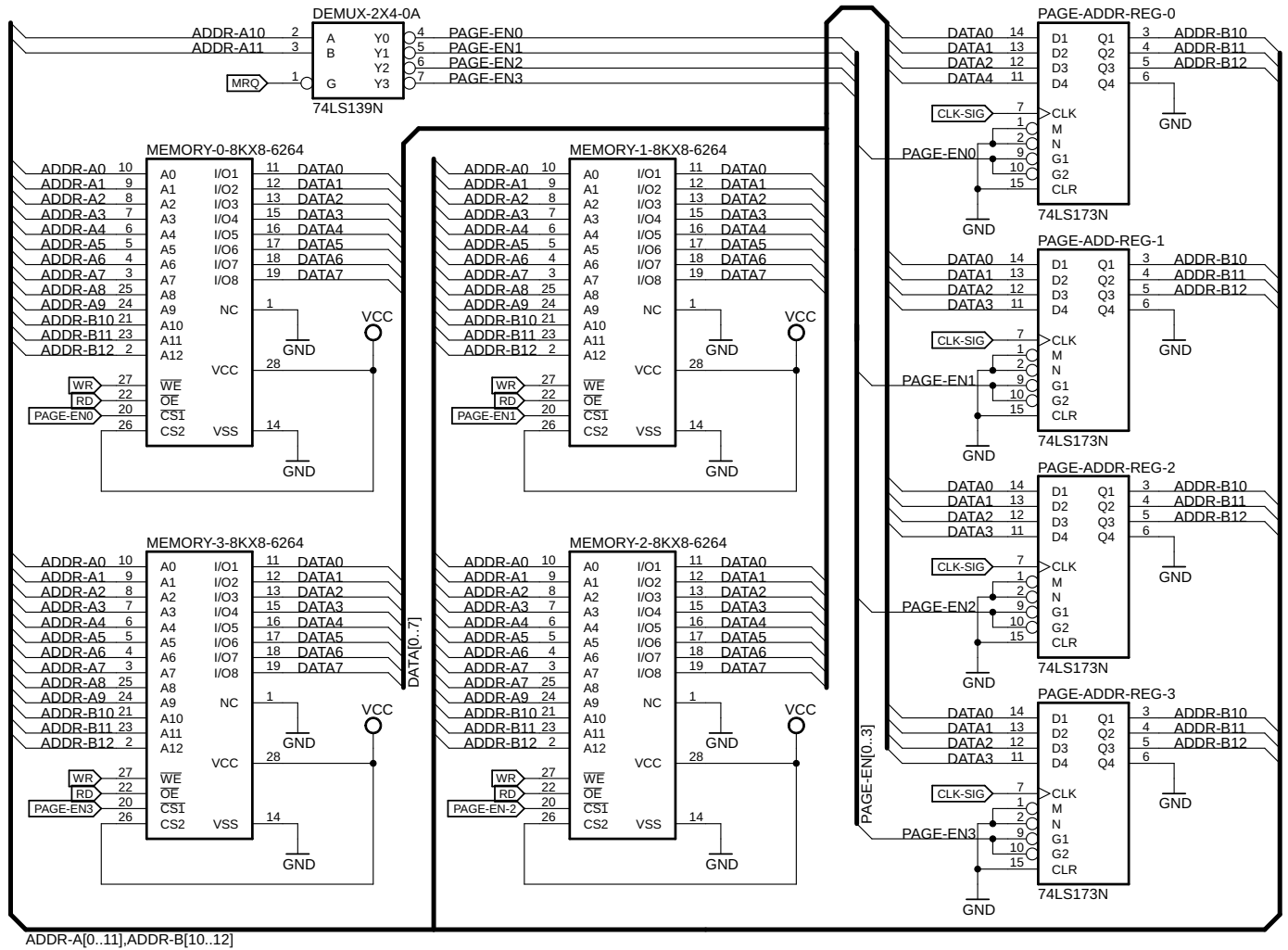


# SRAM (STATIC RANDOM ACCESS MEMORY) 4x8K



ADDR-A[0..11], ADDR-B[10..12]