Figure 7.13 shows a delay line circuit.

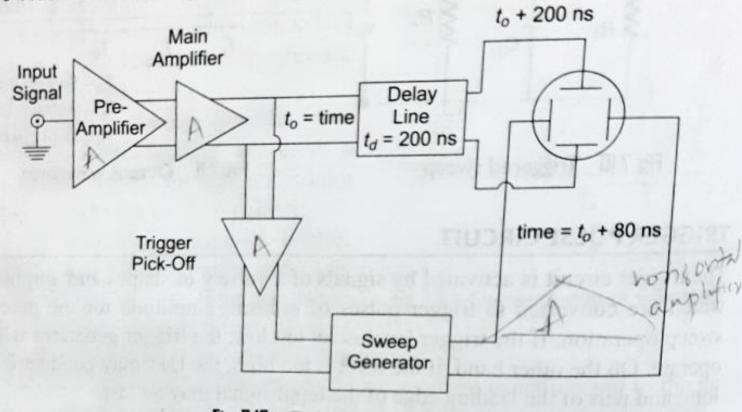


Fig. 7.13 Delay line circuit

Figure 7.14 indicates the amplitude of the signal wrt time and the relative position of the sweep generator output signal. The diagram shows that when

the delay line is not used, the initial part of the signal is lost and only part of the signal is displayed. To counteract this disadvantage the signal is not applied directly to the vertical plates but is passed through a delay line circuit, as shown in Fig. 7.13. This gives time for the sweep to start at the horizontal plates before the signal has reached the vertical plates. The trigger pulse is picked off at a time t_o after the signal has passed through the main amplifier. The sweep generator delivers the sweep to the horizontal amplifier and the sweep starts at the HDP at time $t_o + 80$ ns. Hence the sweep starts well in time, since the signal arrives at the VDP at time $t_o + 200$ ns.

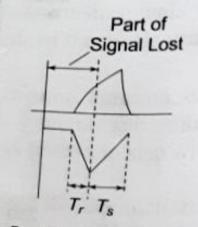


Fig. 7.14 Delay line waveform

7.28.1 Direct Probes (1:1)

The simplest types of probe (one can hardly call it a probe) is the test lead. Test leads are simply convenient lengths of wire for connecting the CRO input to the point of observation. At the CRO end, they usually terminate with lugs, banana tips or other tips to fit the input jacks of the scope, and at the other end have a crocodile clip or any other convenient means for connection to the electronic circuit.

Since a CRO has high input impedance and high sensitivity, the test leads should be shielded to avoid hum pickup, unless the scope is connected to low impedance high level circuits.

Although the input impedances of most CROs are relatively very high compared to the circuits where they are connected, it is often desirable to increase their impedance to avoid loading of the circuits or causing unstable effects.

The input capacitance of the scope, plus the stray capacitance of the test leads, may be just enough to cause a sensitive circuit to break into oscillation when

the CRO is connected. This effect can be prevented by an isolation probe made by placing a carbon resistor in series with the test lead, as shown in Fig. 7.44.

A slight reduction in the amplitude of the waveform and a slight change in the waveshape occurs with this probe. To avoid

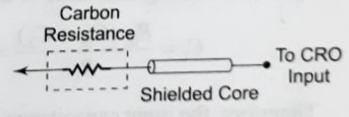


Fig. 7.44 Isolation probe

this possibility, a high impedance compensated probe, called a low capacitance probe or a 10:1 probe, is used.

7.28.2 Passive Voltage (High Z) Probe

Figure 7.45 (a) shows a 10: 1 probe. Figure 7.45 (b) shows the equivalent circuit. Referring to Fig. 7.45 (b). The capacitor is adjusted so that the elements of the bridge are balanced. Under conditions of balance we have

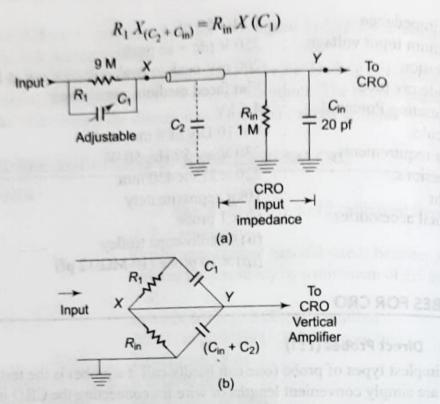


Fig. 7.45 (a) 10:1 Probe (b) Equivalent Circuit of 10:1 Probe

$$\frac{R_1}{\omega(C_2 + C_{in})} = \frac{R_{in}}{\omega C_1}$$

$$R_1 C_1 = R_{in} (C_2 + C_{in})$$

Therefore, X and Y are equipotential and the effect of the probe is equivalent to placing a potential divider consisting of R_1 and $R_{\rm in}$ across the input circuit. The attenuation of the signal is 10:1, i.e. $(R_1+R_{\rm in})/R_1=10:1$ over a wide frequency range. Therefore, it is called a compensated 10×1 probe. As far as dc voltage inputs are concerned, the coaxial capacitance equals 30 pf per foot. (Assuming a coaxial length of 3.5 ft, the total coaxial length capacitance is 105 pf). Substituting this value in the balance bridge equation, we have

$$C_1 = \frac{R_{\text{in}} (C_{\text{in}} + C_2)}{R_1} = \frac{1 \text{ M} (105 + 20) \text{ pf}}{9 \text{ M}} = 13.88 \text{ pf}$$

Therefore, the input capacitance of a CRO can range from 15–50 pf. C_1 should be adjusted from 13–47 pf. It must be adjusted to obtain optimum frequency response from the probe-CRO combination. The C_1 adjustment is done by connecting the probe tip to a square wave of 1 kHz and observing the CRT display. When the CRT display has optimum response, the C_1 value is deemed to be appropriate.

$$V_{\text{out}} = (0.1) V_{\text{in}} = \frac{V_{\text{in}} \times R_{\text{in}}}{R_1 + R_{\text{in}}}$$

7.28.3 Active Probes

Active probes are designed to provide an efficient method of coupling high frequency, fast rise time signals to the CRO input. Usually active probes have very high input impedance, with less attenuation than passive probes. Active devices may be diodes, FETs, BJTs, etc.

Active probes are more expensive and bulky than passive probes, but they are useful for small signal measurements, because their attenuation is less.

Active Probes Using FETs Figure 7.46 shows a basic circuit of an active probe using a FET.

The FET is used as the active element to amplify the input signal. Although the voltage gain of the FET follower circuit shown is unity, the follower circuit provides a power gain so that the input impedance can be increased. To be effective the FET must be mounted directly in the voltage probe tip, so that the capacitance of the interconnecting cable can be eliminated. This requires that the power for the FET be supplied from the oscilloscope to the FET in the probe tip. The FET voltage follower drives a coaxial cable, but instead of the cable connecting directly to the high input impedance of the oscilloscope, it is terminated in its characteristic impedance.

There is no signal attenuation between the FET Amplifier and the probe tip. The range of the signals that can be handled by the FET probe is limited to the dynamic range of the FET amplifier and is typically less than a few volts. To handle a larger dynamic range, external attenuators are added at the probe tip. Active probes have limited use because the FET probe effectively becomes an FET attenuator. Therefore, oscilloscopes are typically used with a 10 to 1 attenuator probe.

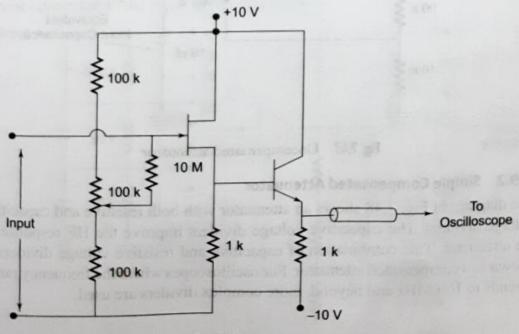


Fig. 7.46 FET probe