

## **Part I: ARM Memory Foundations**

!include chapters/part1-foundations/01-evolution-of-arm-memory.md !include chapters/part1-foundations/02-arm-memory-model.md

## **Part II: Cache Hierarchy in ARM SoCs**

!include chapters/part2-caches/03-l1-l2-microarchitecture.md !include chapters/part2-caches/04-shared-l3-system-cache.md !include chapters/part2-caches/05-cache-coherency.md

## **Part III: DRAM, Interconnects & Latency**

!include chapters/part3-dram-interconnects/06-arm-interconnects.md !include chapters/part3-dram-interconnects/07-dram-mobile-socs.md

## **Part IV: OS & Software Stack**

!include chapters/part4-os-stack/08-linux-memory-arm.md !include chapters/part4-os-stack/09-android-memory.md

## **Part V: Power-Aware Memory Systems**

!include chapters/part5-power-aware/10-memory-power-coupling.md !include chapters/part5-power-aware/11-scheduling-memory-energy.md

## **Part VI: Measurement & Tooling**

!include chapters/part6-measurement/12-measuring-memory.md !include chapters/part6-measurement/13-perfetto-tracing.md

## **Part VII: Practical Guidance**

!include chapters/part7-practical/14-arm-friendly-software.md !include chapters/part7-practical/15-kernel-bsp-pitfalls.md