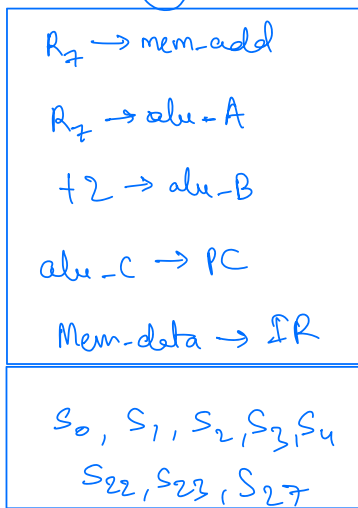
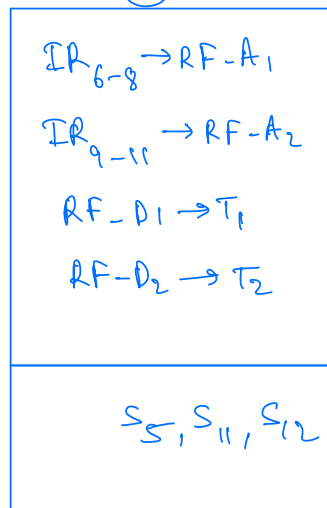


CC = (condition), write (C, W)

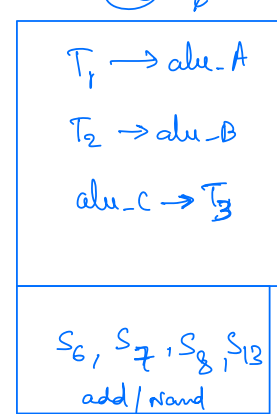
(S₁)



(S₂)

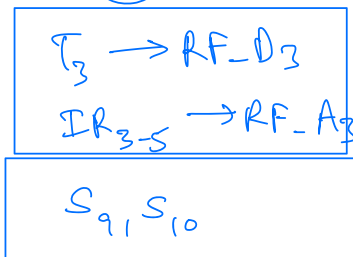


(S₃)

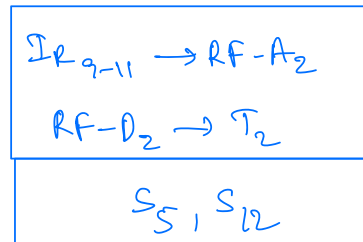


opcode dependent.

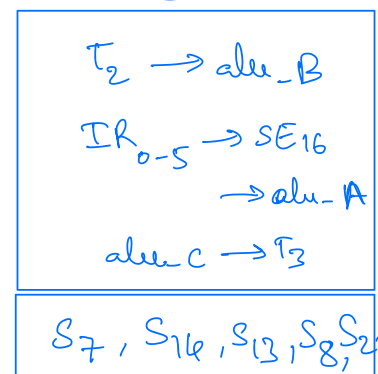
(S₄) (write in R_C)



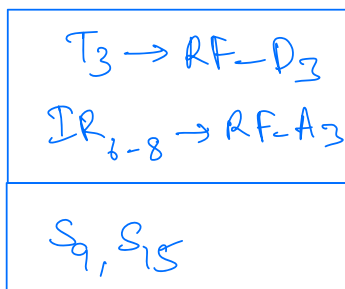
(S₅)



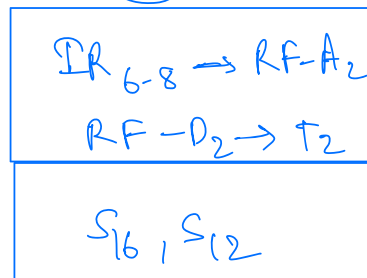
(S₆)



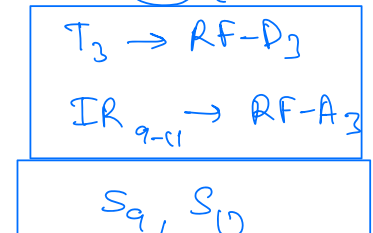
(S₇) (write in R_B)



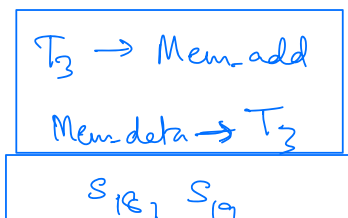
(S₈)



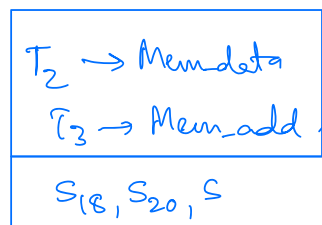
(S₉) (write in R_A)



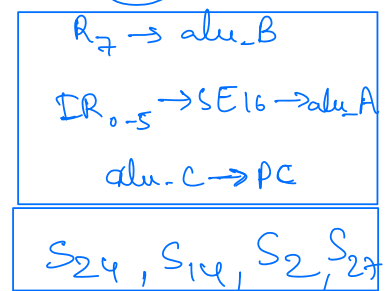
(S₁₀)

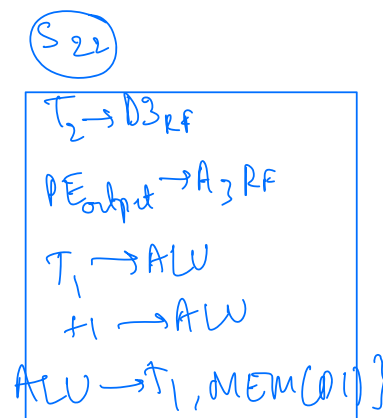
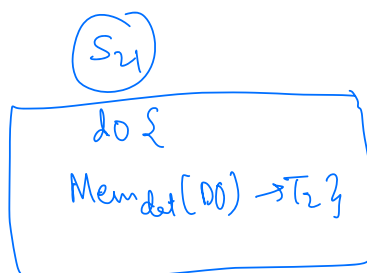
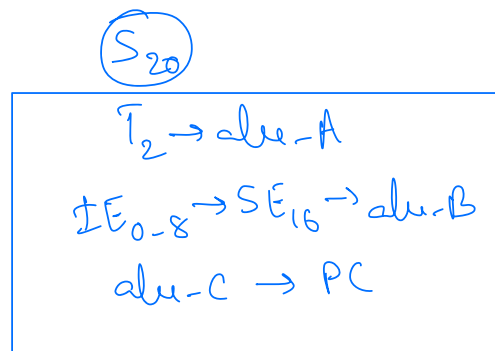
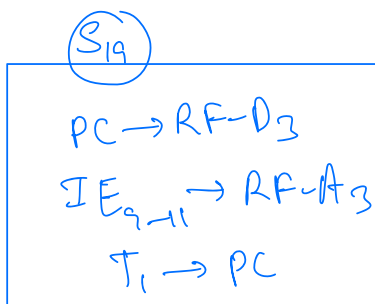
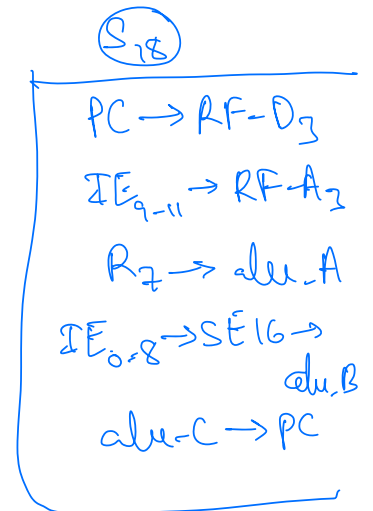
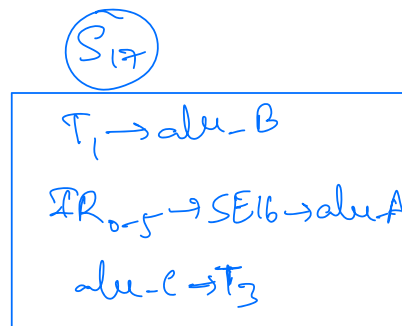
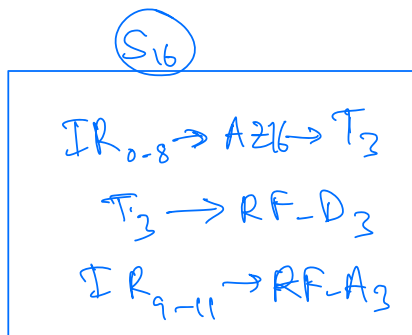
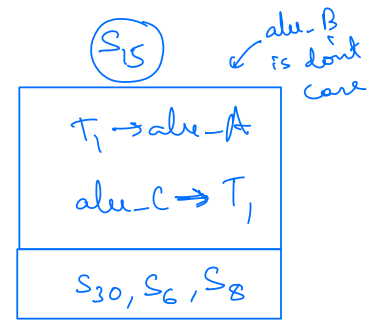
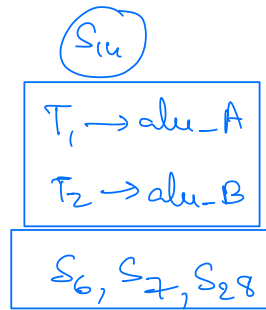
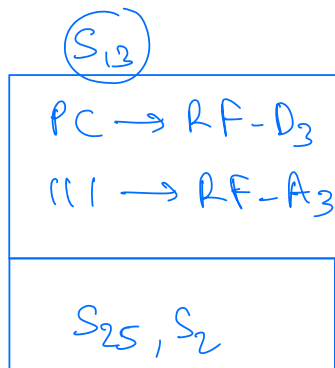


(S₁₁)



(S₁₂)





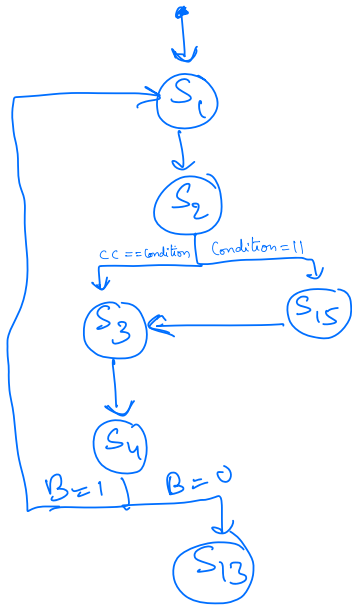
S₂₃

$PE_{\text{output}} \rightarrow A_2 \text{ pf}$
 $T_1 \rightarrow \text{Mem}(A)$

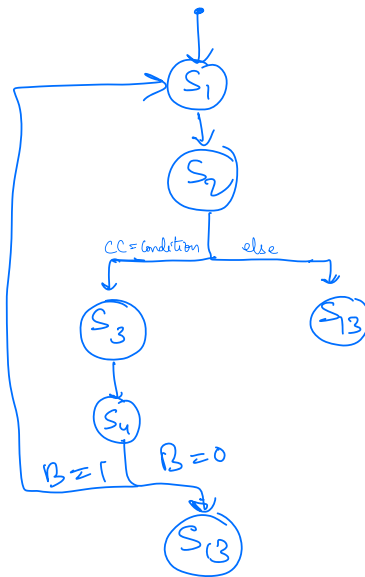
S₂₄

$T_1 \rightarrow AW$
 $+1 \rightarrow AW$
 $AW \rightarrow T$

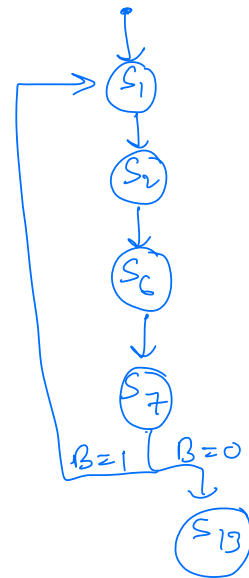
opcode = 00-01



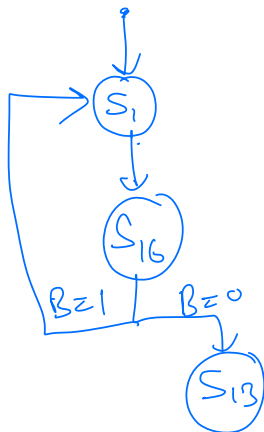
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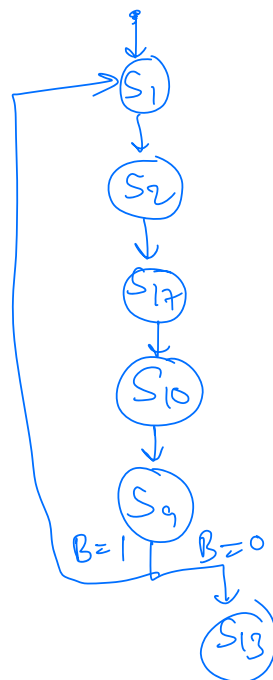
opcode = 00-00 (API)



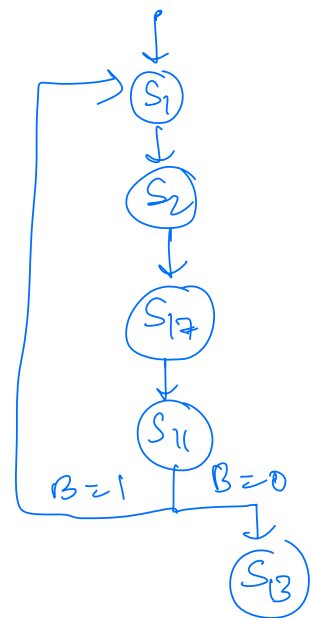
LHI



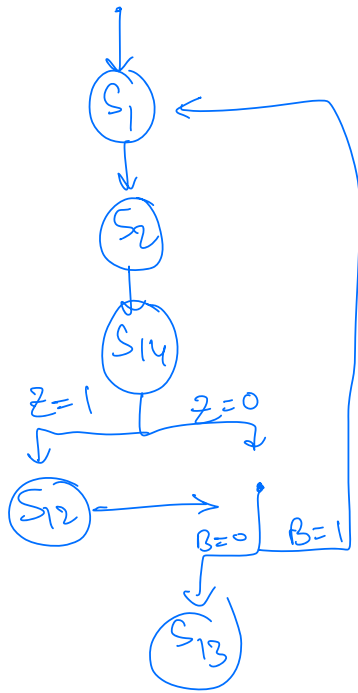
LW(01-01)



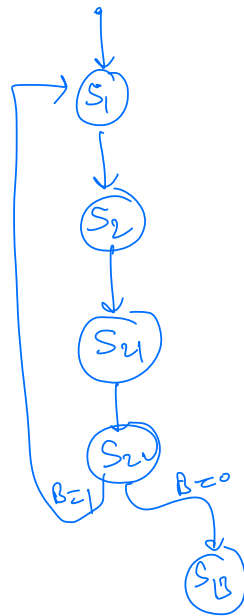
SW(01-11)



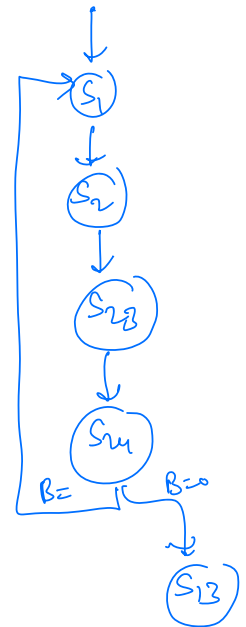
BEQ



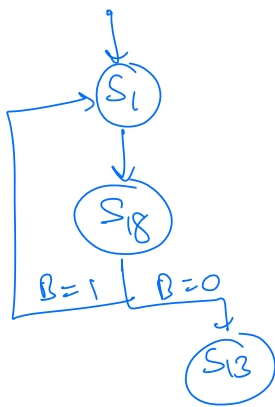
LM



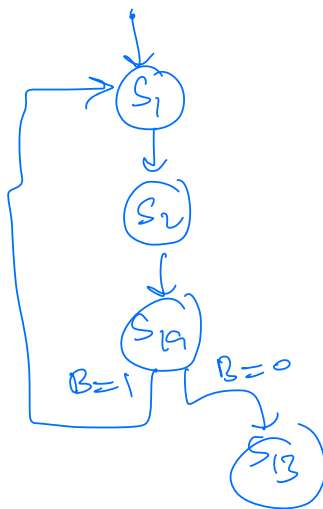
SM



JAL



JLR



JRI

