
| Contents | Pg.no |
|--|--------------|
| 1. Introduction | 2 |
| 2. Circuit Diagram..... | 3 |
| 3. Small Signal Circuit Diagram..... | 4 |
| 4. Computing DC Operating Point..... | 5 |
| 5. Transient time domain simulation using HiSpice - Matlab Interface | 7 |
| 6. Input Output Response..... | 8 |

Introduction

The objective of this assignment is to get deeper understanding about the process in computing the DC point of a non-linear circuit and for studying the steady state and transient analysis of non- linear systems.

MATLAB Hi-Spice interface is used for computing the DC points and the response of the system. The same has been discussed in depth in the document.

Circuit Diagram

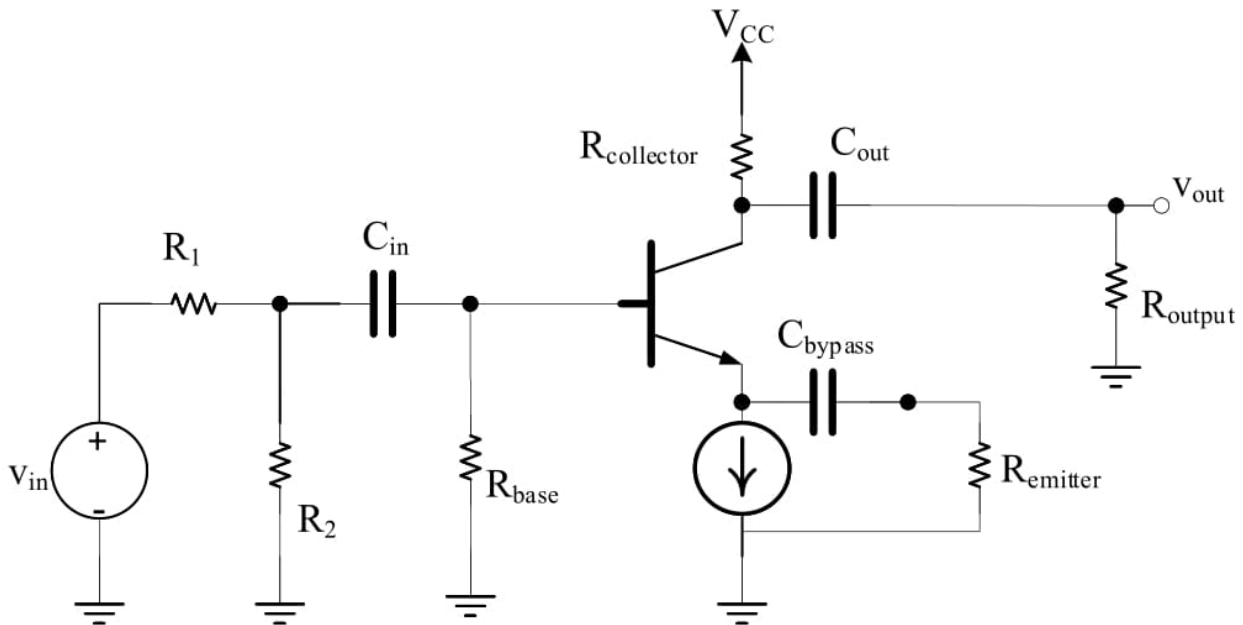


Fig.1 Common Emitter Amplifier

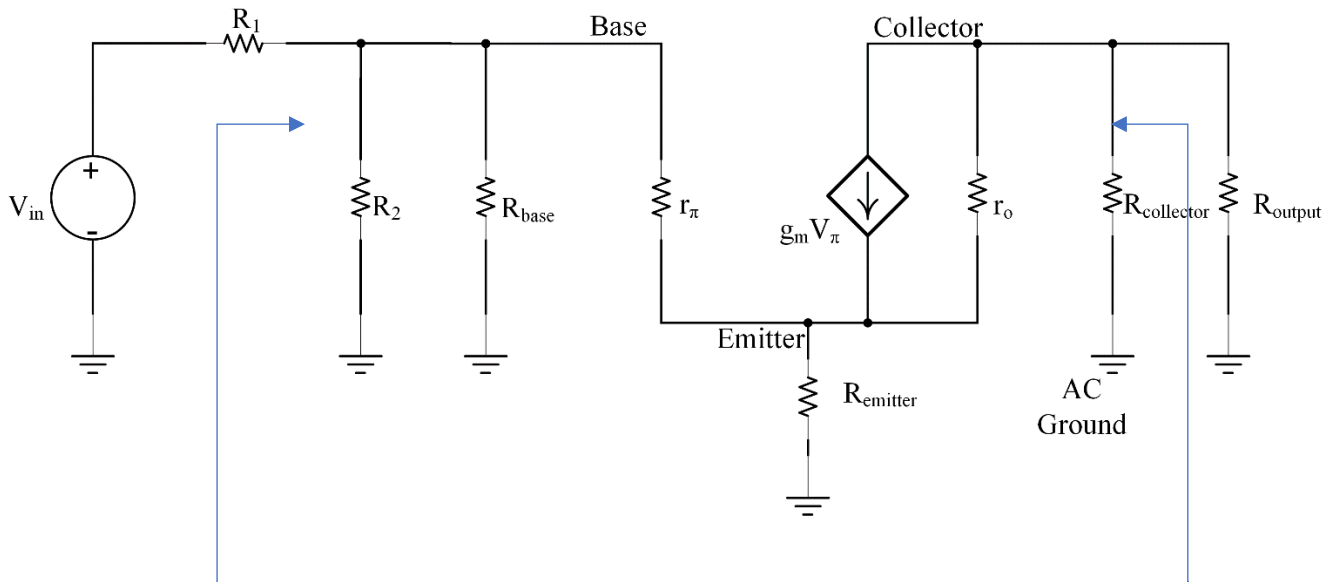
The single stage common emitter amplifier circuit shown above uses “Voltage Divider Biasing”. This type of biasing uses two resistors as a potential divider network across the supply with their centre point supplying the required base bias voltage to the transistor. Voltage divider biasing is commonly used in the design of BJT amplifier circuits.

This method of biasing the transistor greatly reduces the effects of varying Beta, (β) by holding the Base bias at a constant steady voltage level allowing for best stability. The quiescent Base voltage (V_b) is determined by the potential divider network formed by the two resistors, R_1 , R_2 , R_{base} as shown in the figure.

Assignment 2 : Topics in Electronics

The capacitors C_{in} and C_{out} are coupling capacitors and are used to couple the voltages from a previous stage to this stage and also to couple the output of this stage to the next stage. It also blocks all the DC components. C_{bypass} is the bypass capacitor which allows all the current to flow through thus reducing the voltage drop at the emitter terminal.

Small Signal Circuit Diagram



$$R_{in} = R_2 \parallel R_{base} \parallel r_\pi$$

$$V_{in} = \frac{V_{sig} R_{in}}{R_{in} + R_{sig}}$$

$$R_{in} = R_c \parallel r_o$$

$$v_o = -g_m v_{\pi} (R_c \parallel R_L \parallel r_o)$$

Fig.2 Small Signal Equivalent Circuit

AC Analysis of any network is done by following steps

- Identifying all the DC Voltage sources and replacing it by short circuit.
- Identifying all the DC Current sources and replacing it by open circuit.
- All the capacitors are replaced by short circuit.

$$v_{in}(t) = 0.01 \cos(2 \times \pi \times 5 \times 10^6 \times t)$$

$$r_{\pi} = \frac{\beta}{g_m}$$

$$v_{\pi} = \frac{v_i(R_2 \parallel R_B \parallel r_{\pi})}{R_1 + (R_2 \parallel R_B \parallel r_{\pi})}$$

Assignment 2 : Topics in Electronics

$$v_o = -g_m v_{\pi} (R_C \parallel R_L \parallel r_o)$$

$$v_{\pi} = v_i(0.434)$$

$$A_v = v_o / v_i$$

$$A_v = 12.56$$

Assignment 2 : Topics in Electronics

Computing DC Operating Point

The DC operating point x_{DC} can be manually computed by solving the equation

$$Gx + f(x) = b_{DC}$$

It uses the Newton Raphson method for computing the $\phi(x^{(i)})$ at the instant i where initially $i=0$;

A threshold $\epsilon = 10^{-14}$ is set and the norm of the error $\phi(x^{(i)})$ is calculated using the equation

$$\phi(x^{(i)}) = Gx^{(i)} + f(x^{(i)}) - b_{DC}$$

If the norm of the error $\phi(x^{(i)})$ is smaller than the ϵ then the iteration is stopped and the new x obtained is the initial point at $t=0$.

If the error is greater than the threshold, Jacobian of the matrix at $x=x^{(i)}$ and is called as the $J(x^{(i)})$

$$J(x^{(i)}) = G + \partial f / \partial x$$

Where $\partial f / \partial x$ is the partial derivatives. The initial guess is updated using the following equation

$$x^{(i+1)} = x^{(i)} - J^{-1} \phi(x^{(i)})$$

The process is again repeated by the x in the equation until the norm of the error is less than the threshold.

A program involving a function to compute DC point has been included. The function is given the name 'compute_DC_point'.

```
function [xDC] = compute_DC_point(x_initial_guess)
```

Assignment 2 : Topics in Electronics

Transient time domain simulation using HiSpice - Matlab Interface

In the computer-aided analysis of nonlinear circuits with periodic inputs and a stable periodic response the transient-state periodic response is found for a given initial state by using the TR method of the system equations until the response becomes periodic and steady.

The trapezoidal rule is the continuation of the NR method by getting the initial point x_{DC} in the system of equations.

$$\left(C + \frac{h_n}{2}G\right)x_n + \frac{h_n}{2}f(x_n) = \left(C - \frac{h_n}{2}G\right)x_{n-1} - \frac{h_n}{2}f(x_{n-1}) + \frac{h_n}{2}(b(t_{n-1}) + b(t_n))$$

Here the h is the step size which is equal to the difference between the two time-points of the curve. TR method is used for approximation of the output response of a system for a given input.

TR method is implemented using MATLAB by getting the initial point x_{DC} using the function `compute_DC_point` and then the step size h calculated using the formula

$$h = T / 50$$

The input for the system is clocked at 5MHz.

$$T = 1/f \text{ sec}$$

The resistors and the capacitor stamps are computed to be used in the program for computation.

The nonlinear function vector $f(x)$, source vector b and the Jacobin of the vector $J(f(x))$ is computed for time $t=0$;

To calculate the new value of 'x', the TR iterations are done using the equation

and the norm of the vector is compared with a threshold as earlier to continue or stop the iteration process.

$$J(x^{(i+1)}) = C + (H/2) \times J(x^{(i)})$$
$$x^{(i+1)} = x^{(i)} - J(x^{(i+1)}) \times \phi(x^{(i)})$$

Assignment 2 : Topics in Electronics

A function has been implemented using MATLAB to find the transient response of a given nonlinear system.

```
function x_transient =  
compute_TR_time_response(x_0, ...time_points, ...output  
_node_name)
```

Input Output Response

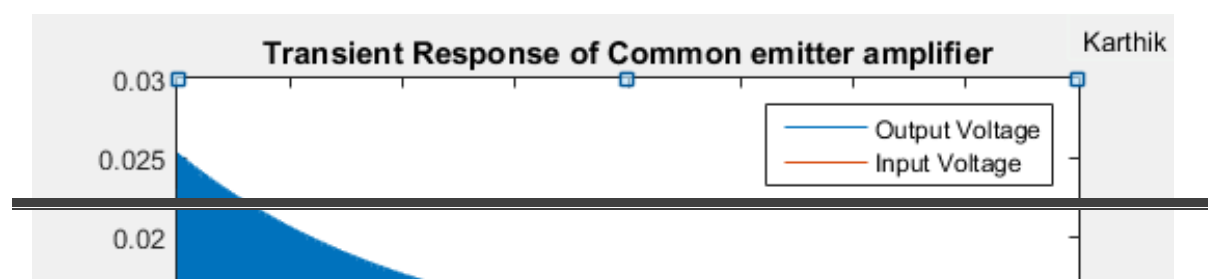


Fig.3 Transient IO Response

The amplifier is designed to work with a voltage gain of 12.6. Initially the system is in the transient state. After a considerable amount of time of around

Assignment 2 : Topics in Electronics

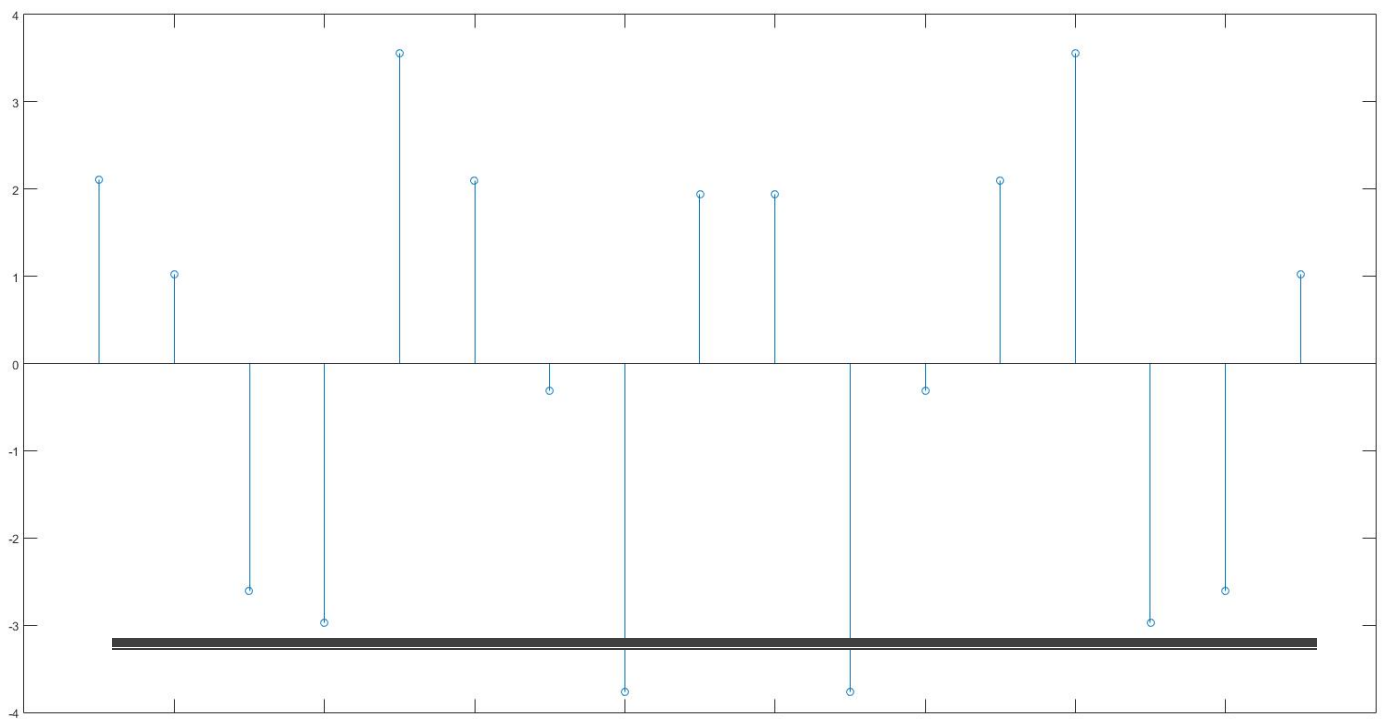


Fig.4 FFT of the Output

The number of cycles 'T' depends on the step size H and the as the step size increases the number of iterations to reduce the error increases but the iterations to calculate the voltage at specific points decreases. Smaller the step size accurate the results will be. Taking an example at $T = 1/4 \times 10^5$ takes 400,000 cycles to compute.