



uOttawa

Master of Engineering
Electrical and Computer Engineering

Assignment 3
ELG7132D

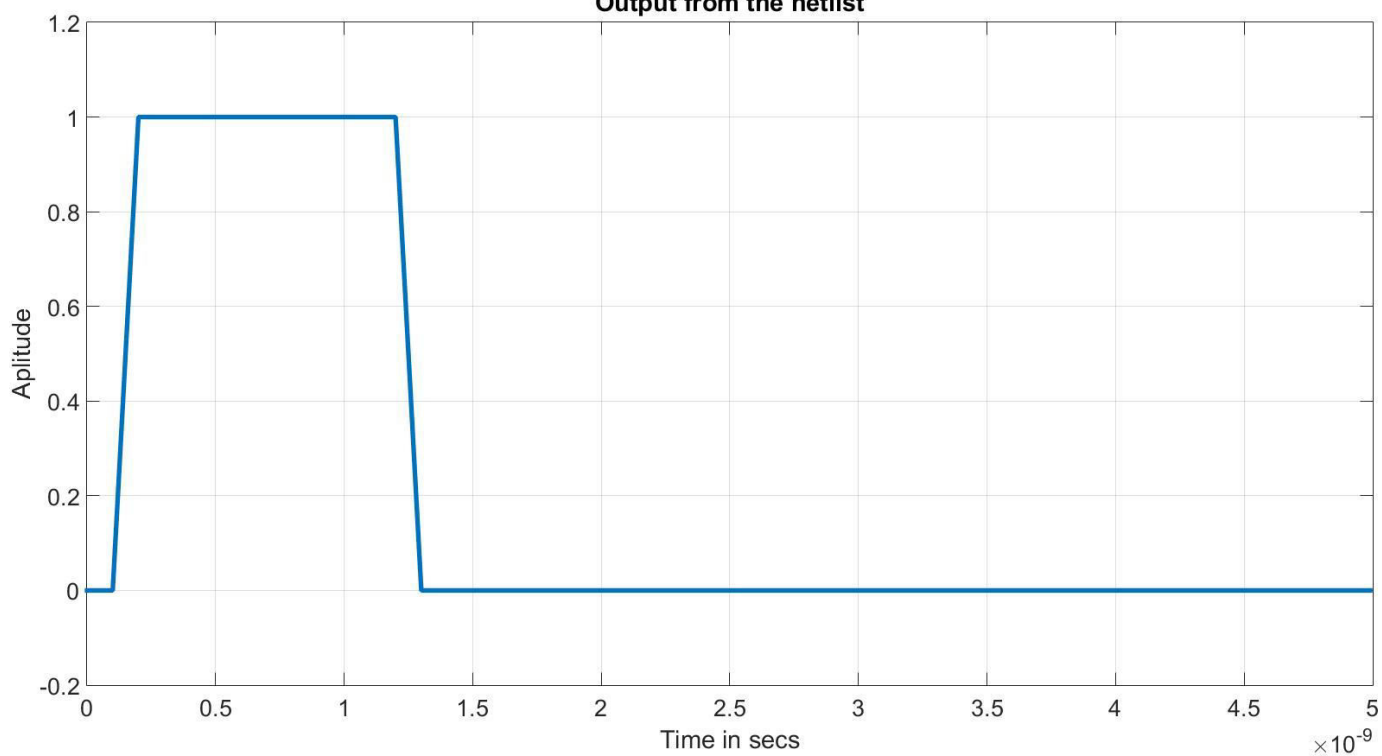
Topics in Electronics I: Simulation of Radio Frequency Circuits

Submitted by

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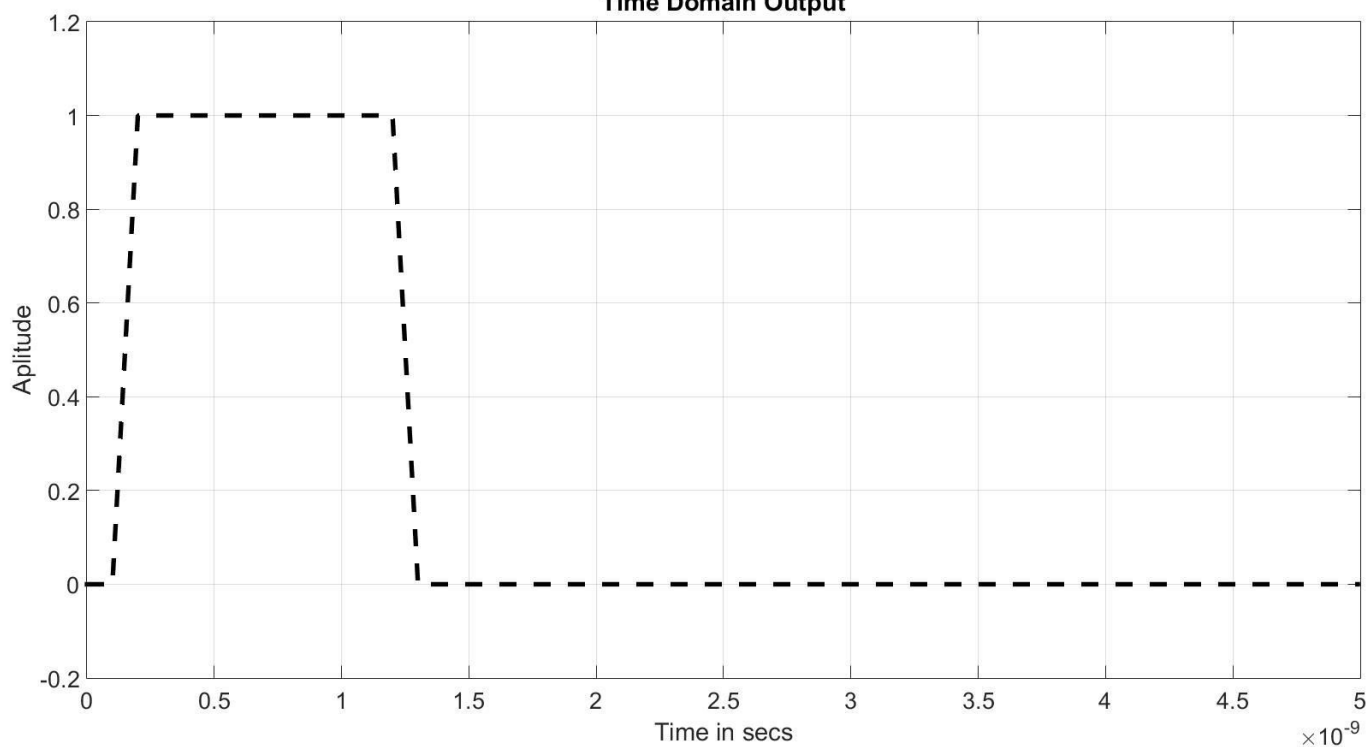
Trace from the Netlist

Output from the netlist



Trace from the Fourier Coefficients

Time Domain Output



Superimposed Trace of the Source Coefficients

Super Imposed Trace

