Vivekanand Education Society's Institute of Technology
Department of Computer Engineering

Academic Year 2018-19

<u>Assignment No. 1</u>

Subject:- COA

Class:- D7-B SE CMPN

Lab Outcomes : LO1, LO2, LO3

Q.1 Explain the Von Neumann architecture and explain the stored program concept.

Q.2. Explain PC, IR, MAR, MBR

Q.3 Differentiate computer organization and computer architecture.

Q.4 Draw flowchart of Booth's Algorithm and solve following

1) M = 9   , Q = - 3
2) M = -5   , Q = -4

Q.5 Explain restoring division method algorithm.Solve for 6/5
.
Q.6 Explain IEEE 754 formats for floating point representation. Represent $(35.64)_{10}$ in single precision format

Q.7 Explain different addressing modes of the processor

Q.8 What is instruction pipelining ? Explain six stage instruction pipelining with timing diagram.

Q.9 Explain different types of hazards in instruction pipelining.

Q.10. Explain hardwired control unit.