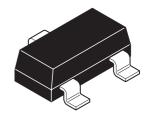


# FMMT459 500V Silicon NPN high voltage switching transistor

# **Summary**

 $\begin{array}{l} V_{(BR)CEV} > 500V \\ V_{(BR)ECV} > 6V \\ I_{c(cont)} = 150 \text{ mA} \\ V_{ce(sat)} = 70 \text{ mV } @ 50 \text{ mA} \end{array}$ 



# **Description**

This new high voltage transistor provides users with very efficient performance, combining low  $V_{CE(SAT)}$  high Hfe to give extremely low on state losses at 500V operation, making it ideal for use in high efficiency Telecom and protected line switching applications.

### **Features**

- 6V reverse blocking capability
- Low saturation voltage 90mV @ 50mA
- Hfe > 50 @ 30 mA
- I<sub>C</sub>=150mA continuous
- SOT23 package with Ptot 625mW
- Specification can be supplied in other package outlines

# **Applications**

- Electronic test equipment
- Offline switching circuits
- Piezo actuators
- RCD circuits

# В

# Pin out - top view

# 

# **Ordering information**

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
FMMT459TA	7	8	3,000
FMMT459TC	13	8	10,000

# **Device marking**

459

# **Absolute maximum ratings**

Parameter	Symbol	Limit	Unit	
Collector-base voltage	V <sub>CBO</sub>	500	V	
Collector-emitter voltage	V <sub>CEV</sub>	500	V	
Collector-emitter voltage	V <sub>CEO</sub>	450	V	
Emitter-base voltage	V <sub>EBO</sub>	6	V	
Emitter-collector voltage	V <sub>ECV</sub>	6	V	
Peak pulse current	I <sub>CM</sub>	0.5	А	
Continuous collector current*	I <sub>C</sub>	0.15	А	
Base current	I <sub>B</sub>	0.2	А	
Power dissipation @ T <sub>A</sub> =25°C* Linear derating factor	P <sub>D</sub>	625 5	mW mW/°C	
Power dissipation @ T <sub>A</sub> =25°C <sup>†</sup> Linear derating factor	P <sub>D</sub>	806 6.4	mW mW/°C	
Operating and storage temperature range	T <sub>j</sub> :T <sub>stg</sub>	-55 to +150	°C	

# Thermal resistance

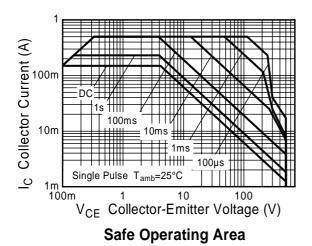
Parameter	Symbol	Value	Unit
Junction to ambient*	$R_{\ThetaJA}$	200	°C/W
Junction to ambient <sup>†</sup>	$R_{\ThetaJA}$	155	°C/W

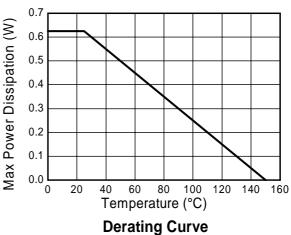
NOTES:

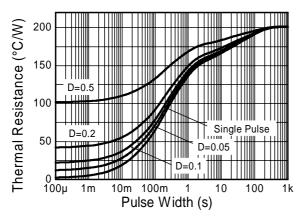
\* For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of 1oz copper, in still air conditions

<sup>†</sup> as above measured at t<5secs.

# Thermal characteristics







**Transient Thermal Impedance** 

# Electrical characteristics (at T<sub>amb</sub> = 25°C unless otherwise stated)

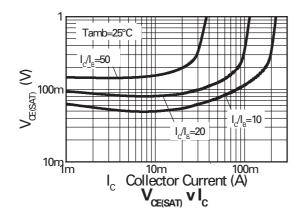
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	BV <sub>CBO</sub>	500	700		V	I <sub>C</sub> = 100μA	
Collector-emitter breakdown voltage	BV <sub>CEV</sub>	500	700		V	$I_C = 10\mu A,$ 0.3V > $V_{BE}$ > -1V	
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	450	500		V	I <sub>C</sub> = 10mA*	
Emitter-base breakdown voltage	BV <sub>EBO</sub>	6	8.1		V	I <sub>E</sub> = 100μA	
Emitter-base breakdown voltage (reverse blocking)	BV <sub>ECV</sub>	6	8.1		V	$I_C = 1\mu A$ , $0.3V > V_{BC} > -6V$	
Collector-emitter cut-off current	I <sub>CES</sub>			100	nA	V <sub>CE</sub> =450V	
Collector-base cut-off current	I <sub>CBO</sub>			100	nA	V <sub>CB</sub> =450V	
Emitter-base cut-off current	I <sub>EBO</sub>			100	nA	V <sub>EB</sub> =5V	
Static forward current transfer ratio	H <sub>FE</sub>	50	120 70			$I_C = 30 \text{mA}, V_{CE} = 10 \text{V}$ $I_C = 50 \text{mA}^*, V_{CE} = 10 \text{V}$	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>		60 70	75 90	mV mV	$I_{C} = 20\text{mA}, I_{B} = 2\text{mA}^{*}$ $I_{C} = 50\text{mA}, I_{B} = 6\text{mA}^{*}$	
Base-emitter saturation voltage	V <sub>BE(sat)</sub>		0.76	0.9	V	$I_{C} = 50 \text{mA}, I_{B} = 5 \text{mA}^{*}$	
Base-emitter turn-on voltage	V <sub>BE(on)</sub>		0.71	0.9	V	$I_C = 50 \text{mA}, V_{CE} = 10 \text{V}^*$	
Transition frequency	f <sub>T</sub>	50			MHz	I <sub>C</sub> = 10mA, V <sub>CE</sub> = 20V f = 20MHZ	
Output capacitance	C <sub>obo</sub>			5	РF	V <sub>CB</sub> = 20V, f = 1MHZ	
Turn-on time	t <sub>(ON)</sub>		113		ns	$I_C = 50\text{mA}, V_C = 100V$ $I_{B1} = 5\text{mA}, I_{B2} = 10\text{mA}$	
Turn-off time	t <sub>(OFF)</sub>		3450		ns	I <sub>C</sub> = 50mA, V <sub>C</sub> = 100V I <sub>B1</sub> = 5mA, I <sub>B2</sub> = 10mA	

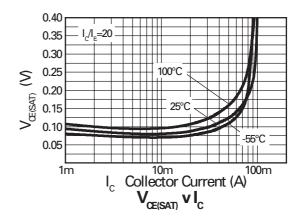
### NOTES

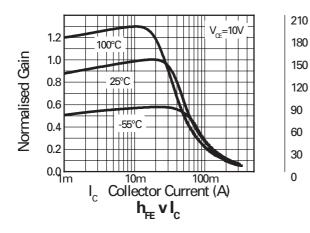
**Note**: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to voltage spacing between Terminals.

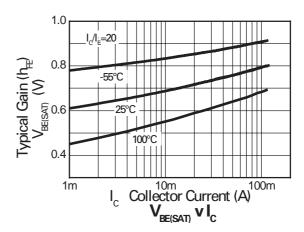
<sup>\*</sup> Measured under pulsed conditions. Pulse width =  $300\mu s$ ; duty cycle < 2%

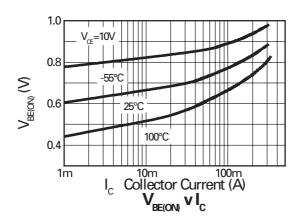
## **Electrical characteristics**



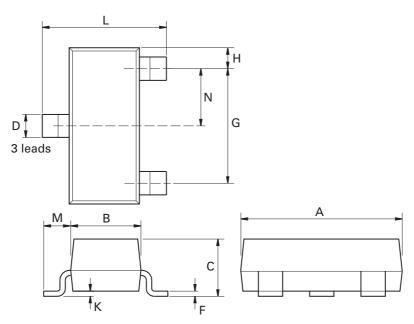








# Packaging details - SOT23



# **Package dimensions**

Dimensions in inches are control dimensions, dimensions in millimeters are approximate.

Dim.	Millimeters		Inches		Dim.	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Max.	Max.
Α	2.67	3.05	0.105	0.120	Н	0.33	0.51	0.013	0.020
В	1.20	1.40	0.047	0.055	K	0.01	0.10	0.0004	0.004
С	-	1.10	-	0.043	L	2.10	2.50	0.083	0.0985
D	0.37	0.53	0.015	0.021	М	0.45	0.64	0.018	0.025
F	0.085	0.15	0.0034	0.0059	N	0.95 Nom.		0.0375 Nom.	
G	1.90 Nom.		0.075 Nom.		-	-	-	-	-

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