# **General Purpose Transistors**

## **PNP Silicon**

#### **Features**

• These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CEO</sub>	-45	V
Collector - Base Voltage	V <sub>CBO</sub>	-50	V
Emitter – Base Voltage	V <sub>EBO</sub>	-5.0	V
Collector Current – Continuous	I <sub>C</sub>	-500	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

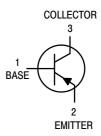
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- 1.  $FR-5 = 1.0 \times 0.75 \times 0.062$  in.
- 2. Alumina = 0.4 x 0.3 x 0.024 in 99.5% alumina.



## ON Semiconductor®

http://onsemi.com





SOT-23 CASE 318 STYLE 6

#### MARKING DIAGRAM



5xx = Device Codexx = A1, B1, or C

M = Date Code\*

= Pb-Free Package
 (Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may

vary depending upon manufacturing location.

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted.)

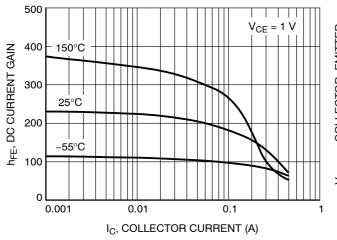
Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector - Emitter Breakdown Voltage (I <sub>C</sub> = -10 mA)		V <sub>(BR)CEO</sub>	-45	-	-	V
Collector – Emitter Breakdown Voltage $(V_{EB} = 0, I_C = -10 \mu A)$		V <sub>(BR)CES</sub>	-50	-	-	V
Emitter – Base Breakdown Voltage $(I_E = -1.0 \mu A)$		V <sub>(BR)EBO</sub>	-5.0	-	-	V
Collector Cutoff Current $(V_{CB} = -20 \text{ V})$ $(V_{CB} = -20 \text{ V}, T_J = 150^{\circ}\text{C})$		I <sub>CBO</sub>	_ _	_ _	-100 -5.0	nA μA
ON CHARACTERISTICS						
DC Current Gain $(I_C = -100 \text{ mA}, V_{CE} = -1.0 \text{ V})$ $(I_C = -500 \text{ mA}, V_{CE} = -1.0 \text{ V})$	BC807-16 BC807-25 BC807-40	h <sub>FE</sub>	100 160 250 40	- - -	250 400 600	-
Collector – Emitter Saturation Voltage (I <sub>C</sub> = –500 mA, I <sub>B</sub> = –50 mA)		V <sub>CE(sat)</sub>	_	_	-0.7	V
Base – Emitter On Voltage (I <sub>C</sub> = –500 mA, I <sub>B</sub> = –1.0 V)		V <sub>BE(on)</sub>	-	=	-1.2	V
SMALL-SIGNAL CHARACTERISTICS						
Current – Gain – Bandwidth Product (I <sub>C</sub> = –10 mA, V <sub>CE</sub> = –5.0 Vdc, f = 100 MHz)		f <sub>T</sub>	100	_	_	MHz
Output Capacitance (V <sub>CB</sub> = -10 V, f = 1.0 MHz)		C <sub>obo</sub>	-	10	-	pF

#### **ORDERING INFORMATION**

Device	Specific Marking	Package	Shipping <sup>†</sup>
BC807-16LT1G	544	SOT-23 (Pb-Free)	3000/Tape & Reel
BC807-16LT3G	5A1	SOT-23 (Pb-Free)	10,000/Tape & Reel
BC807-25LT1G	5D1	SOT-23 (Pb-Free)	3000/Tape & Reel
BC807-25LT3G	5B1	SOT-23 (Pb-Free)	10,000/Tape & Reel
BC807-40LT1G	5C	SOT-23 (Pb-Free)	3000/Tape & Reel
BC807-40LT3G	50	SOT-23 (Pb-Free)	10,000/Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### **TYPICAL CHARACTERISTICS - BC807-16LT1**



I<sub>C</sub>, COLLECTOR CURRENT (A)

Figure 1. DC Current Gain vs. Collector

Current

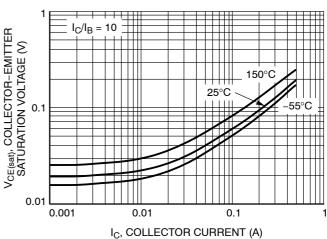


Figure 2. Collector Emitter Saturation Voltage vs. Collector Current

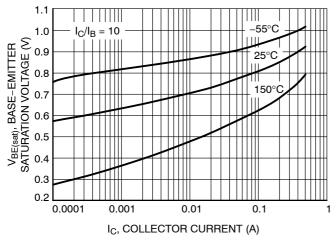


Figure 3. Base Emitter Saturation Voltage vs.
Collector Current

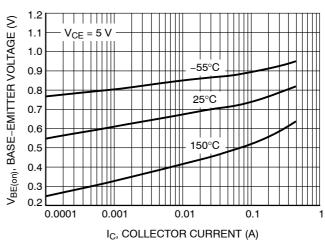


Figure 4. Base Emitter Voltage vs. Collector Current

## **TYPICAL CHARACTERISTICS - BC807-16LT1**

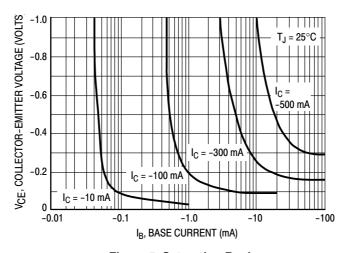


Figure 5. Saturation Region

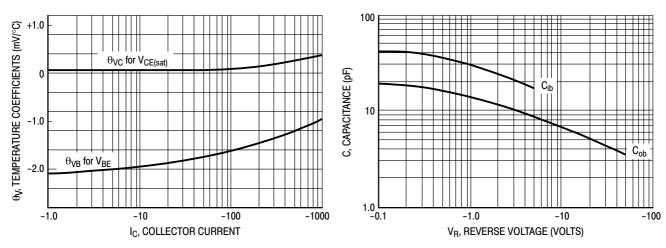


Figure 6. Temperature Coefficients

Figure 7. Capacitances

#### TYPICAL CHARACTERISTICS - BC807-25LT1

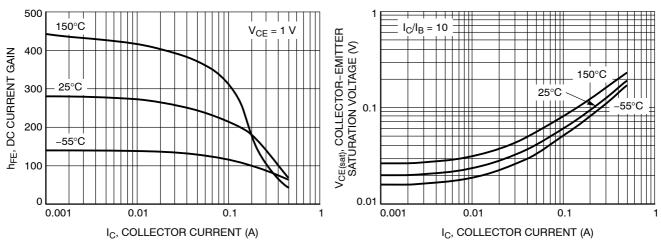


Figure 8. DC Current Gain vs. Collector Current

Figure 9. Collector Emitter Saturation Voltage vs. Collector Current

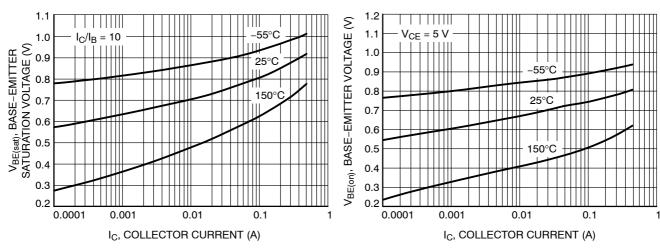


Figure 10. Base Emitter Saturation Voltage vs.
Collector Current

Figure 11. Base Emitter Voltage vs. Collector
Current

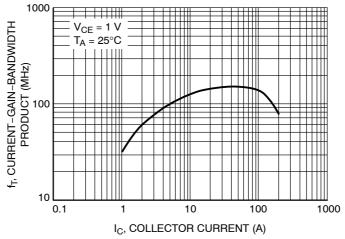


Figure 12. Current Gain Bandwidth Product vs. Collector Current

## **TYPICAL CHARACTERISTICS - BC807-25LT1**

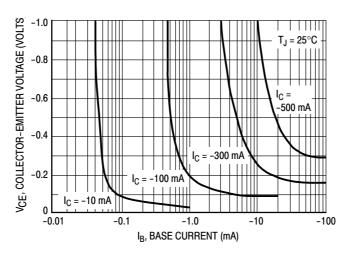


Figure 13. Saturation Region

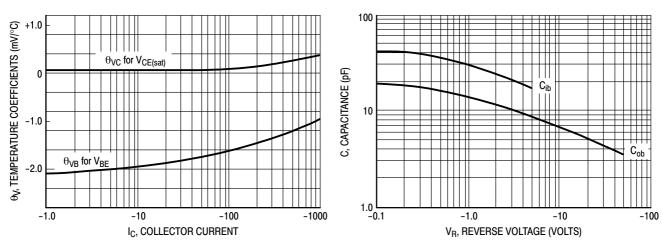


Figure 14. Temperature Coefficients

Figure 15. Capacitances

#### TYPICAL CHARACTERISTICS - BC807-40LT1

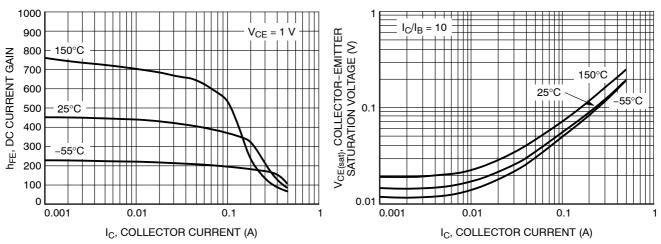


Figure 16. DC Current Gain vs. Collector Current

Figure 17. Collector Emitter Saturation Voltage vs. Collector Current

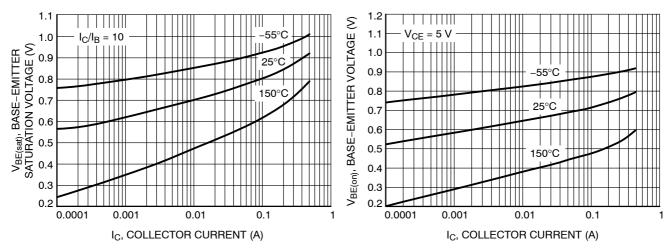


Figure 18. Base Emitter Saturation Voltage vs.
Collector Current

Figure 19. Base Emitter Voltage vs. Collector
Current

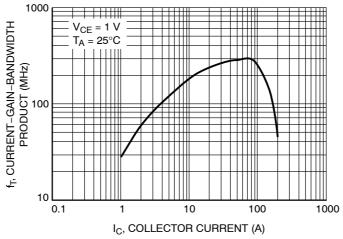


Figure 20. Current Gain Bandwidth Product vs. Collector Current

## **TYPICAL CHARACTERISTICS - BC807-40LT1**

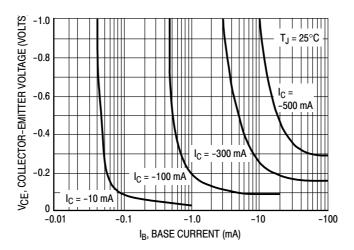


Figure 21. Saturation Region

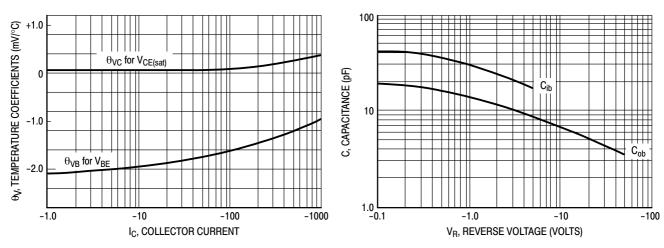


Figure 22. Temperature Coefficients

Figure 23. Capacitances

## TYPICAL CHARACTERISTICS - BC807-16LT1, BC807-25LT1, BC807-40LT1

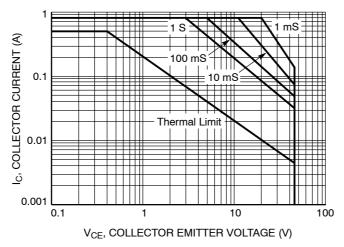
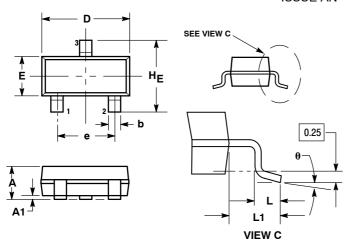


Figure 24. Safe Operating Area

#### PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AN



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
   V14 5M 1982
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 4. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

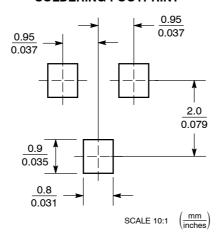
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104

STYLE 6:

PIN 1. BASE

- 2. EMITTER
- 3. COLLECTOR

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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