



PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

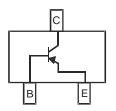
- **Epitaxial Planar Die Construction**
- Complementary NPN Type Available (MMBT2222A)
- Ideal for Low Power Amplification and Switching
- Lead, Halogen and Antimony Free, RoHS Compliant (Note 2)
- "Green" Device (Note 3)
- Qualified to AEC-Q 101 Standards for High Reliability

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound, Note 3. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating) Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)







Device Schematic

Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-60	V
Collector-Emitter Voltage	V _{CEO}	-60	V
Emitter-Base Voltage	V _{EBO}	-5.0	V
Collector Current - Continuous (Note 1)	Ic	-600	mA
Peak Collector Current	I _{CM}	-800	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	P _D	300	mW
Thermal Resistance, Junction to Ambient (Note 1)	$R_{ heta JA}$	417	°C/W
Operating and Storage and Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

- 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- No purposefully added lead. Halogen and Antimony Free
 Product manufactured with Data Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.



Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 4)								
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-60	_	V	$I_C = -10\mu A, I_E = 0$			
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-60		V	$I_C = -10 \text{mA}, I_B = 0$			
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-5.0		V	$I_E = -10\mu A, I_C = 0$			
Collector Cutoff Current	I _{CBO}	_	-10	nA	$V_{CB} = -50V, I_{E} = 0$			
Conector Cuton Current			-10	μΑ	$V_{CB} = -50V$, $I_E = 0$, $T_A = 125$ °C			
Collector Cutoff Current	I _{CEX}		-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$			
Base Cutoff Current	I_{BL}	_	-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$			
ON CHARACTERISTICS (Note 4)			-					
		75	_		$I_C = -100 \mu A, V_{CE} = -10 V$			
		100	_		$I_C = -1.0 \text{mA}, V_{CE} = -10 \text{V}$			
DC Current Gain	h_{FE}	100	_	_	$I_C = -10 \text{mA}, V_{CE} = -10 \text{V}$			
		100 50	300		$I_C = -150 \text{mA}, V_{CE} = -10 \text{V}$			
		50	_		$I_C = -500 \text{mA}, V_{CE} = -10 \text{V}$			
Collector-Emitter Saturation Voltage	V _{CE} (SAT)	_	-0.4	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$			
Concolor Emilior Calaration Voltage			-1.6		$I_C = -500 \text{mA}, I_B = -50 \text{mA}$			
Base-Emitter Saturation Voltage	V _{BE(SAT)}		-1.3	V	$I_C = 150 \text{mA}, I_B = 15 \text{mA}$			
<u> </u>	V BE(SAT)		-2.6	· ·	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$			
SMALL SIGNAL CHARACTERISTICS				1	+			
Output Capacitance	C_{obo}		8.0	pF	$V_{CB} = -10V, f = 1.0MHz, I_E = 0$			
Input Capacitance	C _{ibo}	_	30	pF	$V_{EB} = -2.0V$, $f = 1.0MHz$, $I_{C} = 0$			
Current Gain-Bandwidth Product	f _T	200		MHz	$V_{CE} = -20V, I_{C} = -50mA,$ f = 100MHz			
SWITCHING CHARACTERISTICS				-				
Turn-On Time	t _{off}	_	45	ns				
Delay Time	t _d	_	10	ns	$V_{CC} = -30V, I_C = -150mA,$			
Rise Time	t _r	_	40	ns	I _{B1} = -15mA			
Turn-Off Time	rn-Off Time t _{off} — 100 ns ,			V _{CC} = -6.0V, I _C = -150mA,				
Storage Time	ts		80	ns	$V_{CC} = -6.0V$, $I_{C} = -150\text{mA}$, $I_{B1} = I_{B2} = -15\text{mA}$			
Fall Time	t _f	_	30	ns				

Notes: 4. Short duration pulse test used to minimize self-heating effect.

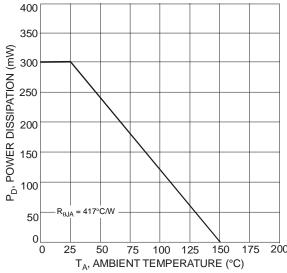


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 1)

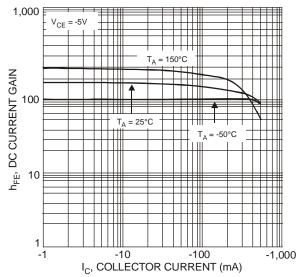


Fig. 2 Typical DC Current Gain vs. Collector Current



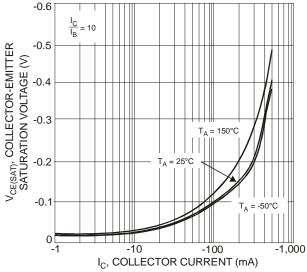


Fig. 3 Typical Collector-Emitter Saturation Voltage vs. Collector Current

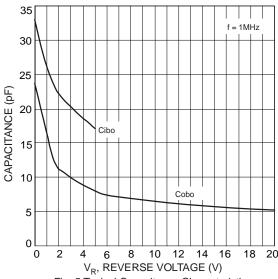


Fig. 5 Typical Capacitance Characteristics

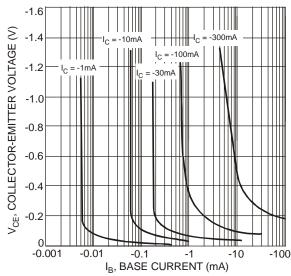


Fig. 7 Typical Collector Saturation Region

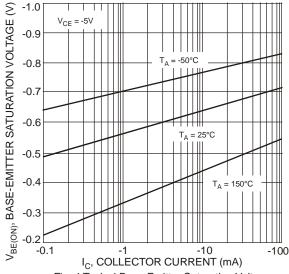


Fig. 4 Typical Base-Emitter Saturation Voltage vs. Collector Current

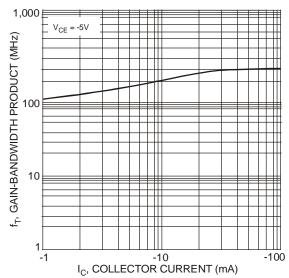


Fig. 6 Typical Gain-Bandwidth Product vs. Collector Current

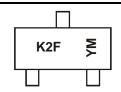


Ordering Information (Note 5)

Part Number	Case	Packaging
MMBT2907A-7-F	SOT-23	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



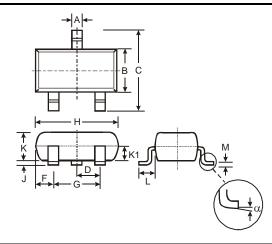
K2F = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: N = 2002) M = Month (ex: 9 = September)

Date Code Key

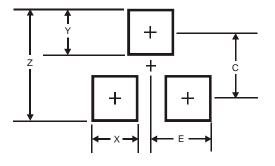
Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
Code	J	K	L	М	Ν	Р	R	S	Т	U	V	W	Χ	Υ	Z	Α	В	С
Month	Jan	1	Feb	Mai	•	Apr	May	,	Jun	Jul		Aug	Sep		Oct	Nov	,	Dec
Code	1		2	3		4	5		6	7		8	9		0	N		D

Package Outline Dimensions



SOT-23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.903	1.10	1.00				
K1	-	1	0.400				
L	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
Е	1.35

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