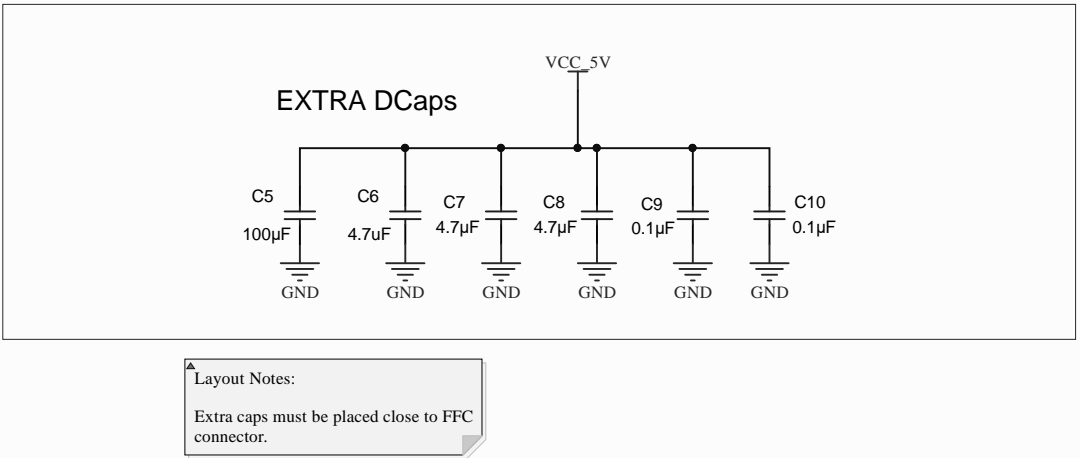
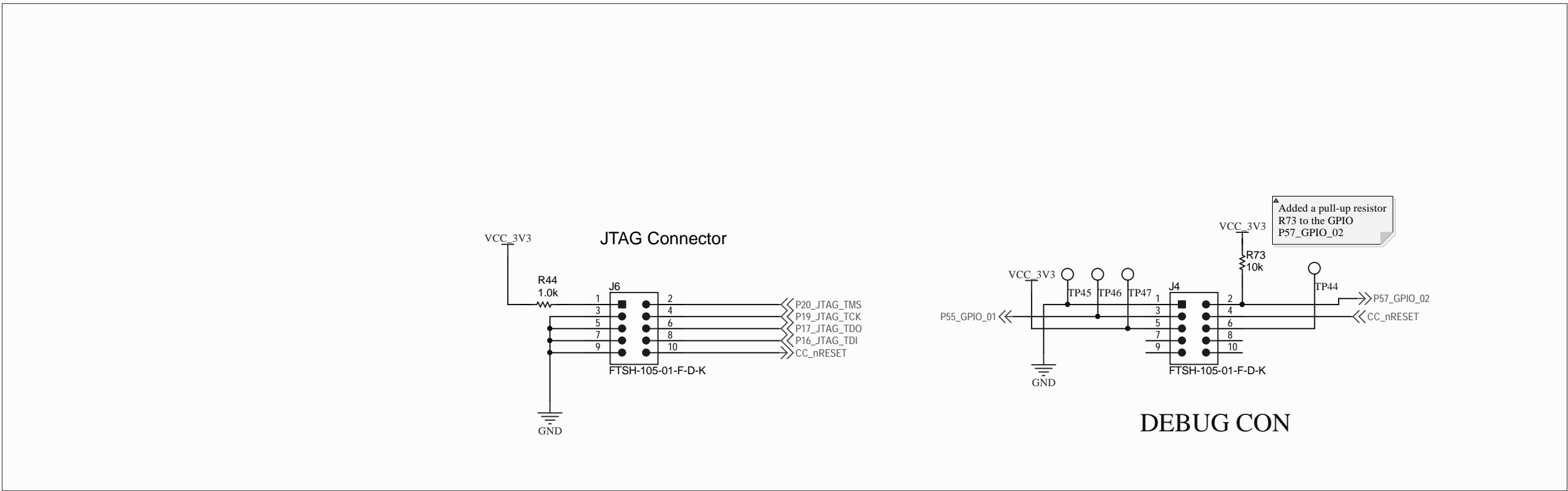


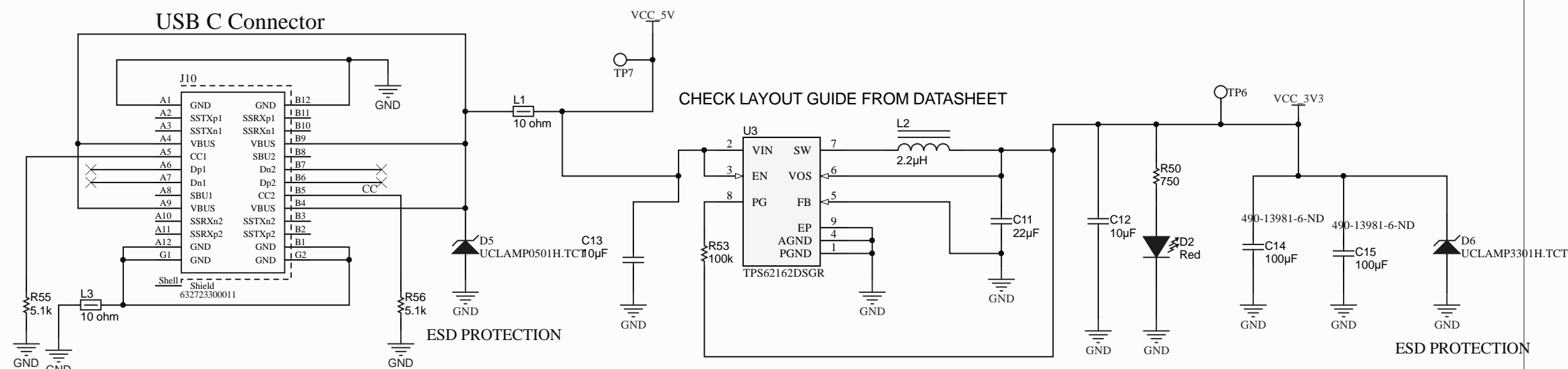
Layout Notes:
Add Ground Test points to both Top and Bottom Layers.



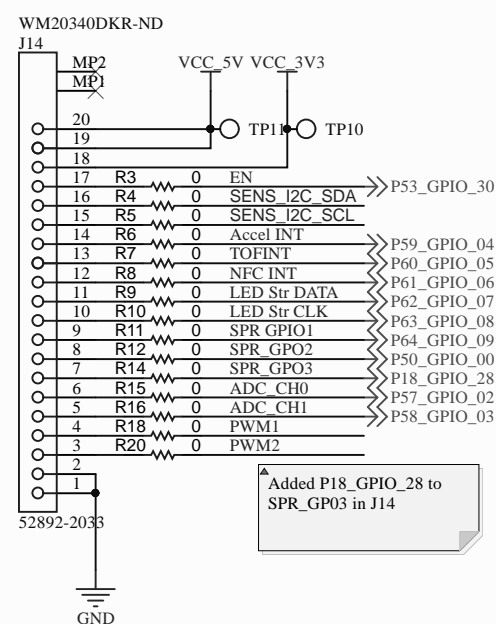
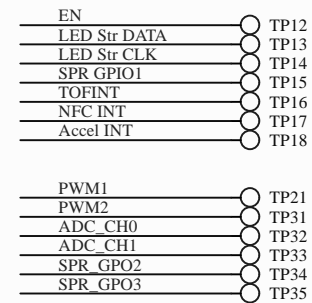
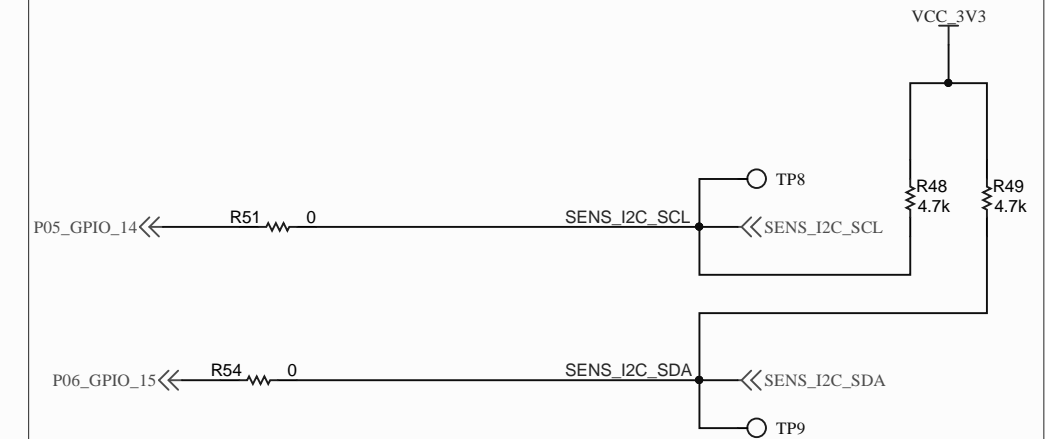
AIRZAI PROPRIETARY & CONFIDENTIAL

Title			
SCHEMATIC, AIRZAI BASE BOARD			
Size	Number	Revision	
A2	SCH-502-0024 Rev A	B	
Date:	2019-09-12	Sheet 1 of 3	
Engineer	KA	PCB Designer: xxx	Check: PM 12/06/17 Release: KA 12/06/17

POWER MANAGEMENT



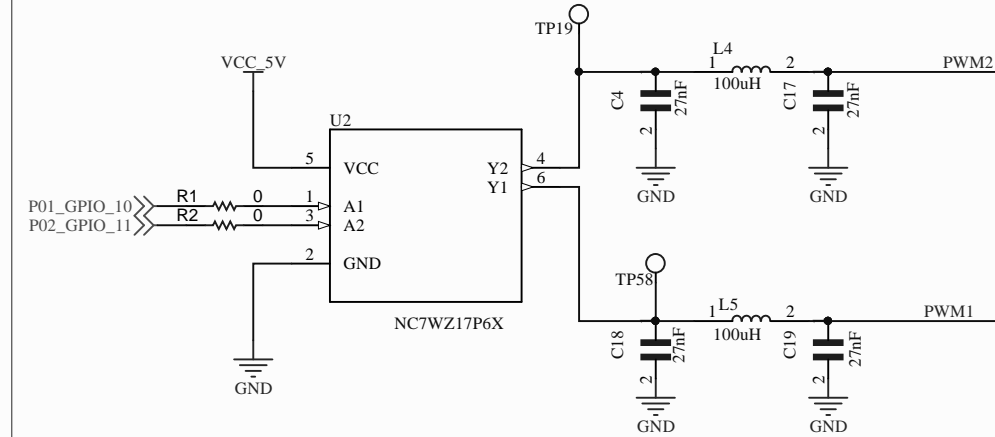
I2C BUS SELECTION



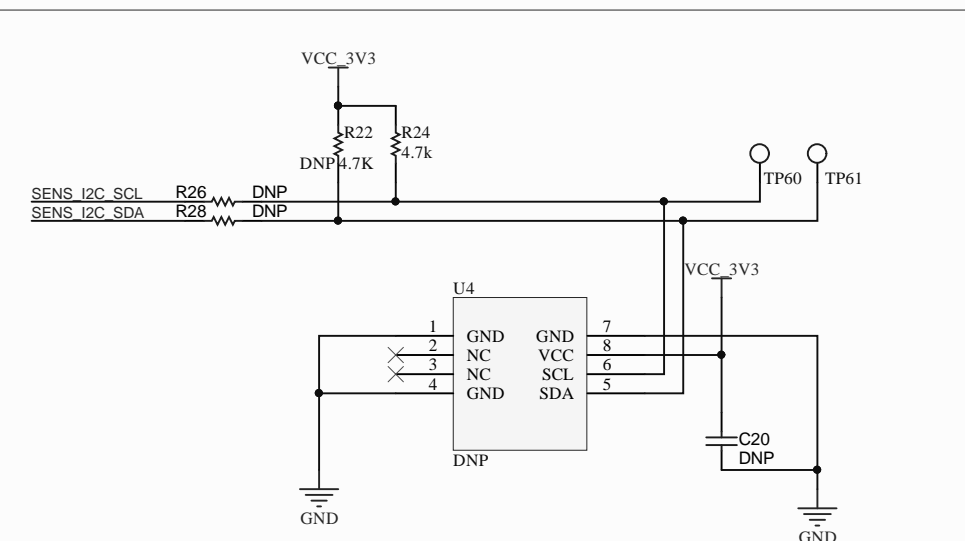
FLEX CON

- Removed P53_GPIO_30 connected to R64.
- Removed P18_GPIO_28 from R67 and connected it to SPP_GPIO3 of I14

▲ Status LED D3 removed
Removed Q1, Q2, Q3,
R61,R62,R63,R64,R67,R68

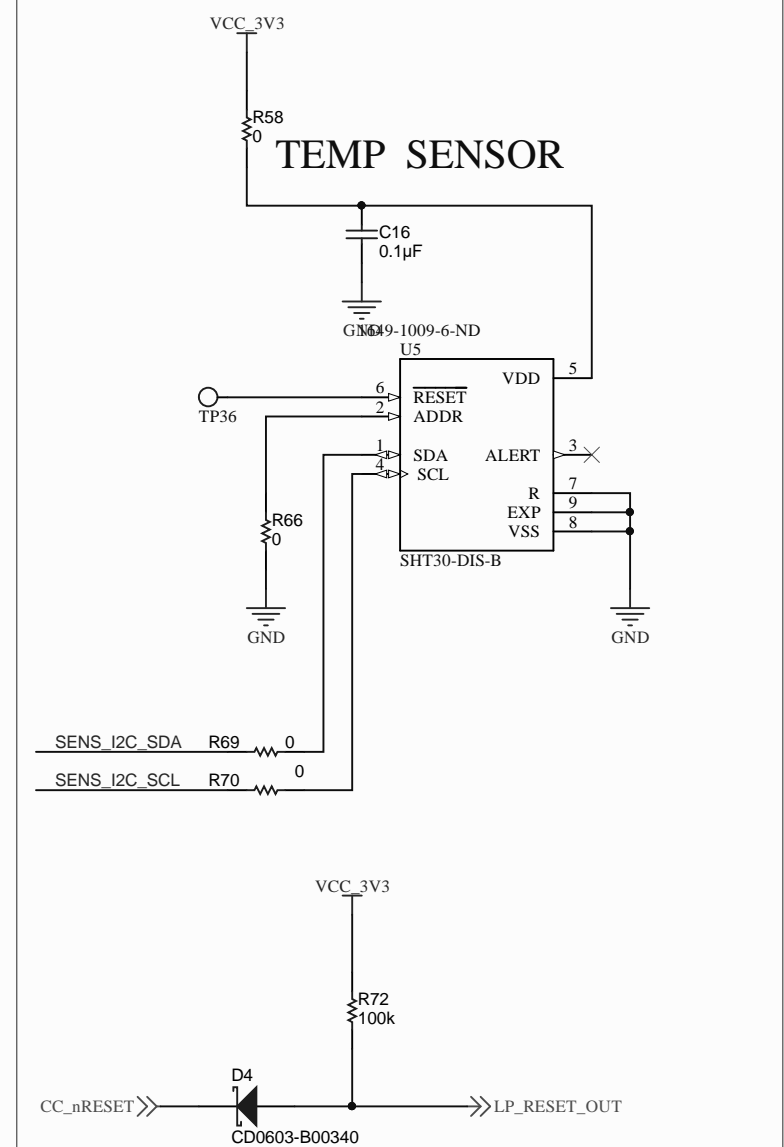


BUFFER



APPLE AUTHENTICATION

TEMP SENSOR



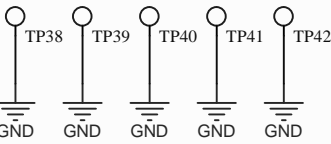
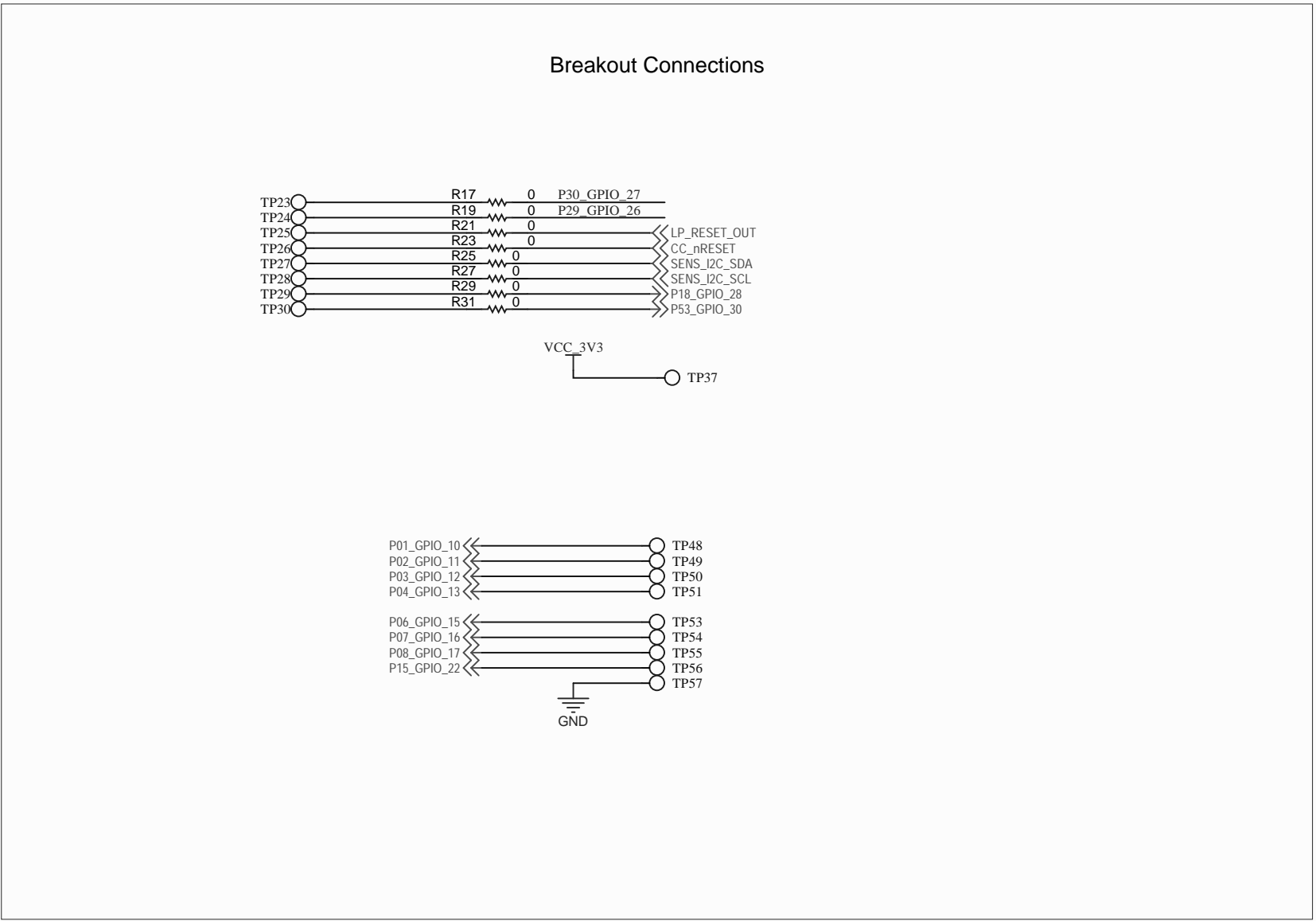
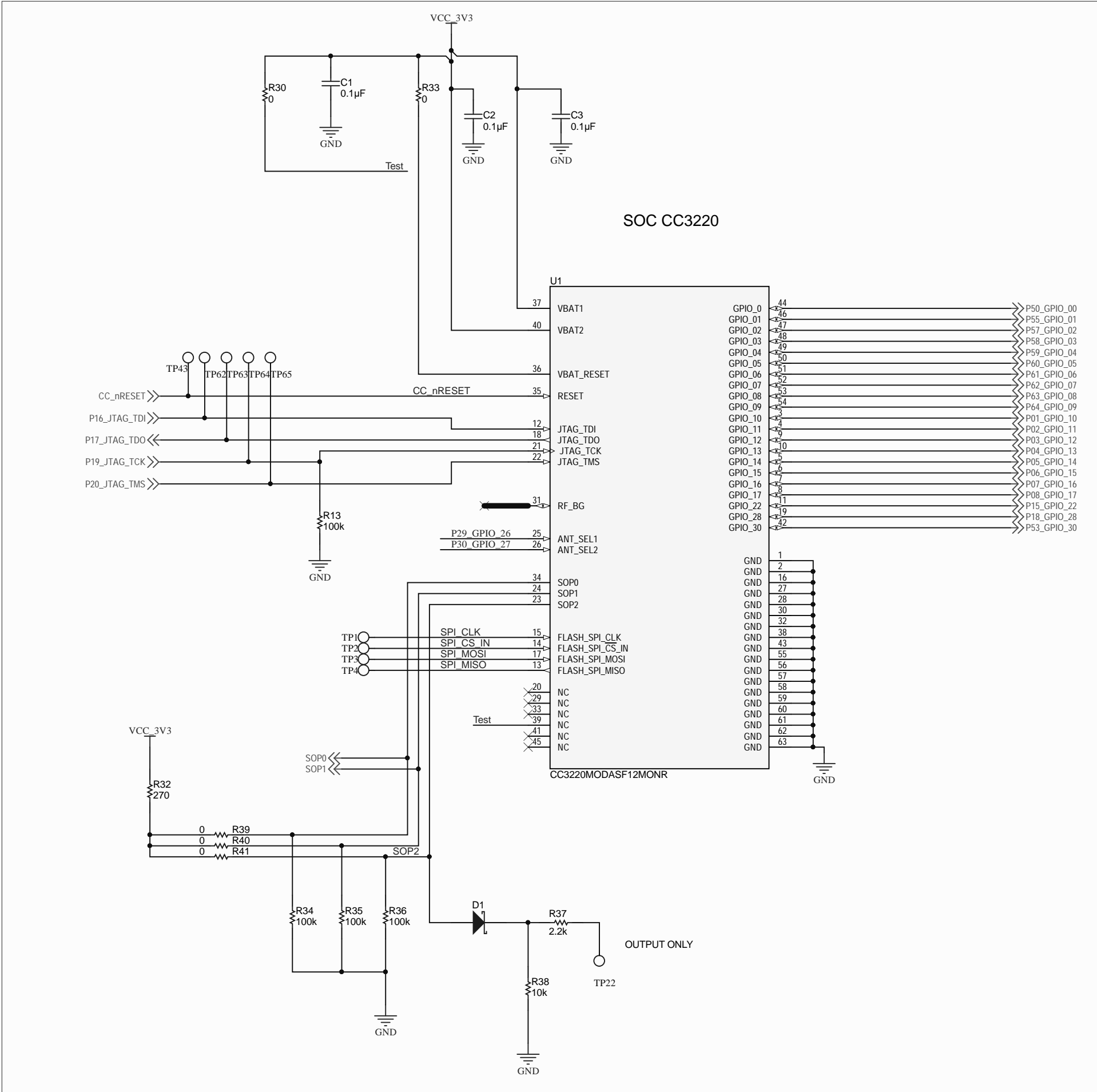
FID1

FID3

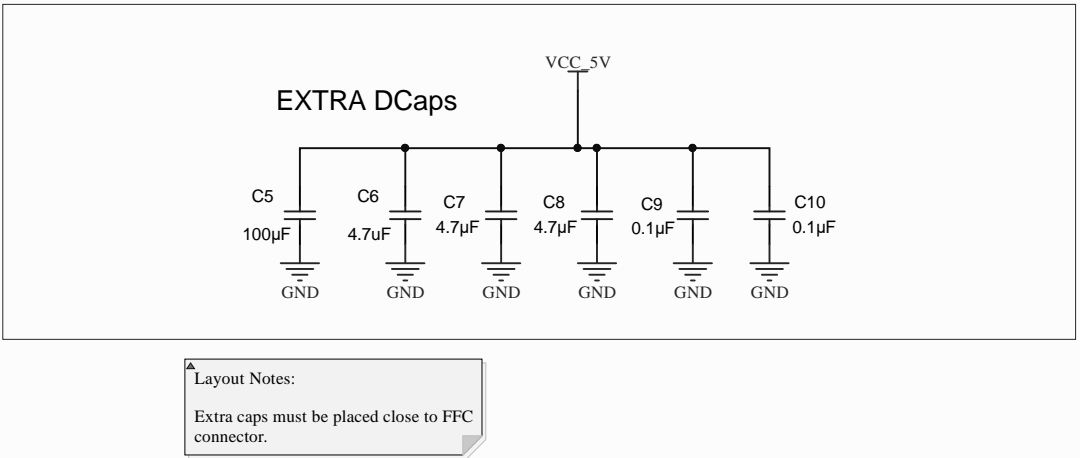
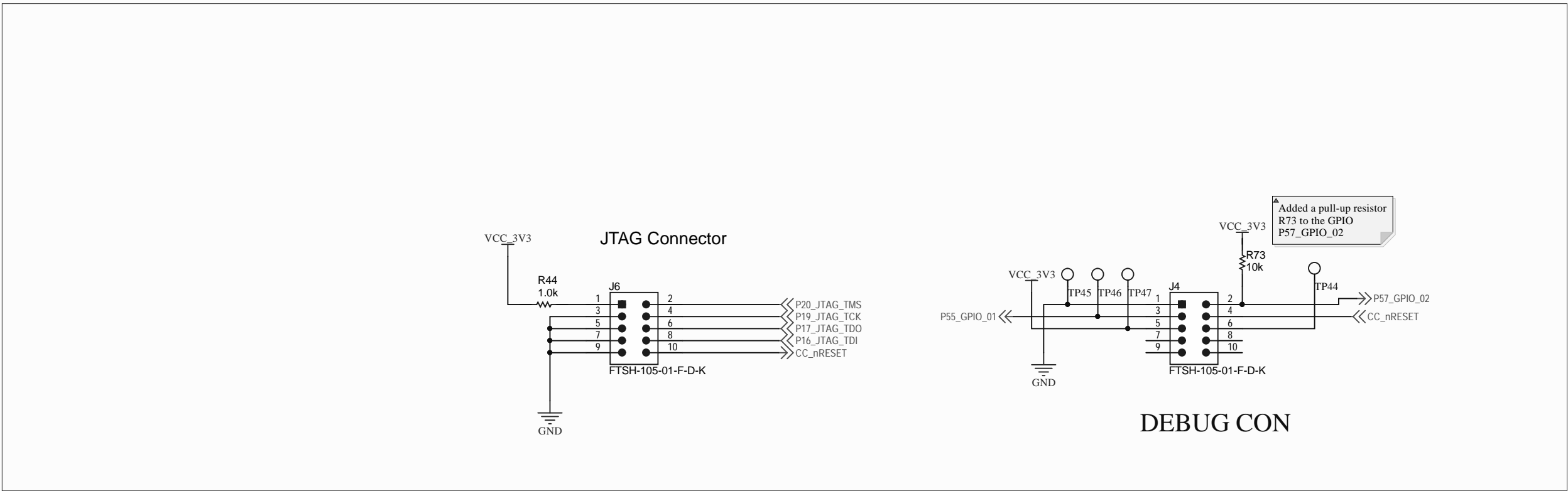
FID4

AIRZAI PROPRIETARY & CONFIDENTIAL

Title				SCHEMATIC, AIRZAI BASE BOARD			
Size		Number		Revision			
C		SCH-110117-001		B			
Date:		2019-09-12		Sheet 2		of 3	
Engineer		KA PCB Designer: xxx		Check: PM 12/06/17		Release: KA 12/06/17	



Layout Notes:
Add Ground Test points to both Top and Bottom Layers.



Layout Notes:
Extra caps must be placed close to FFC connector.

AIRZAI PROPRIETARY & CONFIDENTIAL

Title			
SCHEMATIC, AIRZAI BASE BOARD			
Size	Number	Revision	
A2	SCH-502-0024 Rev A	B	
Date:	2019-09-12	Sheet 1 of 3	
Engineer	KA	PCB Designer: xxx	Check: PM 12/06/17 Release: KA 12/06/17

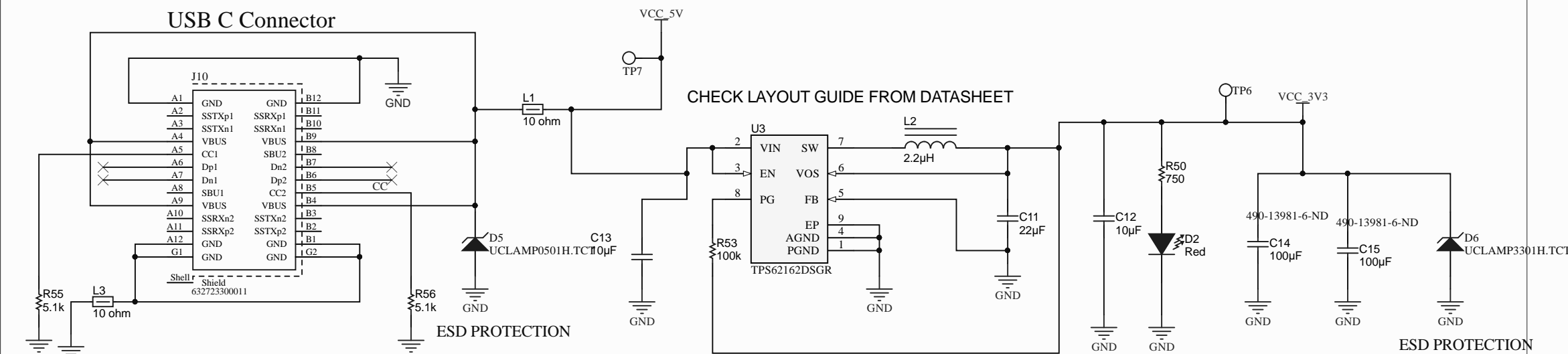
A

B

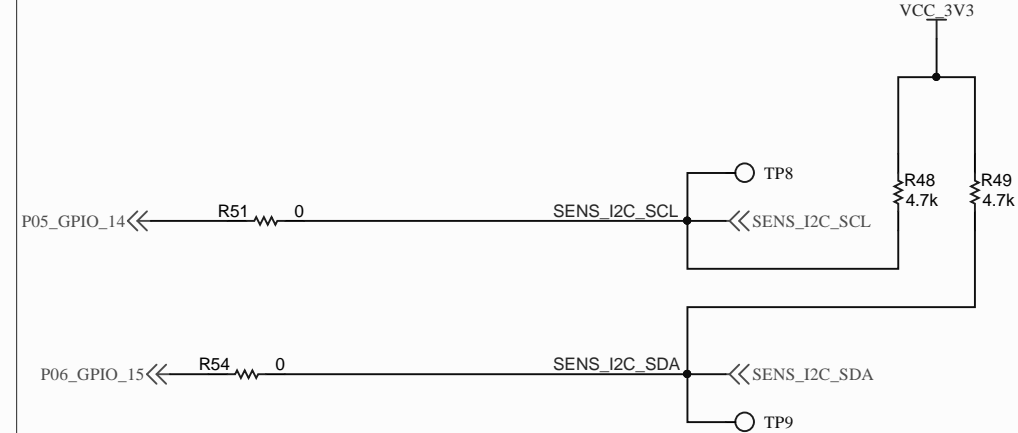
A

B

POWER MANAGEMENT

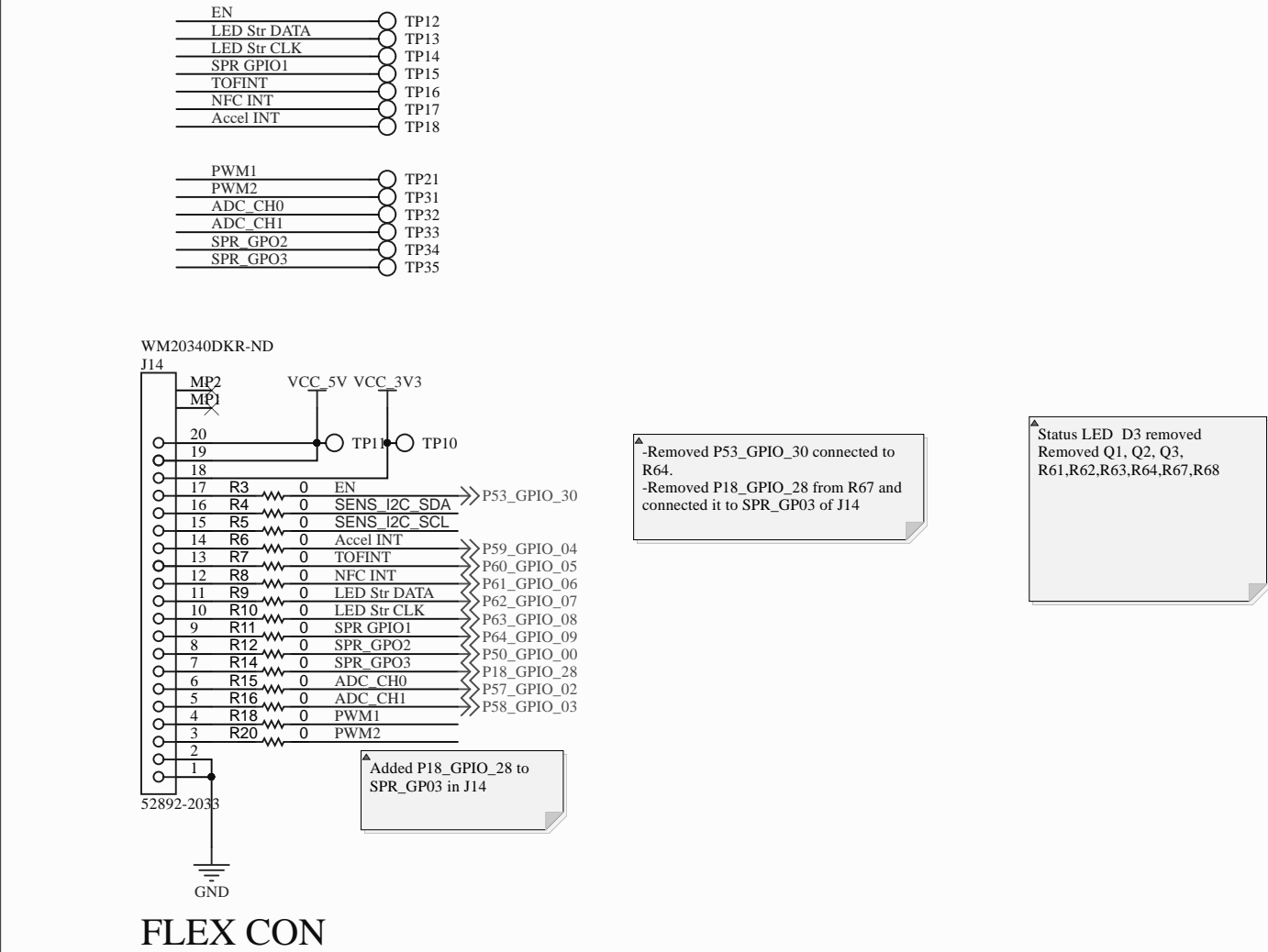


I2C BUS SELECTION

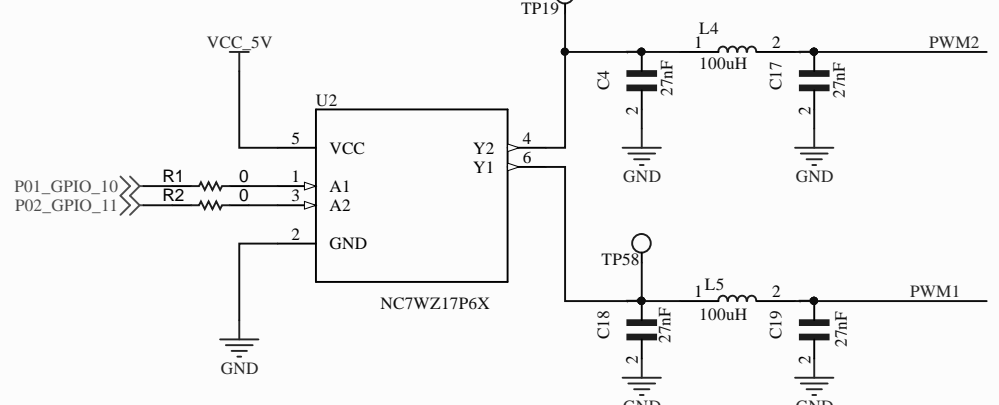


C

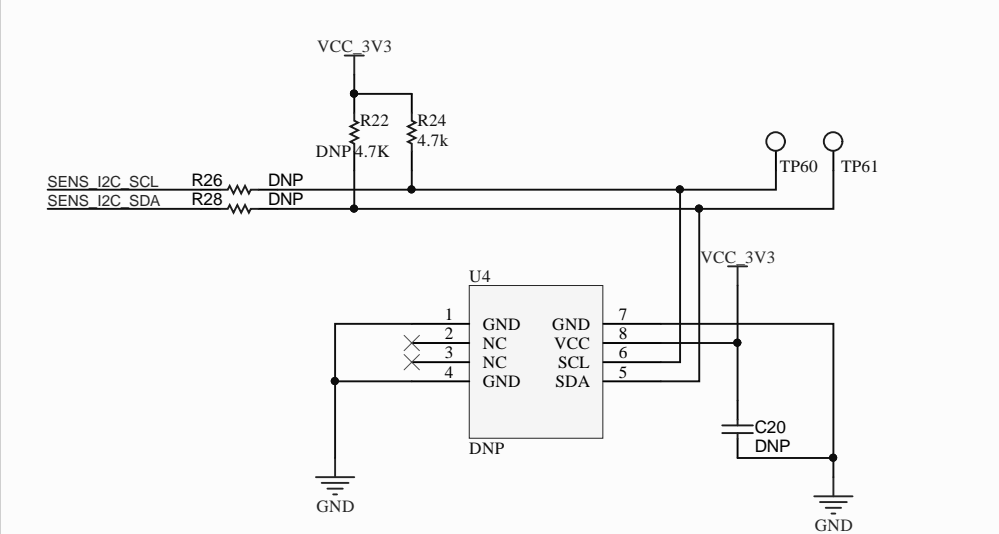
D



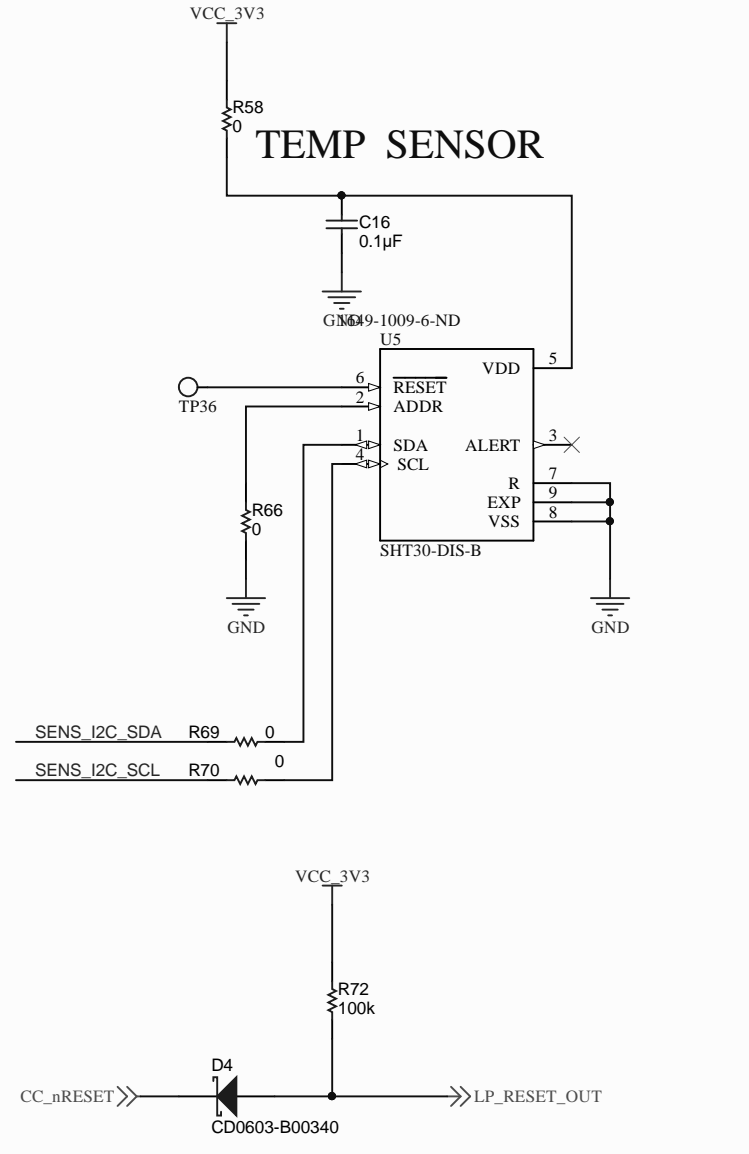
BUFFER



APPLE AUTHENTICATION



TEMP SENSOR



AIRZAI PROPRIETARY & CONFIDENTIAL				
Title				
SCHEMATIC, AIRZAI BASE BOARD				
Size	Number	Revision		
c	SCH-110117-001	B		
Date:	2019-09-12	Sheet 2	of 3	
Engineer	KA	PCB Designer: xxx	Check: PM 12/06/17	Release: KA 12/06/17

