Name:- Kashif Ali
Roll No:- 20P-0648
Section:- 5D

02/10/22

Assignment 01

Q No 1:-

Step-1:- Split Array into tasks (Coarse)

Step-2:- Calculate the Sum(local) of tasks (that we have divided)

Step-3:- Calculate the total Sum of tasks. all local Sum values add to the global value declaired.

Step-4:- Divide total Sum by N
(N Size of Array)

Q NO 8:-

K

20P-0648

Instruction Pipelinning:-

Pipelining attempts to keep every part of processor busy with some Instruction dividing incoming Instructions into series of Sequential Steps performed by dividing units with different parts processed in Parallel.

Super Scalar Execution:-

Super Scalar processor is a CPU that implements a form of Parallelism with in Single Processor can execute more than one instructions during clock Cycle by Simultanously each execution is not not separate processor but an execution resource within single CPU.

K

20P-0648

Waste of Super Scalar Execution :-

Vertical Waste

Horizontal waste.

Vertical waste is when the processor issues no Instruction in a cycle.

Horizontal waste when not all Issues slots can be filled in cycle.

Q NO3:-

K

20P-0648

Map Caching is a way to make your map and image Service run faster when you create map Cache the Server draws at Several Scales and stores copies of Map images the Server can distribute these Images whenever someone asks for Map.

There are three different types of Mapping that are given below.

i) Direct Mapping:- Each block from Main Memory has only one Possible place in the Cache organization in this technique.

ii) Associative Mapping:- Here the Mapping of Main Memory block can be done with any of the caches block

→ Memory address has only two fields i.e word & tag.

iii) Set-Associative Mapping:- It is a combination of advantages of both Direct and Associative Mapping.

Q No 4:

K.
20P-0648

(1) Data Decomposition:

We will use Data Decomposition technique will be used tasks will be assigned to rows data will be Divided.

Q No 5: We will Consider the data set.

T ID	Items
T_1	A, B, C
T_2	B, D, C
T_3	A, F, C, E
T_4	A, C, F
T_5	A, B, C, E, F
T_6	A, B, D
T_7	A, F, D
T_8	D, E, F
T_9	A, B, C, D, E, F
T_{10}	A, C, E, D
T_{11}	B, C, F
T_{12}	B, E, F, D
T_{13}	A, E, D
T_{14}	A, D, E, F
T_{15}	A, B, C, D.
T_{16}	E, F, D, A
T_{17}	C, D, E, F
T_{18}	A, D, B E
T_{19}	B, C, F, E
T_{20}	B, C, D.

This algorithm is used to find the items sets in the table, then we find the combinations of those items and we will find their frequency. Here the minimum support is already given to us like in this case it is 2.

→ So all the items in the set A, B, C, D, E, F and so on exiting more than once? if yes then they will be chosen for the next iteration and these are less frequent will be excluded.

→ Then in the second run we will find the frequencies of the combinations of those items, the frequency of each ~~item~~ combination now any the frequencies less than 2 will be excluded.

→ pruning will be done now, means that if we get a combination A, B, C we will check if all the combinations of A, B, C lets say A, B and A, C and so on will be there for >2 times, if not then we remove the combination from the next item set.

Input Data Partioning :

The input item sets can be divided subsets and then the processing codes will be parallely searching the frequency of each rule set.

A B C D E F
A B D
A B F

we get to chunk

A B C = 1
B D = 2
A B = 3 .

Output Data Partioning :-

Output data partioning can be done we get the combination initialy during the running stage. from that combination (output) we find the frequencies of the rule set.

eg:- A, B, C, D

we get to rule.

A B	2
C D	2
A C	1

we make

Intermediate Data Partioning :-

Intermediate data partioning is talking place because multiple iterations are talking place. Initially in each iteration the item set (considering the input) is checked whose 2 set is make from the Input item set. In every iteration we get the total combination

and divide them in specific rule set- and choose the combination whose the frequency is greater than the minimum.

TID	Items.
T_1	A, B, C
T_2	A, B
T_3	A, D
T_4	A, D, C

→ input data partioning

Items	frequency
A	4
B	2
C	2
D	2

Items	frequency
A, B	2
A, C	1
A, D	2
B, C	1
B, D	0
C, D	1

→ output data partioning

Items	frequency
A, B	2
A, D	2

20-048