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BCD To Seven Segment Display

BCD to Seven Segment Decoder

BCD (Binary Coded Decimal) is an encoding scheme which represents each of the decimal numbers by its equivalent 4-bit binary pattern. Seven segment displays comprise of seven individual segments formed by either Light Emitting Diodes (LEDs) or Liquid Crystal Displays (LCDs) arranged in a definite pattern (Figure 1).

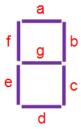


Figure 1 Seven segment display

For the display to work, these segments are to be driven by the certain logic level at their input. Depending on this, seven segment displays are found to be of two types viz., common cathode type and common anode type.

A common cathode display (Figure 2a) has all the cathode terminals of its LED segments tied together (green line). Further, this is grounded and hence is considered to be at logic 0 state. This means that in order to light up an LED, one needs to drive it high. On the other hand, a common anode display shown by Figure 2b has all its anode terminals connected together which is further driven high by connecting it to a positive supply voltage (green line). Hence for this kind of display to work, one has to drive low on the cathode terminals of the individual LED segments.

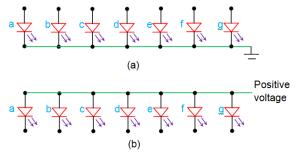


Figure 2 (a) Common cathode type display (b) Common anode type display

BCD to seven segment decoder is a circuit used to convert the input BCD into a form suitable for the display. It has four input lines (A, B, C and D) and 7 output lines (a, b, c, d, e, f and g) as shown in Figure 3. Considering common cathode type of arrangement, the truth table for the decoder can be given as in Table I.

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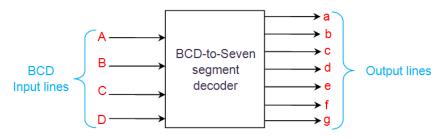
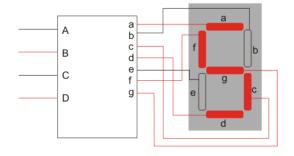


Figure 3 BCD-to-Seven segment decoder

Table I Truth table for common cathode type BCD to seven segment decoder

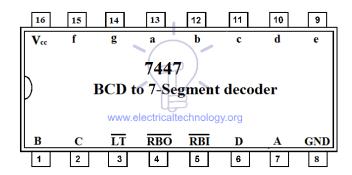
Decimal Digit	Input lines				Output lines							Display
	A	В	С	D	а	b	С	d	е	f	g	pattern
0	0	0	0	0	1	1	1	1	1	1	0	8
1	0	0	0	1	0	1	1	0	0	0	0	8
2	0	0	1	0	1	1	0	1	1	0	1	8
3	0	0	1	1	1	1	1	1	0	0	1	8
4	0	1	0	0	0	1	1	0	0	1	1	8
5	0	1	0	1	1	0	1	1	0	1	1	8
6	0	1	1	0	1	0	1	1	1	1	1	8
7	0	1	1	1	1	1	1	0	0	0	0	8
8	1	0	0	0	1	1	1	1	1	1	1	8
9	1	0	0	1	1	1	1	1	0	1	1	8

This table indicates the segments which are to be driven high to obtain certain decimal digit at the output of the seven segment display. However, it is to be noted that in the case of common anode type, the only change will be to interchange ones and zeros on the table. This means that from the truth table so obtained one can get to know where low has to be driven so as to obtain the required digit at the output.



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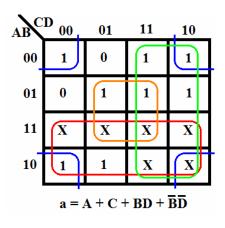
The commonly used IC for *BCD to 7-segment decoding is 7447*. The pin configuration of 7447 is given in the figure below.



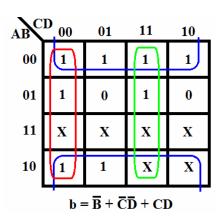
Karnaugh Maps Simplification

For other combinations of input, the output is "don't care X" as there are no more digits to display. We will derive the expression for each output using Karnaugh map (K-MAP).

output a:



output b:



Application of BCD to Display Decoder

- This circuit can be used as a timer circuit.
- With little modification, it can be also used to display the number clock pulses.
- It can also be used with modification to display alphabet display system instead of decimal display system.
- 7-Segment display are mostly used in digital clocks, electronic meters, odometers as well as LCD application due to low current consumption.
- They are also used in various measuring instruments, digital watches and digital counters