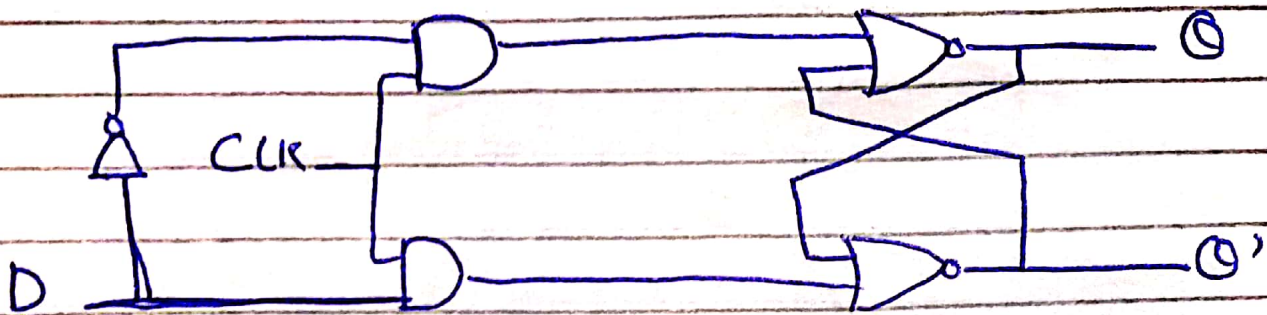
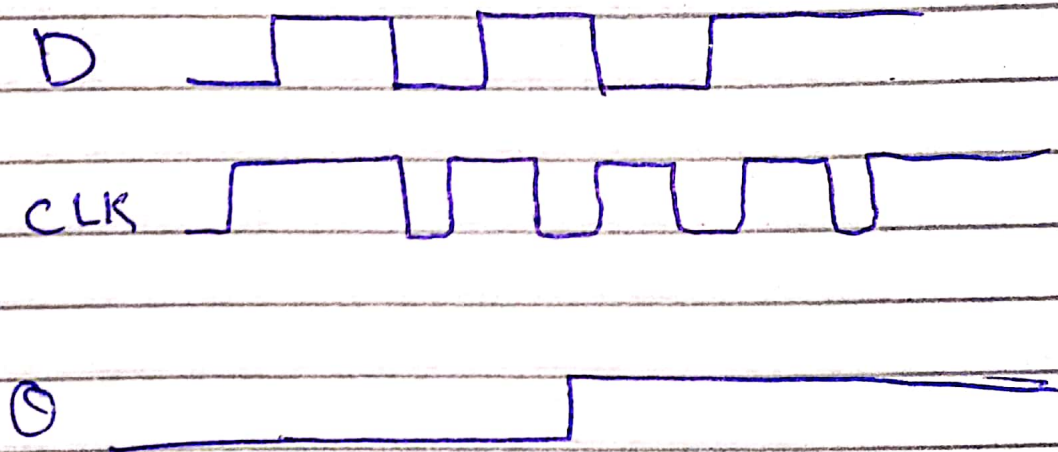


(2)

(a) Logic Diagram.



Timing Diagram.



Truth table.

D	S	R	Q
0	0	0	Ne
1	0	1	0
	1	0	1
	1	1	?