Instruction	Opcode	Instruction	Opcode	Instruction	Opcode	Instruction	Opcode
mov reg8, imm	1011 0rrr [imm8]	pop reg16	0101 1rrr	and reg16, reg16	0010 00x1 [11-reg-r/m]	Loop	1110 0010 [disp8]
mov reg16, reg16	1000 1001 [11-reg-r/m]	pop mem16	1000 1111 [mod-000-r/m]	and reg8, mem8	0010 0010 [mod-reg-r/m]	Loope Loopz	1110 0001 [disp8]
mov reg16, imm	1011 1rrr [imm16]	push reg16	0101 0rrr	and reg16, mem16	0010 0011 [mod-reg-r/m]	Loopne Loopnz	1110 0000 [disp8]
mov reg8, mem	1000 1010 [mod-reg-r/m]	push reg16	1111 1111 [11-110-r/m]	and mem8, reg8	0010 0000 [mod-reg-r/m]	or mem16, reg16	0000 1001 [mod-reg-r/m]
mov mem, reg16	1000 1001 [mod-reg-r/m]	push mem16	1111 1111 [mod-110-r/m]	and mem16, reg16	0010 0001 [mod-reg-r/m]	or reg16, reg16	0000 10x1 [11-reg-r/m]
mov reg16, mem	1000 1011 [mod-reg-r/m]	and reg8, reg8	0010 00x0 [11-reg-r/m]	and reg16, imm16	1000 00s1 [11-100-r/m] [imm]	or reg16, imm16	1000 00s0 [11-001-r/m] [imm]

Mod=11			Effective Address Calculation			
R/M	W=0	W=1	R/M	Mod= 00	Mod= 01	Mod= 10
000	AL	AX	000	[Bx] + [SI]	[Bx] + [SI] + D_8	[Bx] + [SI] + D_{16}
001	CL	CX	001	[BX] + [DI]	[BX] + [DI] + D_8	[BX] + [DI] + D_{16}
010	DL	DX	010	[BP] + [SI]	[BP] + [SI] + D_8	[BP] + [SI] + D_{16}
011	BL	BX	011	[BP] + [DI]	[BP] + [DI] + D_8	[BP] + [DI] + D_{16}
100	AH	SP	100	[SI]	[SI] + D_8	[SI] + D_{16}
101	CH	BP	101	[DI]	[DI] + D_8	[DI] + D_{16}
110	DH	SI	110	Direct Address	[BP] + D_8	[BP] + D_{16}
111	BH	DI	111	[BX]	[BX] + D_8	[BX] + D_{16}