PRACTICAL: 2(C7)

AIM: Add Two 32-bit numbers stored in consecutive memory locations and store the result in memory locations starting from 7000H

CODE:

org 100h

MOV [4000H], 1111H

MOV [4002H], 1111H

MOV [4004H], 2222H

MOV [4006H], 2222H

MOV AX, [4000H]

MOV BX, [4002H]

MOV CX, [4004H]

MOV DX, [4006H]

ADD AX, CX ; ADC SUPPORTS 16BIT ADDITION ONLY

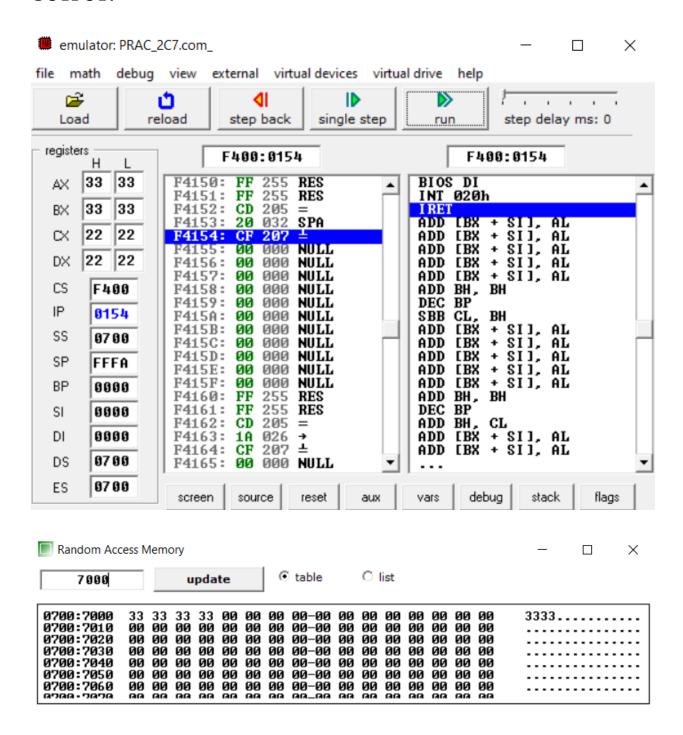
ADC BX, DX ;MSB

MOV [7000H], AX

MOV [7002H], BX

ret

OUTPUT:



CONCLUSION:

We learned about ADC command and its implementation for 32-Bit Numbers.