

Q1) Compilation process contains the sequence of various phases. Each phase takes source program in one representation & produces output in another representation.

* Various phases of compilation :-

1) Analysis - break the source code into parts and arranges these pieces

2) Synthesis - Generation of target code or machine code.

→ Analysis phase is divided into three :-

i) lexical analysis - broke the source code into tokens.

Ex :- $a = a + b * c ;$

$a \rightarrow$ identifier 1

$= \rightarrow$ (assignment) operator

$a \rightarrow$ identifier 1

$+$ → operator

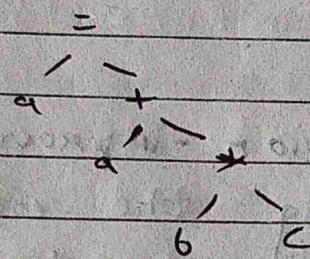
$b \rightarrow$ identifier 2

$*$ → operator

$c \rightarrow$ identifier 3

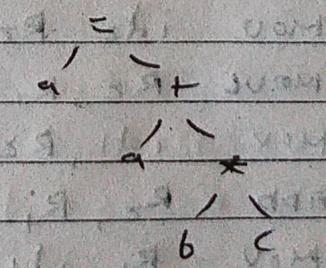
$;$ → special symbol

- 2) Syntax analysis - It is called also as parsing.
- determines syntactical mistakes.
 - if code is error free, then it generates the tree.



- 3) Semantic Analysis - determines meaning of a source string.

- For example: Matching of expression, if else statement, etc checking the scope of expression.
- Also generates tree.



→ Synthesis phase is divided into three:

- 1) Intermediate code generation

- converts code into intermediate phase
- one of the intermediate code example is "three address code"
- Example :-

$$a = a + b * c$$

$id_1 \quad id_2 \quad id_3$

$$t1 = id_2 * id_3$$

$$id_1 = id_1 + t1$$

2) code optimization - improves intermediate code

- by optimizing the code, overall running of program can be improved.

3) code generation - Generation of target code

- The intermediate code instructions are translated into machine level instructions

MOV $id_3, R1$

MOV $id_2, R2$

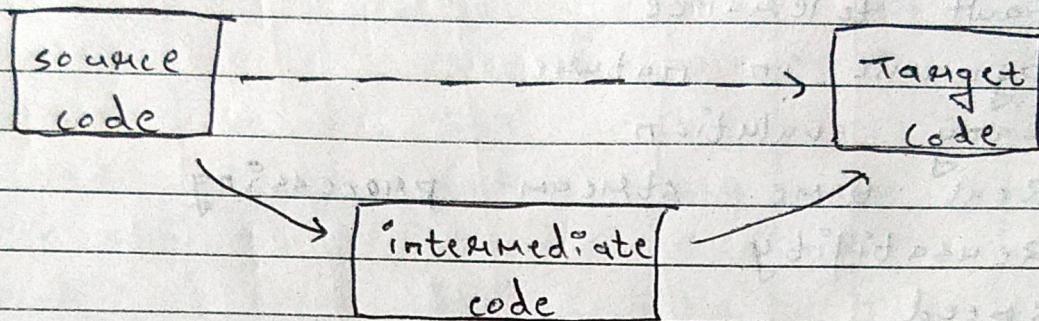
MOVL $R2, R1$

MOV $id_1, R2$

ADD $R2, R1$

MOV $R1, id_1$

Q2) Need of Intermediate code generation phase :



- If a compiler translates the source lang. to its target machine without generating intermediate code, then for each new machine, a full native compiler is required.
- Intermediate code eliminates the need of new full complex for every unique machine by keeping analysis position same for all the compilers.
- It becomes easier to apply the source code modification to improve code performance by applying code optimization techniques on the intermediate code.

→ Ways to implement three address code

1) Quadruples:

- It is structure which consists of 4 fields ; OP, arg₁, arg₂ & result.
- OP denotes operator
- arg₁ & arg₂ are two operands
- Result is used to store result of the expression.

2) Triples:

- In this when a reference to another triple's values is needed , a pointer to that triple is used.
- So it consists of only three fields : OP, arg₁ & arg₂

Q3) Conditions for a grammar to be LL(1)

i) if grammar is free from F productions, then

a is LL(1) if $A \rightarrow^* \alpha, \beta \dots$

if $\text{first}(\alpha_1) \cap \text{first}(\alpha_2) \cap \text{first}(\alpha_3) = \emptyset$

ii) if A has C productions

$$A \rightarrow \alpha_1 C$$

if $\text{first}(\alpha_1) \cap \text{Follow}(A) = \emptyset$

then given grammar is LL(1)

iii) if G has only one alternative on R

i) variable then G is LL(1)

$$\text{ex: } A \rightarrow \alpha$$

$$B \rightarrow \beta$$

$$C - B$$

given grammar

$$E \rightarrow TE'$$

$$E' \rightarrow +TE' | G$$

$$T \rightarrow FT'$$

$$T' \rightarrow +FT' | E$$

$$F \rightarrow id | (E)$$

Symbol $\text{first}()$ Follow

E $\{\epsilon, id\}$ $\{*, +, (\,)\}$

E' $\{+, (\,)\}$ $\{*, +, (\,)\}$

T $\{\epsilon, id\}$ $\{+, (\,)\}$

T' $\{+, (\,)\}$ $\{+, +, (\,)\}$

F $\{\epsilon, id\}$ $\{*, +, (\,)\}$

Q5) Symbol table is an important data structure created & maintained by the compiler in order to keep of semantics of variable i.e. it stores information about the scope and binding information about the scope & names.

Information stored in symbol table are :

- variable names and constants
- procedure and function names
- literal constants & strings
- compiler generated temporaries
- labels in source languages
- associated attributes with identifiers.

→ Data structure used to implement symbol table are :-

i) List - In this method an array is used to store names & associated information

Pros :- insertion is fast $O(1)$

Cons :- it takes minimum amount of space

Cons :- Lookup is slow for large tables $O(n)$

2) Binary Search tree - In this generally we add two link field i.e. left & right child.

Pros :- can grow dynamically
Cons :- insertion & lookup are $O(\log n)$ on average.

3) Hash Tree - In this an array is used with an index range

Pros :- insertion & lookup can be made very fast $O(1)$

- quick to search is possible

Cons :- hashing is complicated to implement
- average time complexity $O(n)$

4) Linked list - this implementation is using linked list by adding link field to each record.

Pros :- insertion is fast $O(1)$

Cons :- lookup is slow for large tables
 $O(n)$ on average.

Q7) LL(1) grammar must be unambiguous + no left recursive + left factored.

If G is a grammar free from ϵ -productions then G is LL(1) if $\forall A \rightarrow L_1, L_2 \dots$

$$S \rightarrow A_1^{\alpha_1} | A_2^{\alpha_2} | A_3^{\alpha_3} | \dots$$

$$\text{first}(L_1) = \text{first}(\alpha_1) = \{x\}$$

$$\text{first}(L_2) = \text{first}(\alpha_2) = \{y\}$$

$$\text{so } \text{first}(L_1) \cap \text{first}(L_2) = \emptyset$$

Given grammar is not LL(1)

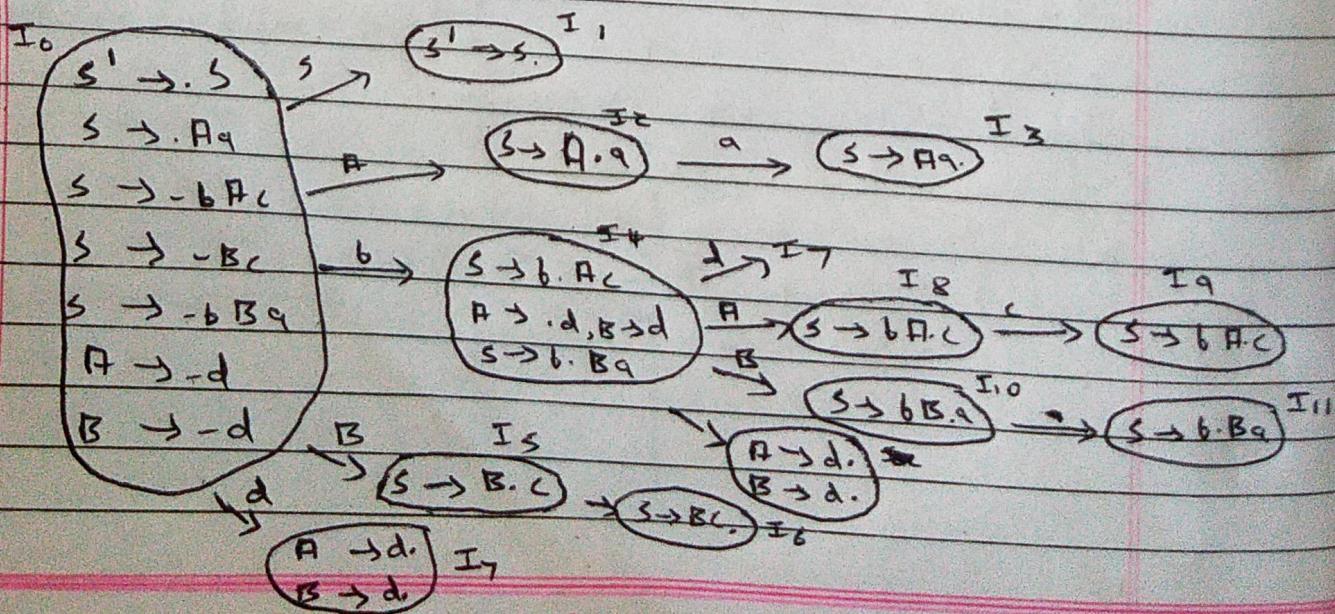
Grammar is SLR if SLR() parse table has no conflicts.

$$G : S' \rightarrow S$$

$$S \rightarrow A_q | bA_c | .B_c | bB_q$$

$$A \rightarrow d$$

$$B \rightarrow -$$



Parse table for SLR(1)

	a	b	c	d	\$	S	A	B
I ₀						S ₄		

1 2 5

Accept

I₂ S₃

I₃ S₁

I₄ S₇ 8 10

I₅ S₆

S₃

(X₅/X₆)

I₆

I₇

I₈

I₉

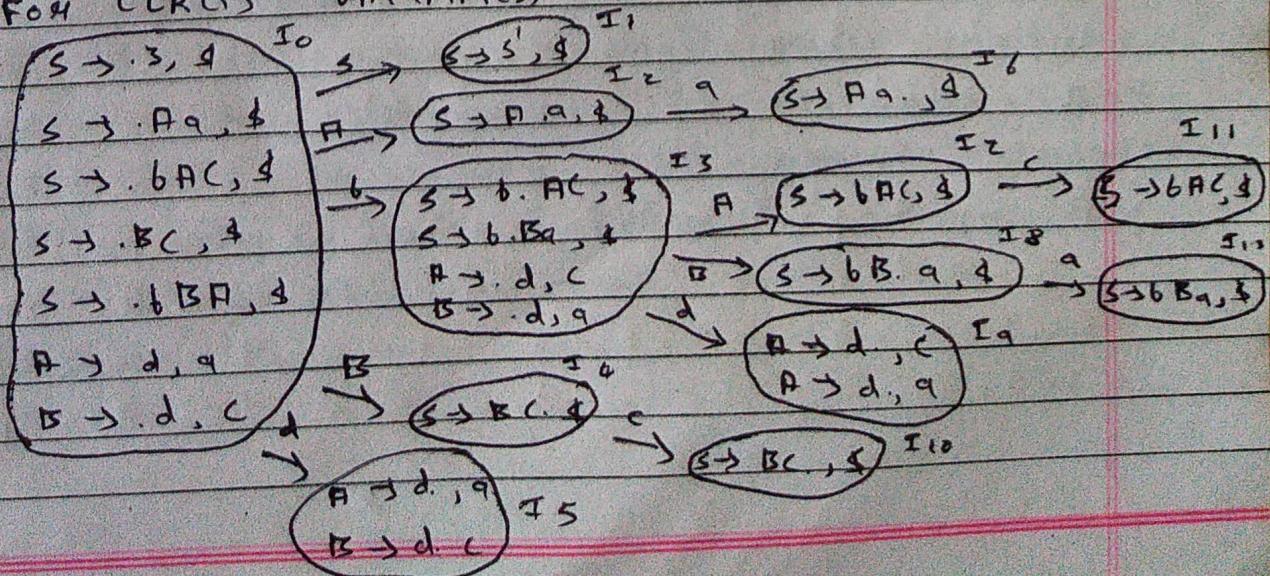
I₁₀

I₁₁

I₁₂

Hence we got RR conflict, so grammar is not in SLR(1)

=> FOF (LR(1)) LHMMEH



a b c d e s F B

I₀ S₃ S₄ 1 2d-4

I₁

I₂ S₆

I₃ S₉ 7 8

I₄ S₁₀

I₅ S₅ S₆

I₆

I₇ S₁₁

I₈ S₁₀

I₉ S₆ S₅

I₁₀ S₃

I₁₁ S₂

I₁₂ S₄

In CLR(1) we get not any L-R/R-L conflict
 Given grammar is CLR(1) grammar.

Q10) Basic block - it is a sequence of three address statements which control enters at the beginning and leaves only at the end without any jumps or hauls

Three address code of c code

```

1   f=1;
2   i=z;
3   if (i>x) goto(9);
4   t1 = F * i;
5   f=t1;
6   t2 = i+1;
7   i=t2;
8   goto(3);
9   goto calling program
  
```

Basic blocks

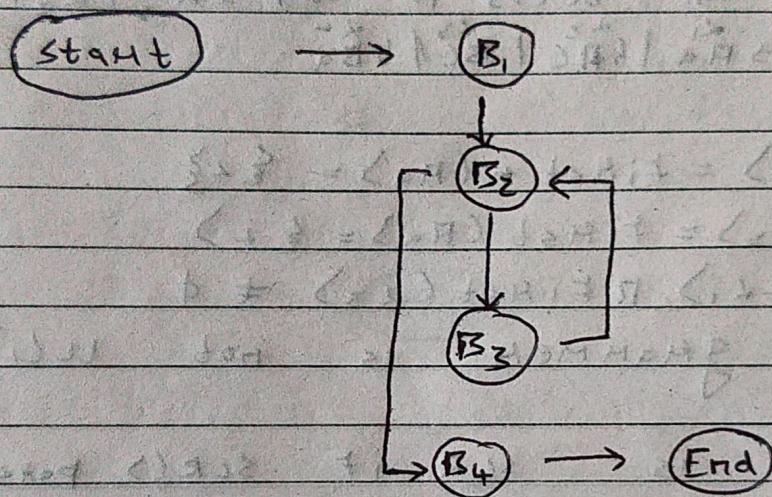
```

f=1;
i=z;
if (i>x) goto(9);
t1 = F * i;
f=t1;
t2 = i+1;
i=t2;
  
```

} B1 } B2

goto (3) ↴
goto calling program] B4

Control flow graph



Q13) The main function of linker is to generate executable files. The linker takes input of object code generated by compiler / assembler. It is a tool that merges the object files produced by separate compilation of assembly & creates an executable.

Whereas the main function of loader is to load executable file to Main Memory. Also the loader takes input of executable files generated by linker.

⇒ There are basically four types of loaders:-

1) Absolute loader : it is a simple type of loader. In this simply accepts the machine language code produced by assembly & place it into main memory at the location specified by assembly.

2) Bootstrap loader : When a computer is first turned on or restarted a special type of absolute loader is executed, called bootstrap loader.

Sometimes

It loads the operation system into the main memory and executes related program. It is added to the beginning of all object programs that are to be loaded into empty ideal system.

3) Relocating loader: To avoid possible reassembling of all sub-routines, it is changed to perform the task of allocation & linking of program, so relocating loader was introduced. Relocation provides efficient sharing of machine with larger memory & when several independent programs are to be run together.

4) Direct linking loader: It is general relocatable loader & most popular loading scan presently used. It allows programmer multiple procedure segments & multiple data segments. It gives programmer complete freedom in referencing data or instructions content in other segments.

- a) The error handling in a parser has the following goals :-
- Report the presence of errors clearly & accurately.
 - Recover from each error quick enough to detect subsequent errors.
 - add minimal overhead to the processing of correct programs.

⇒ Error recovery strategies used by parser :-

- i) Panic Mode Recovery - On discovering an error that parser discards input symbols one at a time until one of a designated set of synchronizing tokens are found while it often skips a considerable amount of input, it has the advantage of simplicity.
- ii) Phrase level Recovery - On discovering an error, parser may perform local correction on the remaining input, it can correct any input string. Its major drawback is the difficulty it has in coping with situations in which actual error has occurred before the point of detection.
- iii) Error Production - A parser constructed from a grammar augmented by common error detects the anticipated errors diagnostic about the erroneous constructs that has been recognized in the input.

Q6) Because of large amount of time consumption in moving characters, specialised buffering techniques have been developed to reduce the amount of overhead required to process in input characters.

⇒ The techniques are as follows:

- 1) Scheme - consists of two buffers, each consists of N -character size which are reloaded alternatively.
 - N -number of characters on one disk block, e.g. 4096
 - N -characters are read from the input file to the buffer using one system Read command.
 - EOF is inserted at the end if the number of characters is less than N .

- 2) Pointers - Two pointers lexemeBegin and forward are maintained.
 - lexemeBegin points to the beginning of the current lexeme which is yet to be found.
 - Forward scans ahead until a match for a pattern is found.
 - Once a lexeme is found, lexemeBegin is set to the characters immediately after the lexeme which is first found and forward is set

to the character at its right end.

- current lexeme is the set of two pointers.

(a2) The runtime environment is the environment in which a program or application is executed.

It's the hardware & software infrastructure of a particular codebase in real time

⇒ Memory allocated during compile time means the compiler resolves at compile time where certain things will be allocated inside the process memory map.
 For example, consider a global array
`int arr [100];`

- The compiler knows at compile time the size of array & the size of int, so knows the entire size of the array at compile time
- Also a global variable has static storage duration by default it is allocated in static memory area of the process memory space
- The compiler decides during compilation in what address of that static memory area the array will be.

⇒ Limitations of stack based memory allocation

- Each process is allocated a fixed amount of stack space.

- Amount of memory available through stack based allocation is commonly far less

than the amount of memory available through heap-based memory allocation

- Once you are out of stack-based variable memory your process will crash.