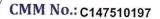
SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES (AUTONOMOUS)

Dr. D.K. Audikesavulu Marg (Bangalore - Tirupathi Bye-Pass Road), Chittoor-AP.517 127, INDIA.

SI. No.: 00812

(Affiliated to JNTUA, Ananthapuramu)



CONSOLIDATED GRADE SHEET

Name: S S R C KASHYAP Year of Admission: 2014
Course: ELECTRONICS AND COMMUNICATION ENGINEERINGYear of Completion: 2018

Class Awarded: ***FIRST CLASS WITH DISTINCTION*** Admission No.: 14751A04C4



	Class Awarded: FIRST CLASS WITH DISTINCTION*** Admission No.: 14751A04C4								
S.No.	SUBJECT TITLE	GRADE	CREDITS	Month & Year Of Passing	S.No.	SUBJECT TITLE	GRADE	CREDITS	Month & Year Of Passing
ACCESSION CONTRACTOR STATES	I YEAR								
1 2	ENGLISH ENGINEERING PHYSICS	A S	4	06/15 06/15	9	ENGLISH LANGUAGE & COMMUNICATION SKILLS LAB	S	3	06/15
3 4	ENGINEERING CHEMISTRY MATHEMATICS-I	B S	4 5	06/15 06/15		C PROGRAMMING AND DATA STRUCTURES LAB ENGINEERING AND I.T. WORKSHOP	S S	4 4	06/15 06/15
5 6 7 8	REASONING AND APTITUDE C PROGRAMMING AND DATA STRUCTURES ENGINEERING DRAWING ENGG. PHYSICS LAB & ENGG. CHEMISTRY LAB	C A A S	3 5 5 3	06/15 06/15 06/15 06/15					
	II YEAR I SEMESTER					II YEAR II SEMESTER			
1 2 3 4 5 6 7 8 9	MATHEMATICS-II ENVIRONMENTAL SCIENCE ELECTRONIC DEVICES AND CIRCUITS SIGNALS AND SYSTEMS SWITCHING THEORY AND LOGIC DESIGN PRINCIPLES OF ELECTRICAL ENGINEERING ELECTRONIC DEVICES AND CIRCUITS LAB ELECTRICAL ENGINEERING LAB PROFESSIONAL ETHICS*	A B A C C C S B	3 3 3 3 2 2	12/15 12/15 12/15 12/15 12/15 12/15 12/15 12/15	5 6 7	MATHEMATICS-III BUSINESS MANAGEMENT ELECTRONIC CIRCUIT ANALYSIS PULSE AND DIGITAL CIRCUITS PROBABILITY THEORY AND STOCHASTIC PROCESS ELECTROMAGNETIC WAVES AND TRANSMISSION LINES ELECTRONIC CIRCUIT ANALYSIS LAB	Α	3 3 3 3 3 2	06/16 06/16 06/16 06/16 06/16 06/16
	III YEAR I SEMESTER				8	PULSE AND DIGITAL CIRCUITS LAB III YEAR II SEMESTER	В	2	06/16
1 2 3 4 5 6 7 8	ANALOG COMMUNICATIONS LINEAR IC APPLICATIONS DIGITAL IC APPLICATION CONTROL SYSTEMS COMPUTER ORGANIZATION TECHNICAL CASE STUDY IC APPLICATIONS LAB SOFT SKILLS LAB	ВССВСSS	3 3 3 3 3 2 2	12/16 12/16 12/16 12/16 12/16 12/16 12/16 12/16	1 2 3 4 5 6 7 8	ANTENNAS &WAVE PROPAGATIONS DIGITAL COMMUNICATIONS MICROPROCESSORS AND INTERFACING DIGITAL SIGNAL PROCESSING ELECTRONIC MEASUREMENTS AND INSTRUMENTATION VLSI DESIGN ANALOG & DIGITAL COMMUNICATIONS LAB MICROPROCESSORS AND INTERFACING LAB	D C B C C B S A	3 3 3 3 3 2 2	05/17 05/17 05/17 05/17 05/17 05/17 05/17 05/17
	IV YEAR I SEMESTER					IV YEAR II SEMESTER			
1 2 3 4	MICROWAVE ENGINEERING COMPUTER NETWORKS OPTICAL FIBER COMMUNICATION DIGITAL IMAGE PROCESSING	B B C	3 3 3 3	12/17 12/17 12/17 12/17	2	MOBILE COMMUNICATION & NETWORKS SATELLITE COMMUNICATIONS(ELECTIVES-III) DIGITAL DESIGN THROUGH VERILOG HDL(ELECTIVES-IV)	B C B	2000	04/18 04/18 04/18
5 6 7	EMBEDDED SYSTEMS(ELECTIVE-I) RADAR SYSTEMS(ELECTIVE-II) MICROWAVE AND OPTICAL COMMUNICATION LAB	C B S	3 3 2	12/17 12/17 12/17 12/17	4 5	COMPREHENSIVE EXAMINATION PROJECT WORK	S S	4 10	04/18 04/18
8 9	DIGITAL SIGNAL & IMAGE PROCESSING LAB PROJECT SEMINAR	SS	2 3	12/17 12/17					
						Ç 18			SAME PARTY OF THE

Number of Credits registered for: 180

Total Credits Acquired: 180

CGPA: 8.38

(*Audit Pass Course) (see overleaf for Instructions) CMM Issue Date: 07/08/2018



S. Sectarily
Controller of Examinations

AWARD OF LETTER GRADES FOR UG

LETTER GRADES	GRADE POINTS	MARKS RANGE
S	10	90-100
А	9	80-89
В	8	70-79
С	7	60-69
D	6	50-59
E	5	40-49
F	0	<40 (Fail)
ABS	0	1

AWARD OF LETTER GRADES FOR PG

LETTER GRADES	GRADE POINTS	MARKS RANGE
S	10	90-100
А	9	80-89
В	8	70-79
С	7 -	60-69
D	6	50-59
F	0	<50 (Fail)
ABS	0	

$$CGPA = \frac{\sum_{i=1}^{n} C_i GP_i}{\sum_{i=1}^{n} C_i}$$

Where

Ci - is the credits assigned to the course.

GPi- is the point corresponding to the grade obtained for each course.

 $\it n$ - is the number of all courses successfully cleared during the particular semester in case of GPA and during all semester in case of CGPA

ABS - Absent

Equivalent Percentage of Marks = 45+(CGPA-5)* 10

₩	Additional Controller - 01	Additional Controller - 02
VERIFIED	E. Jurun	D. y Pr



