



# NHD-C12832A1Z-FSW-FBW-3V3

## COG (Chip-On-Glass) Liquid Crystal Display Module

NHD- Newhaven Display C12832- 128 x 32 Pixels

A1Z- Model

F- Transflective

SW- Side White LED Backlight

F- FSTN Positive B- 6:00 Optimal View

W- Wide Temp

3V<sub>DD</sub>, 3V Backlight

**RoHS Compliant** 

#### Newhaven Display International, Inc.

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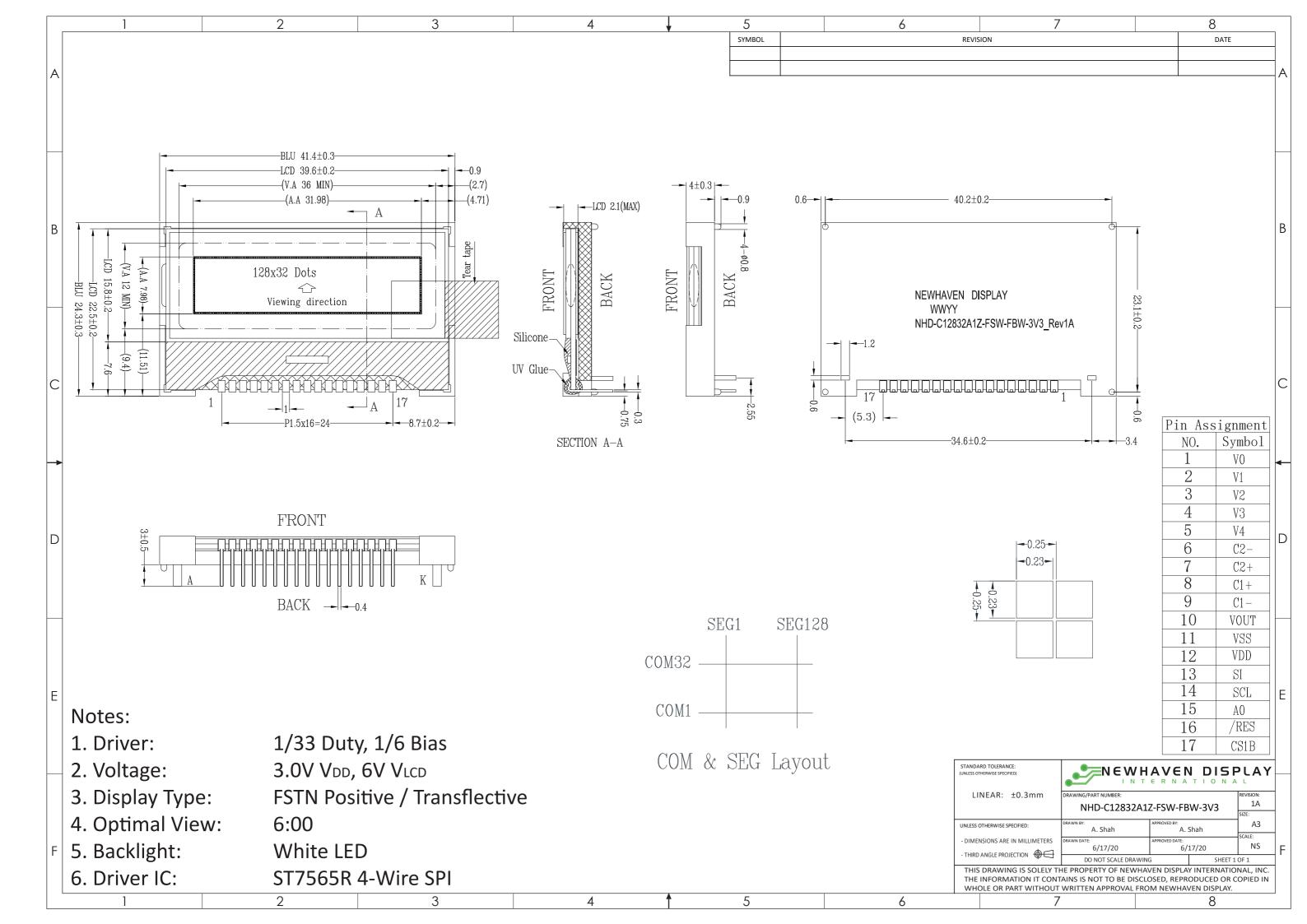
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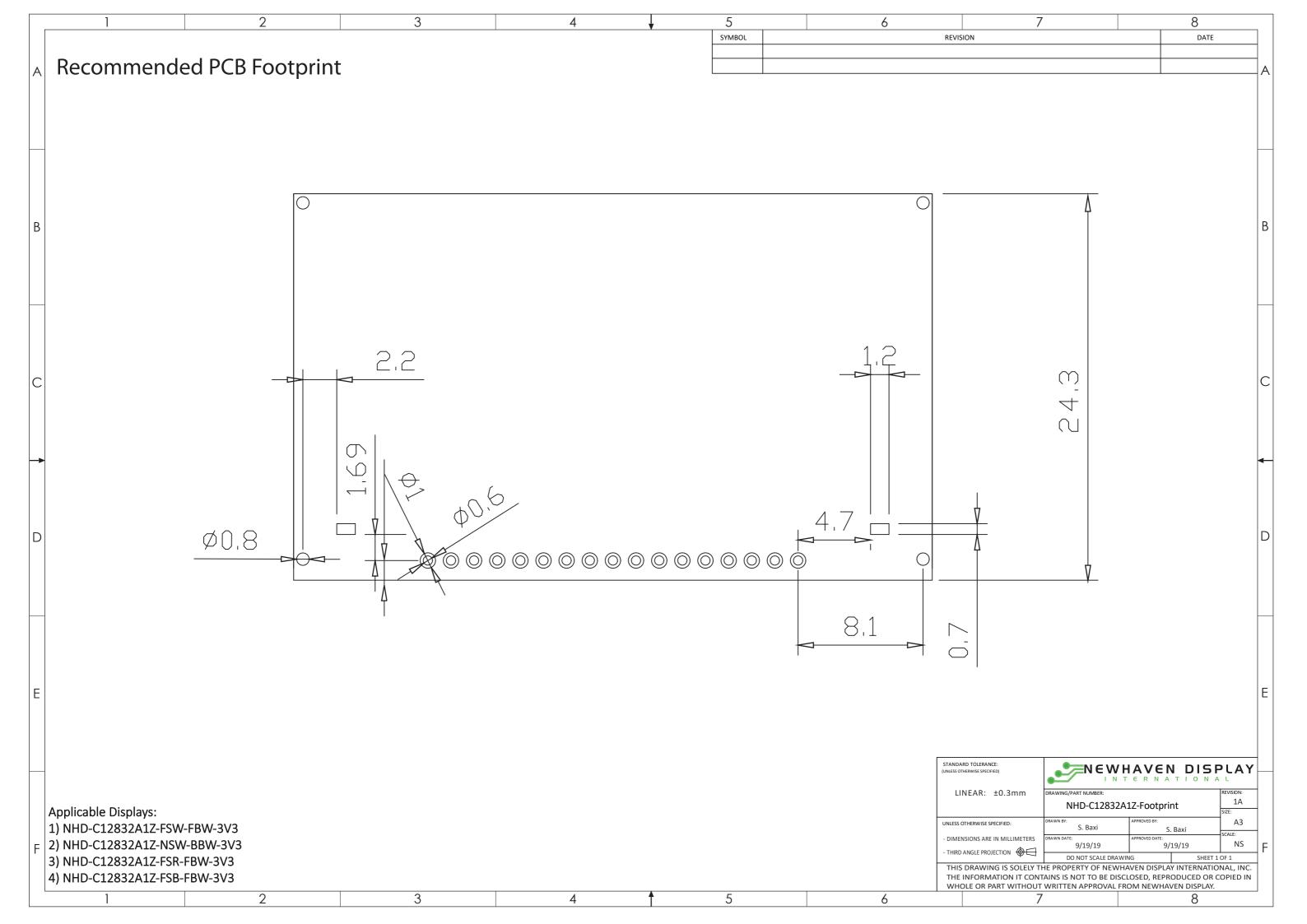
**Document Revision History** 

Revision	Date	Date Description				
0	11/12/08	Initial Release	-			
1	8/24/09	User guide reformat	BE			
2	10/13/09	Updated Electric Characteristic	MC			
3	5/6/13	Electrical and Optical characteristics updated. Pin	JN			
		description, wiring diagram, mechanical drawing page and				
		example initialization program updated.				
4	1/23/17	Mechanical Drawing, Electrical & Optical Char. Updated	SB			
5	7/5/19	Added PCB Footprint Drawing	AS			
6	1/30/20	Glass Panel Updated	SB			
7	6/17/20	Updated 2D Mechanical Drawing, Footprint Drawing for	AS			
		Correct Display & Quality Information				

#### **Functions and Features**

- 128 x 32 pixels
- 4-line SPI MPU interfaces
- Built-in ST7565R controller
- +3.0V power supply
- 1/33 duty cycle; 1/6 bias
- RoHS Compliant



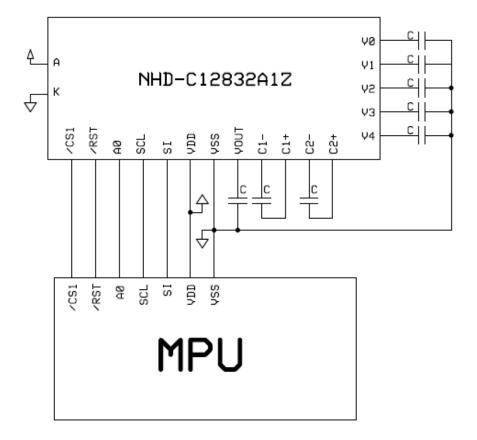


## **Pin Description and Wiring Diagram**

Pin No.	Symbol	External Connection	Function Description
1	V <sub>0</sub>	Power Supply	0.1μF – 1μF Capacitor to V <sub>SS</sub>
2	V <sub>1</sub>	Power Supply	0.1μF – 1μF Capacitor to V <sub>SS</sub>
3	V <sub>2</sub>	Power Supply	0.1μF – 1μF Capacitor to V <sub>SS</sub>
4	V <sub>3</sub>	Power Supply	0.1μF – 1μF Capacitor to V <sub>SS</sub>
5	$V_4$	Power Supply	0.1μF – 1μF Capacitor to V <sub>SS</sub>
6	C2-	Power Supply	Connect 1μF – 2.2μF Capacitor to C2+ (pin 7)
7	C2+	Power Supply	Connect 1μF – 2.2μF Capacitor to C2- (pin 6)
8	C1+	Power Supply	Connect 1μF – 2.2μF Capacitor to C1- (pin 9)
9	C1-	Power Supply	Connect 1μF – 2.2μF Capacitor to C1+ (pin 8)
10	V <sub>оит</sub>	Power Supply	Connect 1μF – 2.2μF Capacitor to VSS (pin 11)
11	$V_{SS}$	Power Supply	Ground
12	$V_{DD}$	Power Supply	Supply Voltage for LCD and Logic (+3V)
13	SI	MPU	Serial Data
14	SCL	MPU	Serial Clock
15	A0	MPU	Register Select. A0=0: Instruction, A0=1: Data
16	/RST	MPU	Active LOW Reset signal
17	/CS1	MPU	Active LOW Chip Select signal
Α	LED+	Power Supply	Backlight Anode(+3V)
K	LED-	Power Supply	Backlight Cathode (Ground)

**Recommended LCD connector:** 1.5mm pitch pins, solder directly into PCB **Backlight connector:** 1.2mm Wide pins, solder directly into PCB **Mates with:** ---

Recommended Breakout Board: NHD-PCB12832A1Z



#### **Electrical Characteristics**

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Temperature Range	T <sub>OP</sub>	Absolute Max	-20	-	+70	٥C
Storage Temperature Range	T <sub>ST</sub>	Absolute Max	-30	•	+80	٥C
Supply Voltage	$V_{DD}$	-	2.7	3.0	3.3	V
Supply Current	I <sub>DD</sub>	T <sub>OP</sub> =25°C, V <sub>DD</sub> =3.0V	0.1	0.4	1	mA
Supply for LCD (contrast)	$V_{LCD}$	T <sub>OP</sub> =25°C	5.8	6.0	6.2	V
"H" Level input	V <sub>IH</sub>	-	0.8 *V <sub>DD</sub>	-	$V_{DD}$	V
"L" Level input	V <sub>IL</sub>	-	V <sub>SS</sub>	-	0.2 * V <sub>DD</sub>	V
"H" Level output	Voh	-	0.8 * V <sub>DD</sub>	-	$V_{DD}$	V
"L" Level output	V <sub>OL</sub>	-	Vss	-	0.2 * V <sub>DD</sub>	V
Backlight supply voltage	$V_{LED}$	-	2.9	3.0	3.1	V
Backlight supply current	I <sub>LED</sub>	V <sub>LED</sub> =3.0V	10	30	36	mA

## **Optical Characteristics**

	Ite	em	Symbol	Condition	Min.	Тур.	Max.	Unit
Outine	Тор		φΥ+		-	20	-	0
Optimal	Bott	tom	φΥ-	CD > 2	-	40	-	0
Viewing	Left		θХ-	CR ≥ 2	-	40	-	0
Angles	Righ	nt	θХ+		-	40	-	0
Contrast Rat	Contrast Ratio			-	2	8	-	-
Dosponso T	ina a	Rise	T <sub>R</sub>	T - 25°C	-	200	250	ms
Response T	Fall		T <sub>F</sub>	$T_{OP} = 25^{\circ}C$	-	250	320	ms

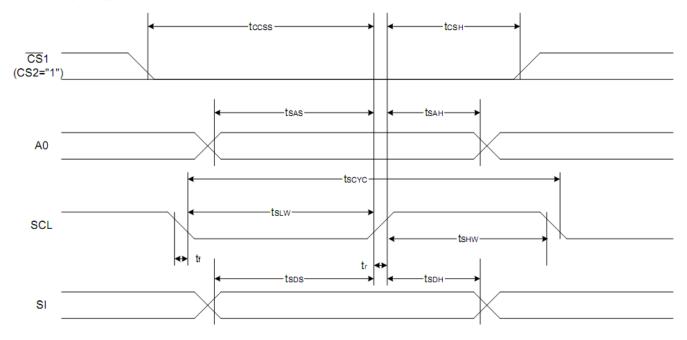
#### **Controller Information**

Built-in ST7565R controller.

Please download specification at <a href="http://www.newhavendisplay.com/app">http://www.newhavendisplay.com/app</a> notes/ST7565R.pdf

## **Timing Characteristics**

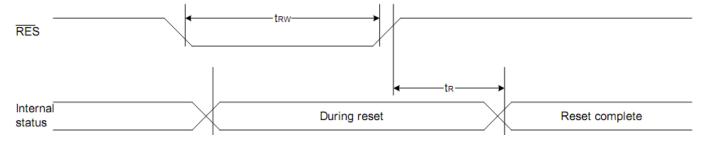
#### The 4-line SPI Interface



Item	Signal	Symbol	Condition	Rati	Units	
item	Signal	Syllibol	Condition	Min.	Max.	Units
4-line SPI Clock Period		Tscyc		50	_	
SCL "H" pulse width	SCL	Tshw		25	_	
SCL "L" pulse width		Tslw		25	_	
Address setup time	A0	Tsas		20	_	
Address hold time	AU	Tsah		10	_	ns
Data setup time	SI	Tsds		20	_	
Data hold time	31	Tsdh		10	_	
CS-SCL time	CS	Tcss		20	_	
CS-SCL time	03	Tcsh		40	_	

<sup>\*1</sup> The input signal rise and fall time (tr, tf) are specified at 15 ns or less. \*2 All timing is specified using 20% and 80% of VDD as the standard.

#### **Reset Timing**



## **Table of Commands**

0	Command Code											Function		
Command	Α0	/RD	/WR	D7	D6	D5	D4	D3	D2	D1	D0	Function		
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON		
(2) Display start line set	0	1	0 0 1 Display start address		Sets the display RAM display start line address									
(3) Page address set	0	1	0	1	0	1	1	Р	age	addre	SS	Sets the display RAM page address		
(4) Column address set upper bit Column address set lower bit	0	1	0	0	0	0	1	co Le	column address Least significant		olumn address		ess cant	Sets the most significant 4 bits of the display RAM column address. Sets the least significant 4 bits of the display RAM column address.
(5) Status read	0	0	1		Sta	itus		0	0	0	0	Reads the status data		
(6) Display data write	1	1	0					W	rite d	ata		Writes to the display RAM		
(7) Display data read	1	0	1					Re	ead d	ata		Reads from the display RAM		
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse		
(9) Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0	Sets the LCD display normal/ reverse 0: normal, 1: reverse		
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	Display all points 0: normal display 1: all points ON		
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0 1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565R)		
(12) Read-modify-write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0		
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write		
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset		
(15) Common output mode select	0	1	0	1	1	0	0	0 1	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction		
(16) Power control set	0	1	0	0	0	1	0	1	0	perat mode	_	Select internal power supply operating mode		
(17) V <sub>0</sub> voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Res	sistor	ratio	Select internal resistor ratio(Rb/Ra) mode		
(18) Electronic volume mode set	0	1	0	1	0	0	0	0	0	0	1	Set the V <sub>0</sub> output voltage		
Electronic volume register set				0	0	E	lectro	onic v	volun	ne val	ue	electronic volume register		
(10) Sleep made set	0	4		1	0	1	0	1	1	0	0	0: Sleep mode, 1: Normal mode		
(19) Sleep mode set	0	1	0	*	*	*	*	*	*	0	1 0			
(20) Booster ratio set	0	1	0	1	1	1	1	1	0	0	0	select booster ratio 00: 2x,3x,4x		
(20) Dooster ratio set	U		U	0	0	0	0	0	0 step-up value			01: 5x 11: 6x		
(21) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation		
(22) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command		

## **Example Initialization Program**

```
void data_out(unsigned char i) //Data Output Serial Interface
       unsigned int n;
       CS = 0;
       A0 = 1;
       for(n=0; n<8; n++){
 i <<=1;
       SCL = 0;
       P1 = i;
       delay(2);
       SCL = 1;
       CS = 1;
}
void comm_out(unsigned char j) //Command Output Serial Interface
       unsigned int n;
       CS = 0;
       A0 = 0;
       for(n=0; n<8; n++){
 j <<=1;
       SCL = 0;
       P1 = j;
       delay(2);
       SCL = 1;
       CS = 1;
}
    ***************
      Initialization For controller
void init_LCD()
comm_out(0xA0);
comm_out(0xAE);
comm out(0xC0);
comm_out(0xA2);
comm_out(0x2F);
comm out(0x21);
comm_out(0x81);
comm_out(0x3F);
```

## **Quality Information**

Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage	+80°C, 96hrs	2
	temperature for a long time.		
Low Temperature storage	Endurance test applying the low storage	-30°C, 96hrs	1,2
	temperature for a long time.		
High Temperature	Endurance test applying the electric stress	+70°C, 96hrs	2
Operation	(voltage & current) and the high thermal		
	stress for a long time.		
Low Temperature	Endurance test applying the electric stress	-20°C, 96hrs	1,2
Operation	(voltage & current) and the low thermal		
	stress for a long time.		
High Temperature /	Endurance test applying the electric stress	+40°C, 90% RH, 96hrs	1,2
<b>Humidity Operation</b>	(voltage & current) and the high thermal		
	with high humidity stress for a long time.		
Thermal Shock resistance	Endurance test applying the electric stress	-20°C, 30min -> 70°C, 60min	
	(voltage & current) during a cycle of low	= 1 cycle	
	and high thermal stress.	For 20 cycles	
Vibration test	Endurance test applying vibration to	10-50Hz, 5G amplitude.	3
	simulate transportation and use.	30min in each of 3 directions X, Y, Z	
Static electricity test	Endurance test applying electric static	Air: ±8kV 150pF/330Ω, 5 Times	
	discharge.	Contact: ±4kV 150pF/330Ω, 5 Times	

Note 1: No condensation to be observed.

Note 2: Conducted after 4 hours of storage at 25°C, 0%RH.

Note 3: Test performed on product itself, not inside a container.

### **Precautions for using LCDs/LCMs**

See Precautions at <a href="https://www.newhavendisplay.com/specs/precautions.pdf">www.newhavendisplay.com/specs/precautions.pdf</a>

### **Warranty Information and Terms & Conditions**

http://www.newhavendisplay.com/index.php?main\_page=terms