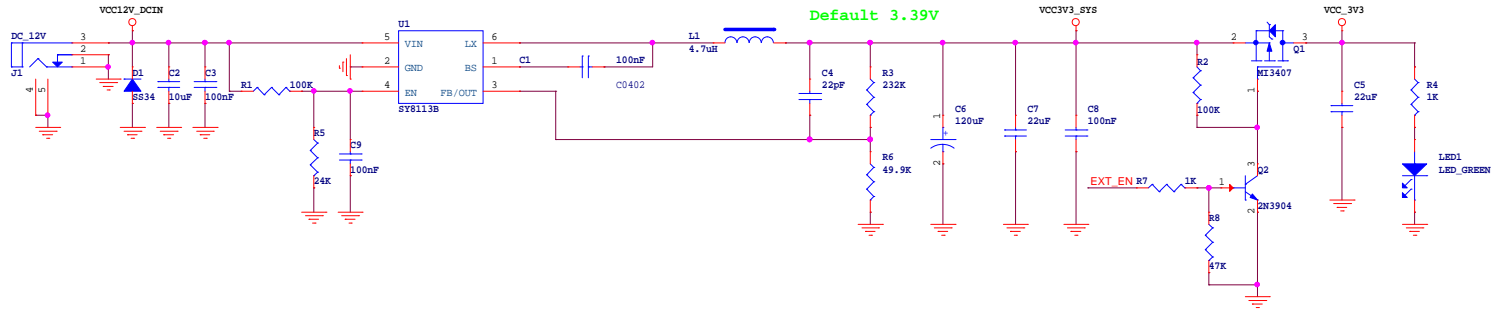
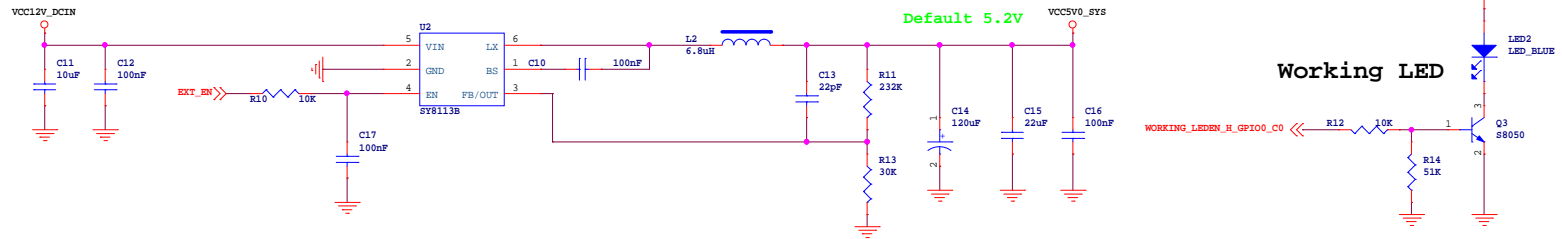


12V/3A DCIN

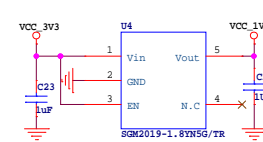
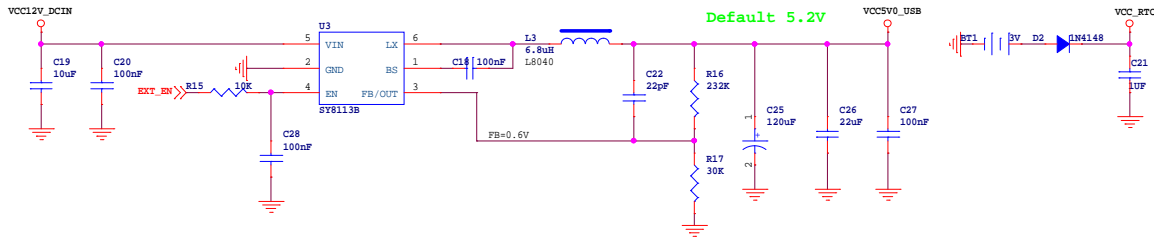
VCC3V3_SYS



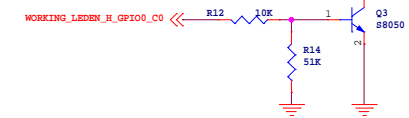
VCC5V0_SYS

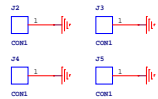


VCC5V0_USB

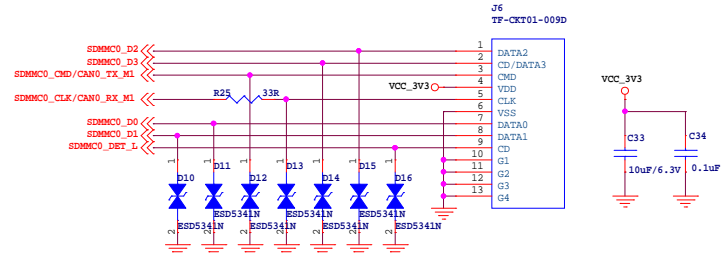
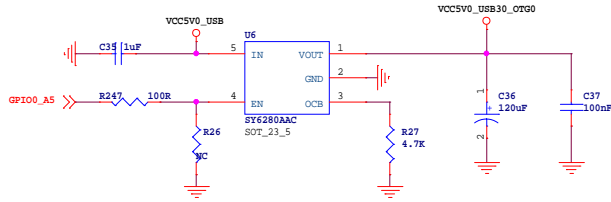
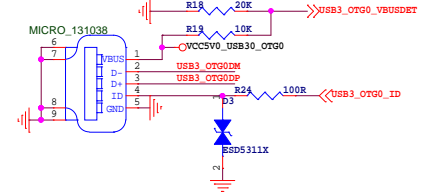
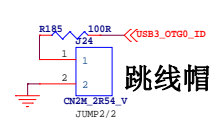
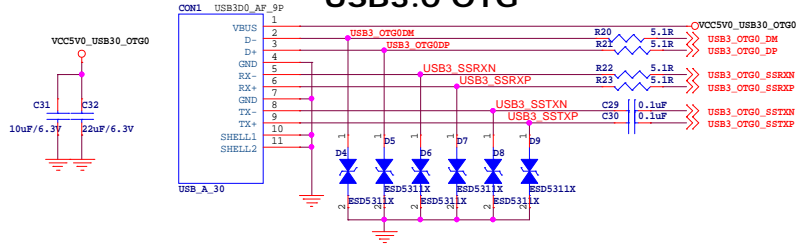


Working LED

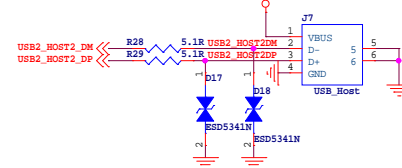




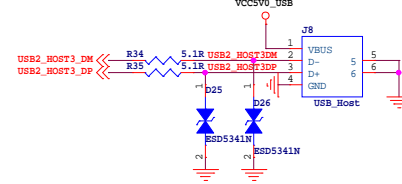
USB3.0 OTG



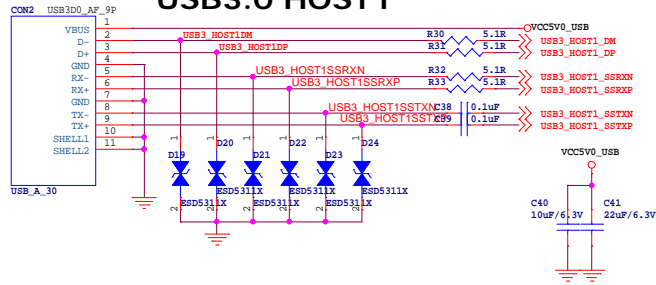
USB2.0 HOST2

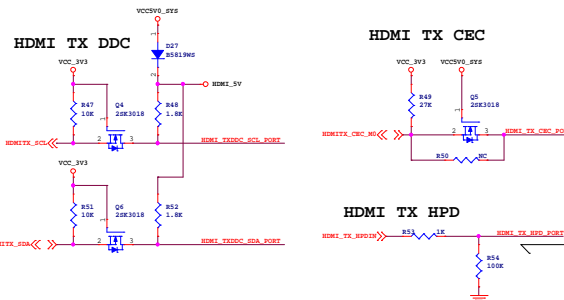
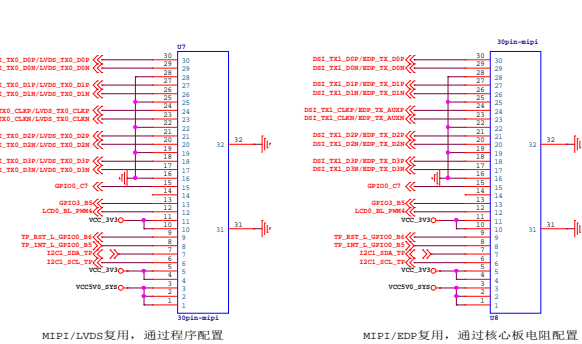


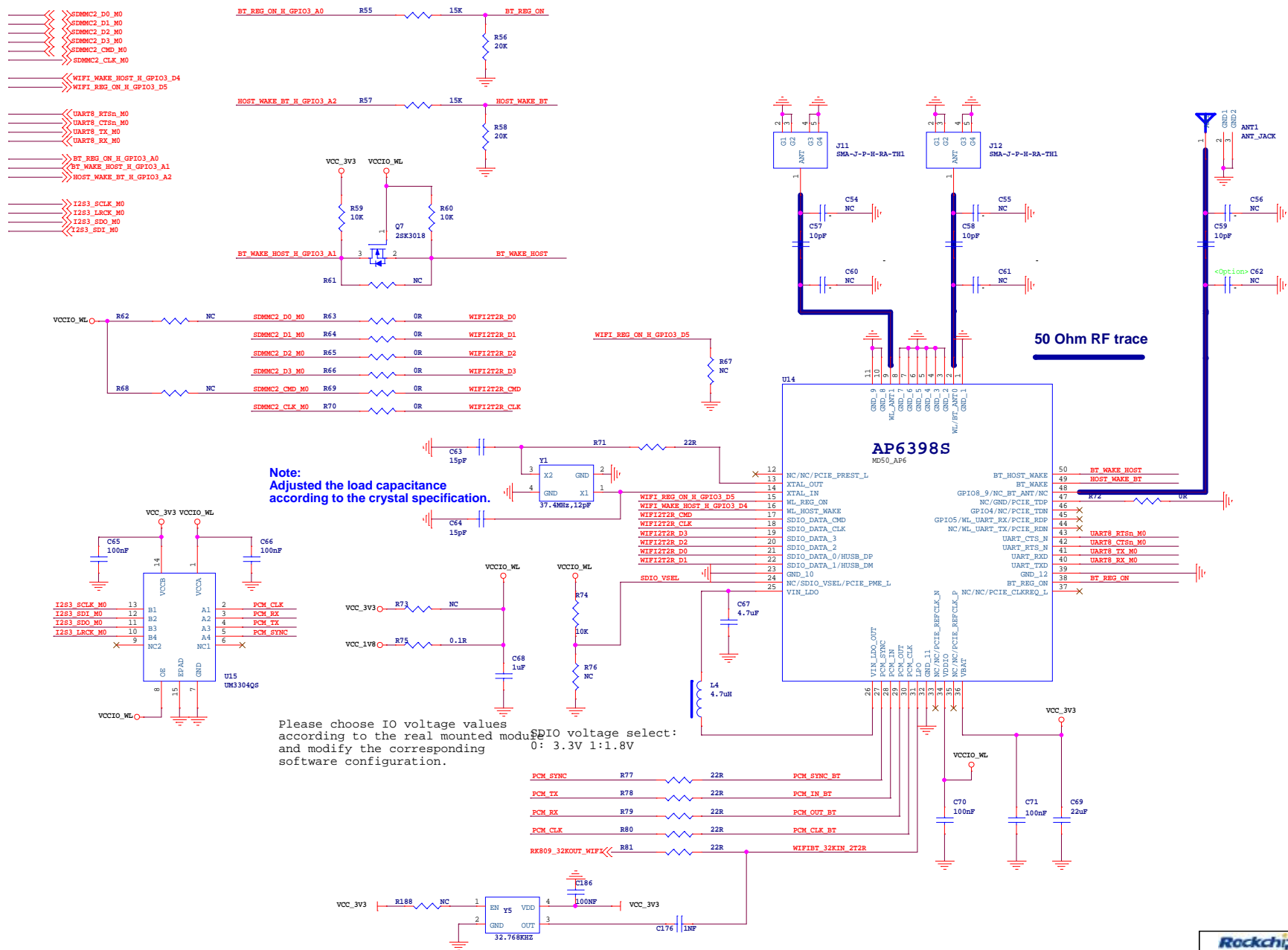
USB2.0 HOST3



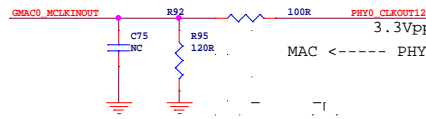
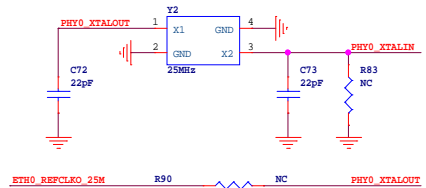
USB3.0 HOST1



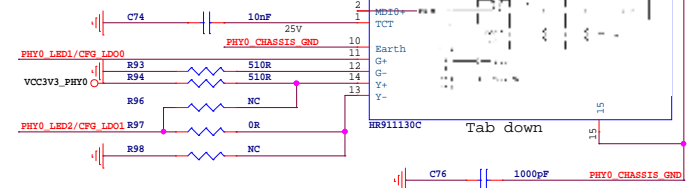
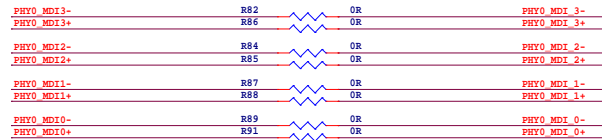




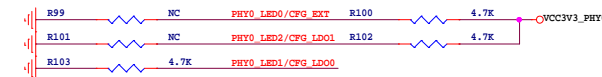
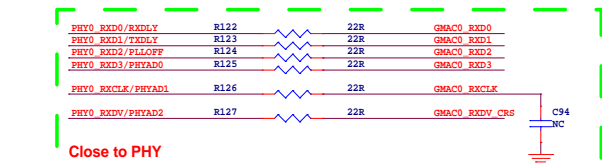
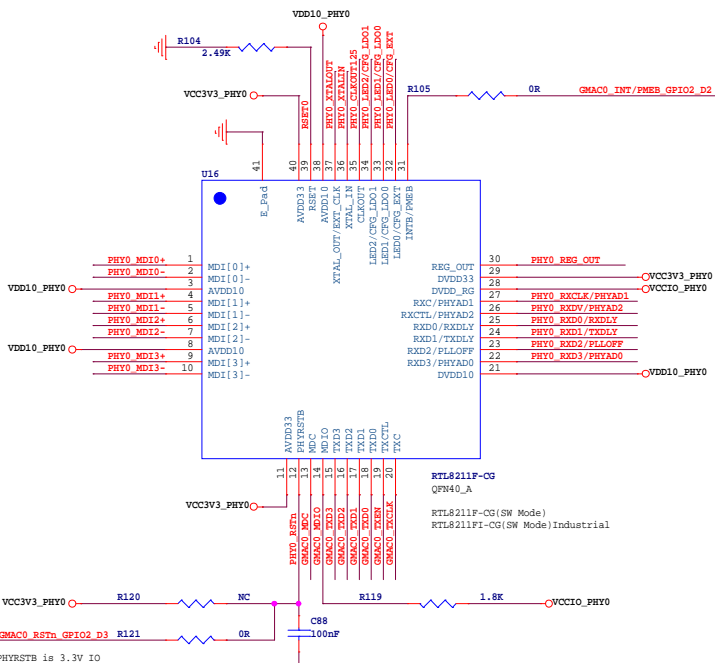
>>> GMACO_TXD0
 >>> GMACO_TXD1
 >>> GMACO_TXD2
 >>> GMACO_TXD3
 >>> GMACO_TXEN
 >>> GMACO_TXCLK
 >>> GMACO_RXD0
 >>> GMACO_RXD1
 >>> GMACO_RXD2
 >>> GMACO_RXD3
 >>> GMACO_RXDV_CRS
 >>> GMACO_RXCLK
 >>> ETH0_REFCLK0_25M
 >>> GMACO_MCLKINOUT
 >>> GMACO_MDC
 >>> GMACO_MDIO
 >>> GMACO_RSTn_GPIO2_D3
 >>> GMACO_INT/PMBE_GPIO2_D2



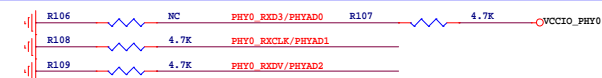
VCCIO_PHY0=3.3V	DNP	22R	Default
VCCIO_PHY0=1.8V	120R	100R	



HR915330CE: -40~85



VCC_PHY0_IO Voltage Config



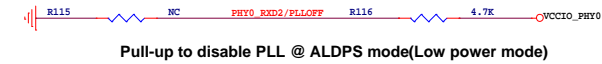
PHY Address Config



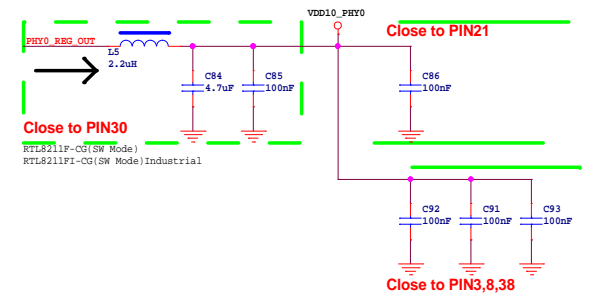
Pull-up for additional 2ns delay to RXC for data latching



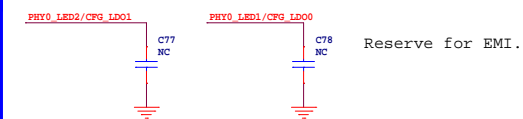
Pull-up for additional 2ns delay to TXC for data latching



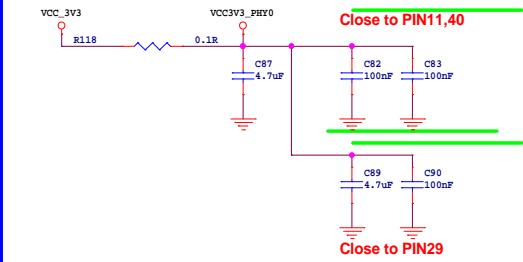
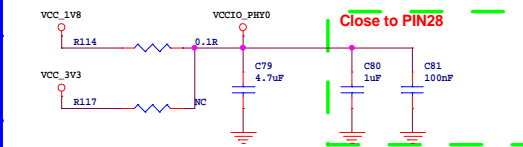
Pull-up to disable PLL @ ALDPS mode(Low power mode)



BGM11 Power Source	CPG_EXT	CPG_LDO1[1:0]
External 3.3V	1'b1	2'b00
External 1.8V (default)	1'b1	2'b10
Internal 1.8V	1'b0	2'b10

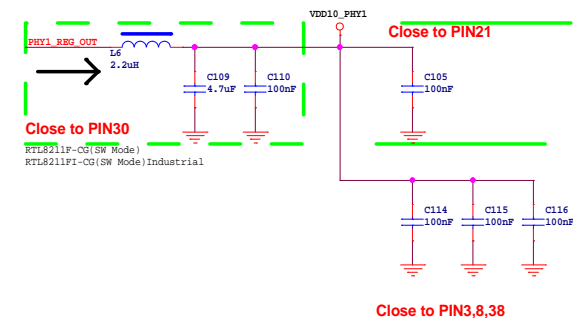
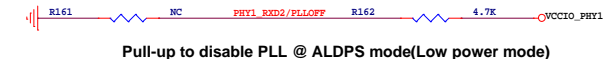
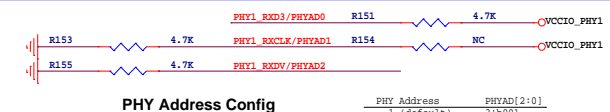
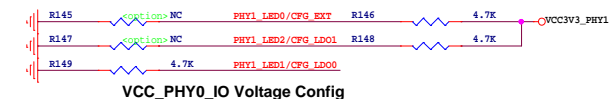
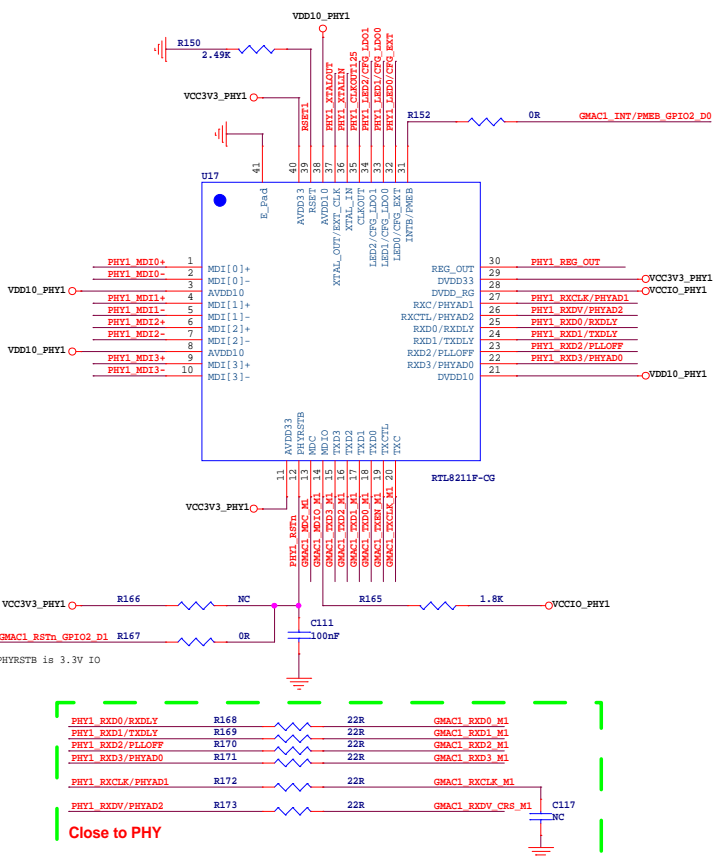
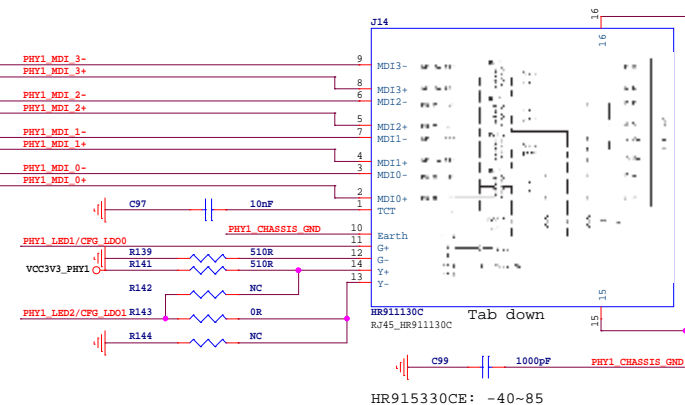
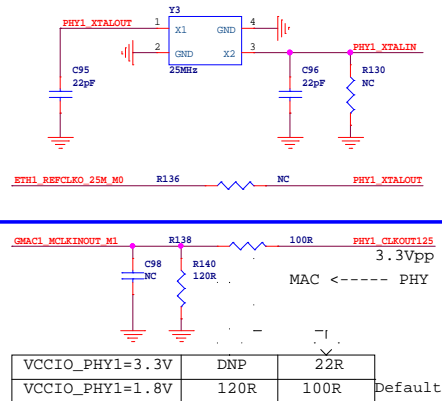


Reserve for EMI.

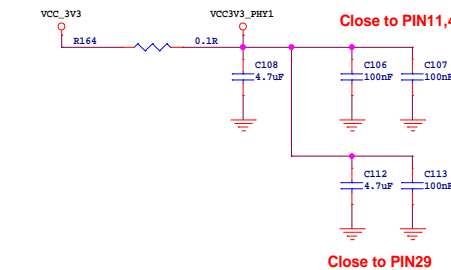
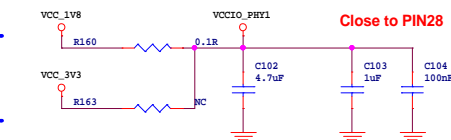
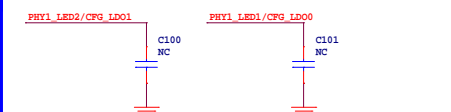


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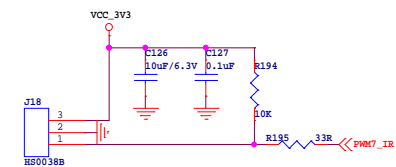
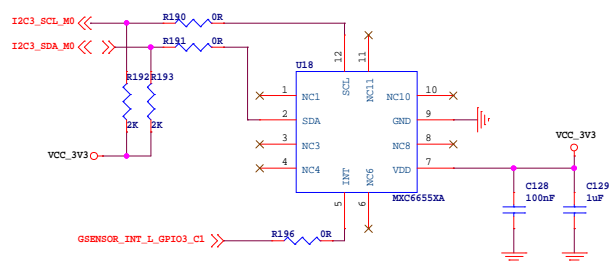
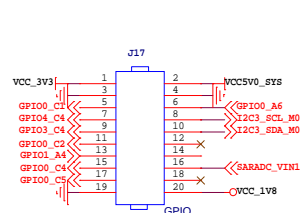
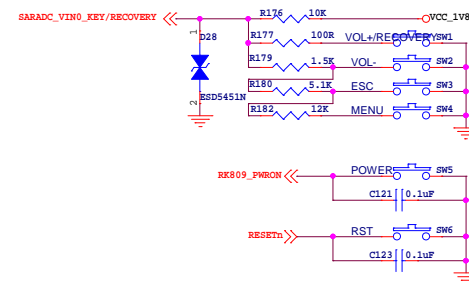
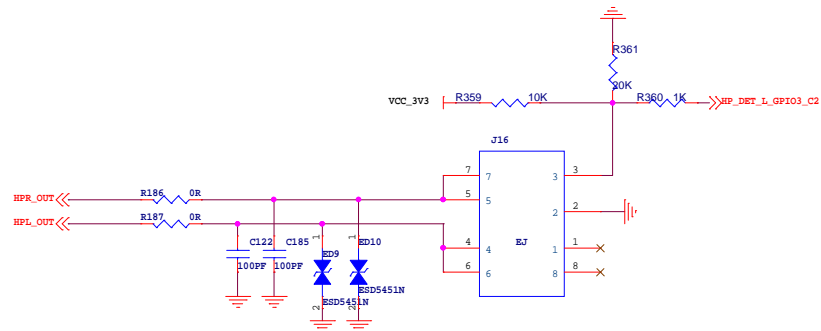
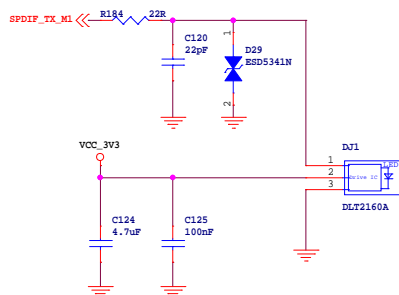
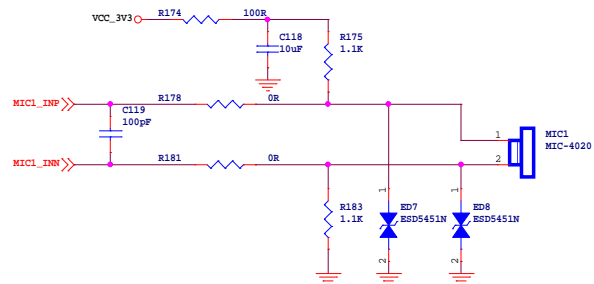
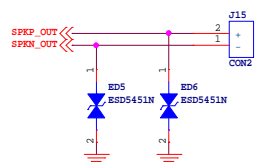
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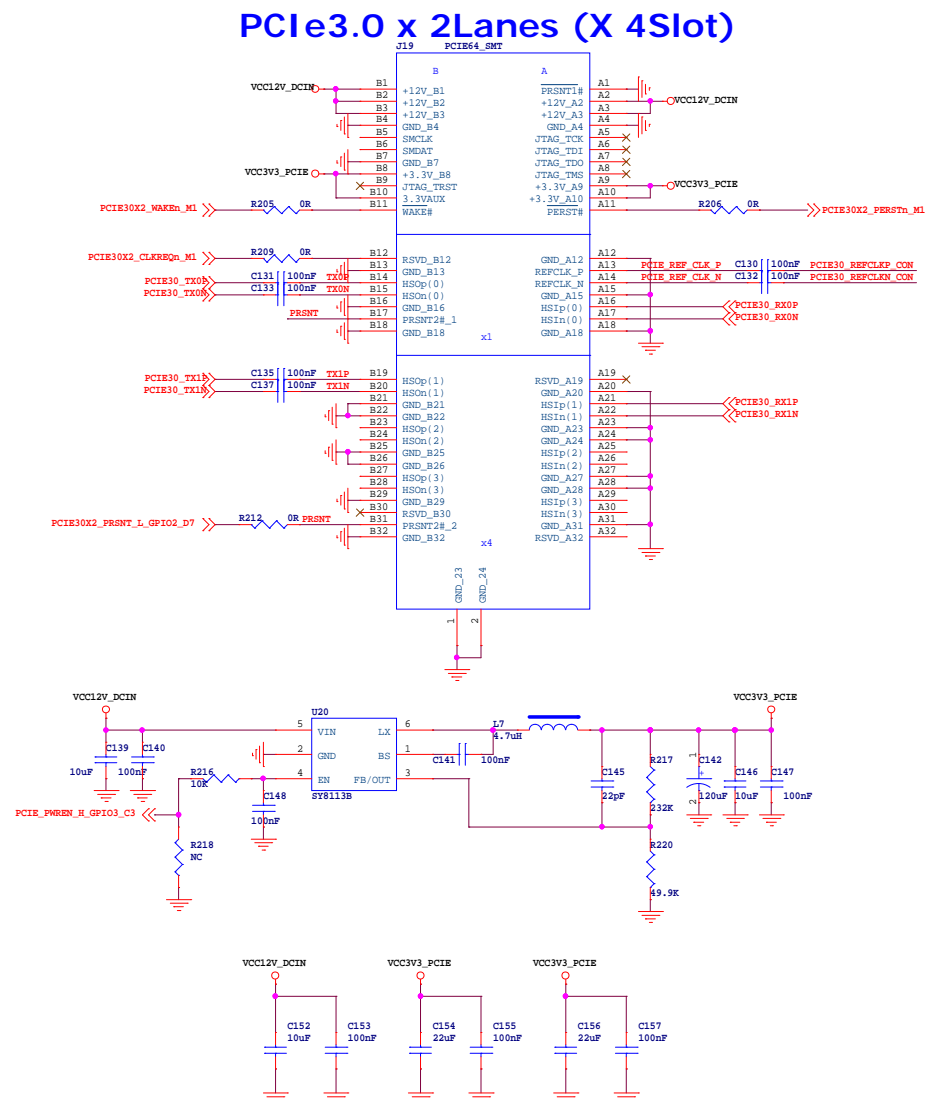
RGMII Power Source	CFG_EXT	CFG_LDO[1:0]	
External 3.3V	1'b1	2'b00	
External 1.8V (default)	1'b1	2'b10	
Internal 1.8V	1'b0	2'b10	

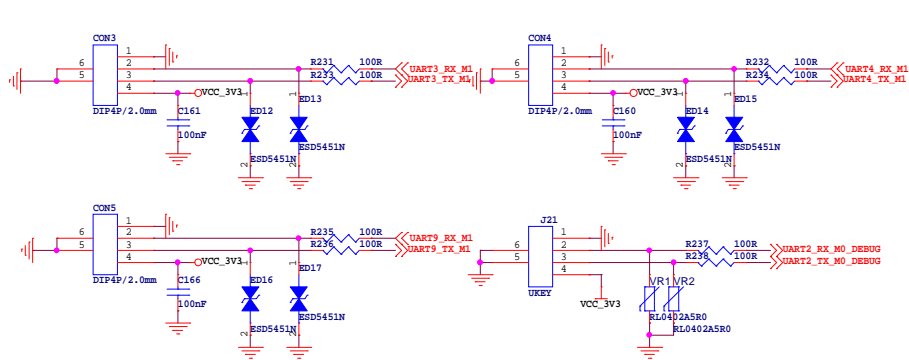


Note: 8ohm/1.3W
Speaker Output

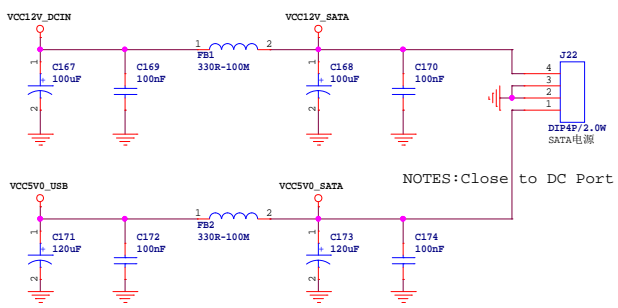
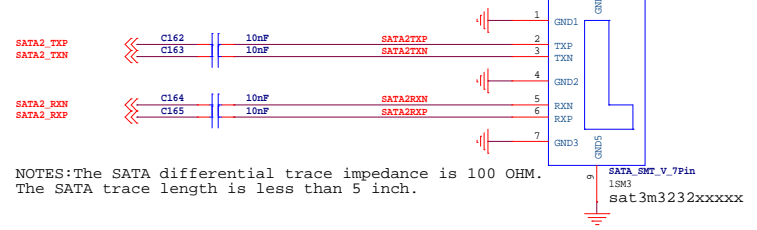


If board target trace impedance is 50ohm
then $R = 475\text{ohm}$ providing an IREF of 2.32 mA .
The output current (IOH) is $6 * \text{IREF}$.
 $6 \times 2.32 \times 50 = 696\text{mV}$



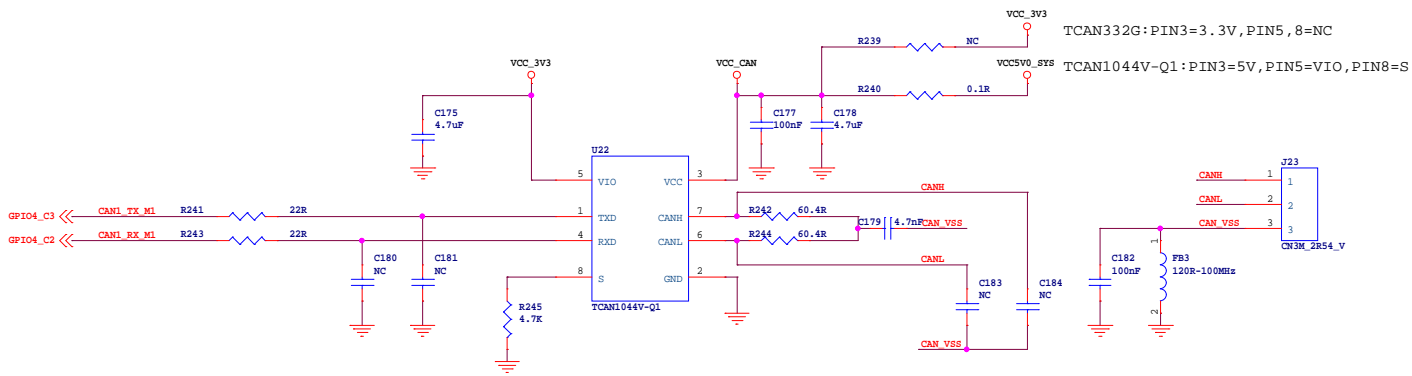


SATA3.0 Port2




CAN1_M1

CAN transceiver IC



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