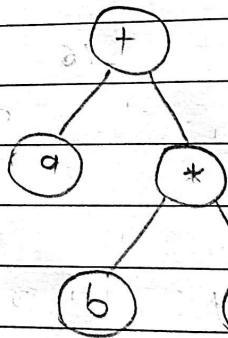


Syntax Analysis.

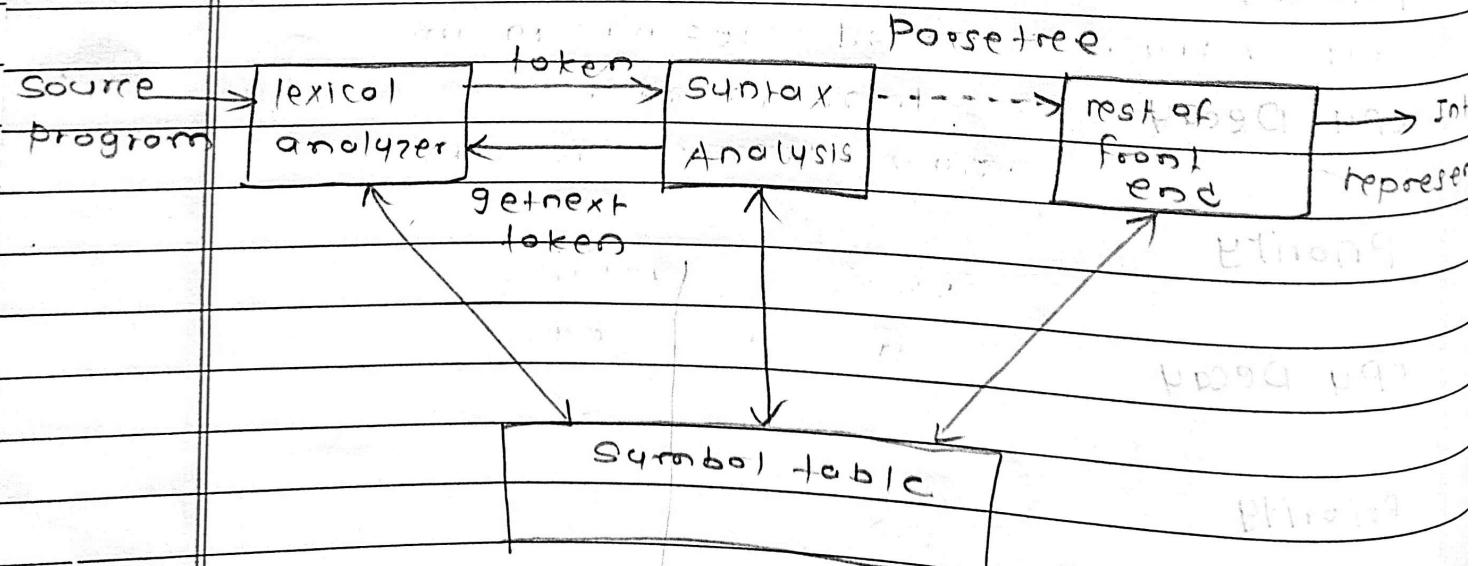
- 1) Syntax Analysis is second phase of compilation
- 2) Syntax Analysis is performed by Syntax analyzer.
- 3) Syntax Analyzer also called as parser.
- 4) Syntax Analysis phase checks grammatical structure & syntactical structure by using context free grammar.
- 5) Input to Syntax Analysis is stream of tokens generated by lexical analysis phase.
- 6) Output of Syntax Analysis is parse tree or a syntax tree.
- 7) The main goal is to create a hierarchy of source code hierarchy shows grammatical structure of source code.

Ex -

$a + b * c$



Structure of parser



D	D	M	M	Y	Y	Y	Y
---	---	---	---	---	---	---	---

In the above diagram,

parser takes input from lexical analyzer
form in tokens & for getting next token parser
requests the lexical analyzer for next token. Then
parser uses context free grammar & create the
parse tree of those ~~available~~ tokens. Similarly
from parse tree intermediate code will generated
in next some phases.

out to get to target specific dialect like C etc.

There are 3 types of parser. ① Universal

- ① Top Down - Starts building parse from root node
etc to the leaf or hidden node. Top down parsing
generates parse tree with out without backtracking.
- ② Bottom Up - Starts building parse tree from
child of leaf node to root node if consist of
types like shift reduce parser etc.

functions: reading symbols parser, symbol table etc.

Tasks performed by the parser symbol & table

- ① It verifies the structural grammatical structure of
- ② It constructs parse tree
- ③ It reports errors if any
- ④ It performs error recovery to say fair

Advantages - Improved code generation

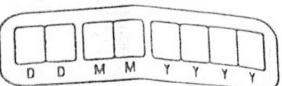
Structural validation

Easier semantic Analysis

Disadvantages - Complexity

Reduce performance

Limited error recovery.



Shift Reduce Parsing

- 1) Shift reduce parsing is form of Bottom Up parsing in which a stack holds grammar symbols & input buffer holds rest of input string to be parsed.
- 2) Handle - Handle always appears at top of the stack.
- 3) Shift reduce parsing is a process of reducing a string to start symbol of grammar.

A string $\xrightarrow{\text{Reduce to}}$ starting symbol

- 4) Shift reduce parsing always performs two action shift & reduce that's why it is known as shift reduce parsing.
- 5) We use \$ to make bottom of stack & also right end of input.
- 6) The parser repeats the cycle until it has detected an error or until stack contains the start symbol & input is empty.

STACK	INPUT
\$	\$

- 7) Example id* id

D	D	M	M	Y

JNP47

ACTION

STACK

\$ id * id \$ shift
\$ id * id \$ reduce by $f \rightarrow id$
\$ id * id \$ reduce by $T \rightarrow F$
\$ T id \$ shift
\$ T * \$ reduce by $F \rightarrow id$
\$ T * id \$ reduce by $T \rightarrow T^*F$
\$ T \$ reduce by $E \rightarrow T$
\$ E accept

Q8 There are 4 main & important possible actions in shift reduce parser
i) shift or reduce or accept or error.

Q9 Shift action - next input symbol is pushed to stack at top of stack.

Q10 Reduce action - replaces the handle within stack with non terminal.

Q11 Accept - Announces successful completion of parsing

Q12 Error - Discoveres syntax error & calls the error recovery routine. The routine begins by popping

Q13 The use of stack in shift reduce parsing is identified as handle will always eventually be at the top of stack & never inside the stack.

Advantages -

- ① Shift reduce parsing is efficient & can handle a wide range of context free grammars.
- ② It can parse a large variety of programming languages.
- ③ It is capable for handling both left & right recursive grammar.
- ④ This parser is efficient in terms of memory usage.

Disadvantages -

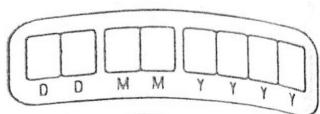
- ① Shift reduce has a limited lookahead and it will miss some syntax errors.
- ② It may also generate false-positive shift-reduce conflicts.

D	D	M	M	Y	Y	Y
---	---	---	---	---	---	---

S Attributed

L Attributed.

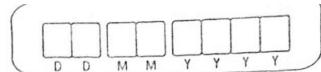
- ① Associated with synthesized attributes. Associated with both inherited & synthesized.
- ② Bottom up parsing is used for starting grammar. Top down parsing is used for starting grammar.
- ③ Semantic Rules are defined in bottom up parsing. Semantic Rules are defined in top down parsing.
- ④ Attributes calculated during reduction. Attributes calculated during parsing.
- ⑤ Occurs in LR(1) & LALR(1) parsing. Occurs in LL(1) parsing.
- ⑥ Attribute values determined by production rules. Attribute values determined by semantic Rules.
- ⑦ Implemented using shift reduce parsing. Implemented using recursive descent parsing.
- ⑧ Inherited from parents. Synthesized from children.
- ⑨ No circular Dependencies. Circular dependencies possible.
- ⑩ Simple to implement. Complex to implement.



advantages

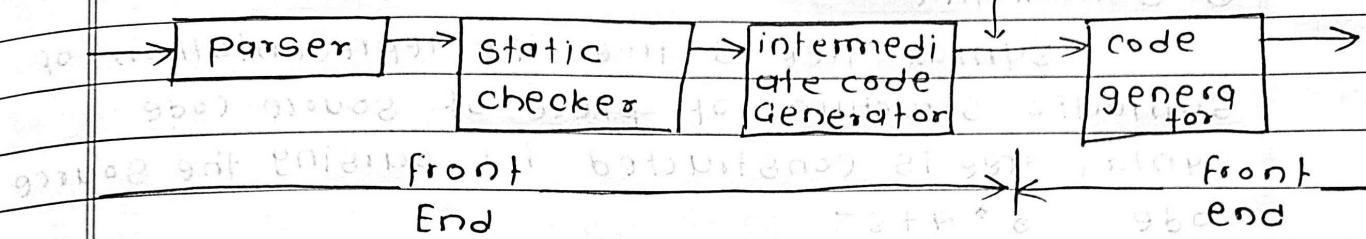
disadvantages

- | | | |
|------|--------------------------------|------------------------------|
| (11) | less expressive | more expressive |
| (12) | No need of symbol table | Symbol table is required |
| (13) | Suitable for simpler grammar | Suitable for complex grammar |
| (14) | Does not handle left recursion | can handle left recursion |



Intermediate Code Generation

Intermediate code



① Intermediate code generation is fourth phase compilation.

② The front end translates a source program into an intermediate representation, from which back end generates target code

③ Parsing, static checking & intermediate code generation are done sequentially

④ Intermediate code is lips betn high level language

from machine level language

or data structures in memory

⑤ Intermediate code generator takes inputs as syntax tree & generate intermediate code

⑥ The compiler cannot generate machine code directly. Therefore first it convert source program into intermediate code

⑦ There are 3 ways of intermediate representation

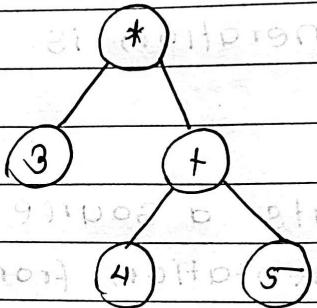
1) Syntax tree

2) Postfix Notation

3) Three address code

① Syntax tree -

Syntax tree is tree like representation of syntactic structure of piece of source code. Syntax tree is constructed by parsing the source code.



② Postfix Notation -

Postfix notation are linearized representation of syntax trees resulting.

Ex. a b c uminus * b c uminus + + assi

③ Three address code

Three address codes are type of intermediate code which is easily generated & easily converted into machine code.

General Representation

a, b, c operands

operator

Example

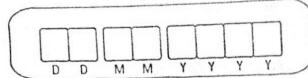
$$d = a * b - c$$



$$t_1 = a * b$$

$$t_2 = t_1 - c$$

$$d = t_2$$



Advantages - to optimize code by using

platform-independent code.

Simplify coding by using

Disadvantages - add extra steps to compilation.

Additional memory usage

Increased complexity.

Implementation of global variables is difficult.

Annotated parse tree

The parse tree containing value of attribute at each node for given input string is called annotated syntax parse tree.

It is a data structure used to represent syntactical structure of program with additional information.

Additional information contains datatype, attribute & name

Annotated parse tree is useful at time of compilation because it include both syntax & semantic of program.

Features - High level specification

Hides implementation details.

Explicit order of evaluation.

Production	Semantic Rules
$S \rightarrow E \text{ return}$	$\text{print}(E.\text{val})$
$E \rightarrow E_1 + T$	$E.\text{val} = E_1.\text{val} + T.\text{val}$
$E \rightarrow T$	$E.\text{val} = T.\text{val}$
$T \rightarrow T_1 + F$	$T.\text{val} = T_1.\text{val} + F.\text{val}$
$T \rightarrow F$	$T.\text{val} = F.\text{val}$
$F \rightarrow (E)$	$F.\text{val} = E.\text{val}$
$F \rightarrow \text{digit}$	$F.\text{val} = \text{digit}$

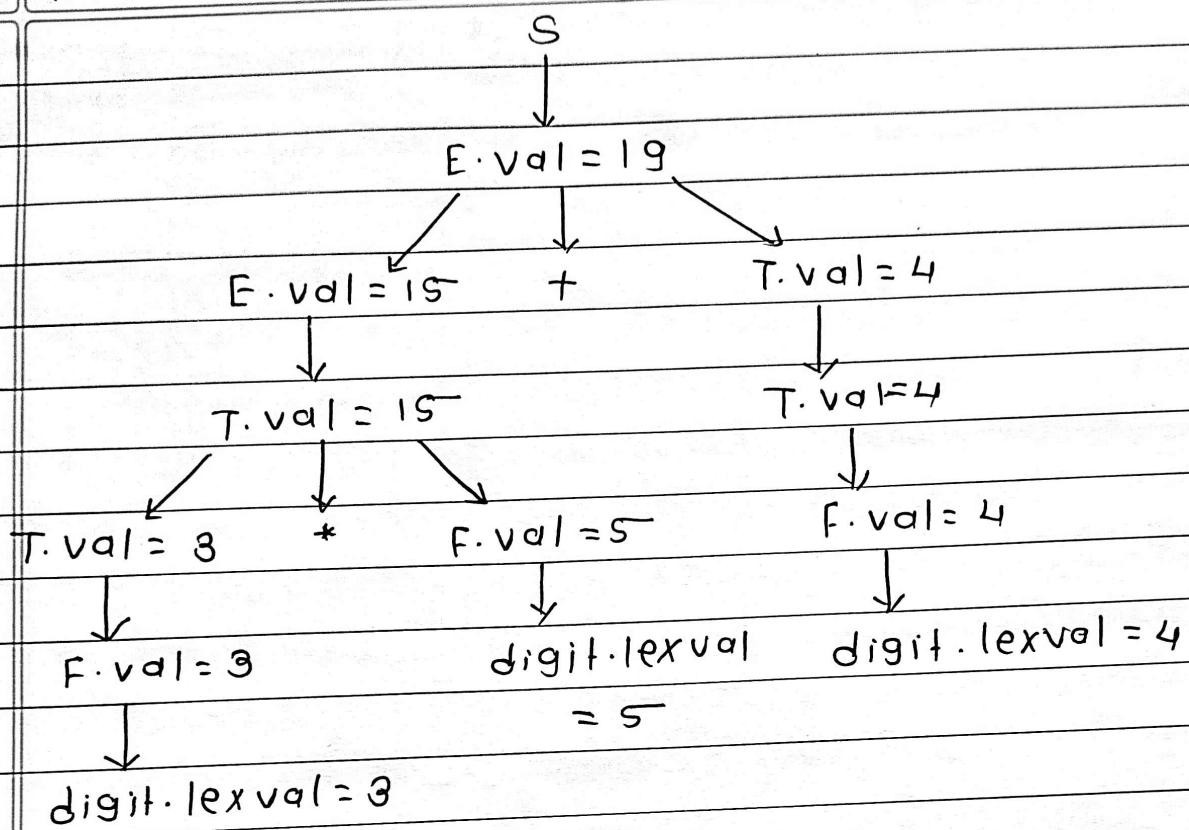
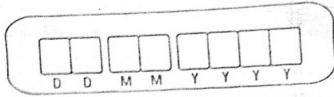
Where E = Represents Expression

T = term

F = Factor

digit = numeric value.

Annotated parse tree.



5. Code Optimization.

Q. 1 What are sources of optimization?

→ ① Code optimization is a process of eliminating redundancies from program & improving performance of program.

② It improves code by making it consume less resources & delivers high speed. (Reduce space & time)

✓ Following are the code optimization transformation

① Elimination of common subexpression

② Copy propagation

③ Dead code elimination

④ Code Motion

⑤ Induction Variables & Reduction in strength.

↳ 1) Elimination of common subexpression.

✓ An occurrence of Expression E is called as common sub-expression if

① E was previously computed by compiler

② & values of variable in E have not changed

by previous computation.

✓ We can avoid recomputing expression E if we can use previously computed value.

Example: Removal of common subexpression

~~$a = b + c$ and $b = a - d$ so $a = b + c$ is common subexpression~~

~~$b = a - d \rightarrow d = a - b$ so $d = a - b$ is common subexpression~~

~~$c = b + c \rightarrow c = a - b$ so $c = a - b$ is common subexpression~~

$c = a$

The common subexpression $b + c$ is eliminated in 3rd step as its computed already in a & value of expressions are not changed.

↳ 2) Copy propagation

✓ Copy propagation means eliminate a statement with new transformation.

(2)

(3)

Assignment in form of $f := g$ is called as copy statements or copies for short.

The idea behind the copy propagation is transformation is to use g for f , whenever possible after the copy statement.

Copy propagation means use one variable instead of another.

Example.

$$\begin{array}{l} x = a \text{ // copy stmt. } \quad x = a \\ y = x + b \quad \longrightarrow \quad y = a + b \\ z = x + c \quad \quad \quad z = a + c \end{array}$$

Here variable x is replaced / eliminated by a

By Dead code elimination

Dead code is nothing but a useless code of program.

A variable is useful in the program if its value is used in program frequently otherwise it is an dead code.

A main idea behind this is dead code - means the statements that computes values that never get used.

Programmer is unlikely to introduce the dead code intentionally, it may appears as the result of previous transformations.

Example - $i = 0;$

```
if (i = 1) {  
    a = b + 5  
}
```

Here if statement is dead code because this condition will never get satisfied.

4) Code Motion

- ✓ code motion is loop optimization technique
- ✓ Code Motion moves the code outside of the loop.
- ✓ In loops especially in inner loops, programs tend to spend the bulk of their time. The running time of program may be improved if number of instructions in inner loop is decreased.

Example

```
while(i<10){  
    X = y + 2;  
    i = i + 1;  
}
```

the expression $x = y + 2$ is loop invariant expression computation. it is not based on loop. every time x value will be same & no of statement will reduced.

5) Induction Variable Elimination / Reduction in

strength. (Induction variable is loop based variable in)

- ✓ Another important optimization is to find induction variable in loops & optimize their computation.

A variable x is said to be induction variable if there is positive or negative constant c such that each time x is assigned & its value is increased by c .

The transformation of replacing a complex operation with simpler one is known as strength reduction. For ex. multiplication is replaced by addition.

Example

```
i = 1;  
while (i<10){  
    t = t + 4;  
    i = i + 1;  
}
```

(4)

(5)

In this above example i is induction variable
 loop will execute 9 times & value of t increased
 by 4 & multiplication operation $t = i * 4$ is replaced
 with $t = t + 4$ & induction variable is removed.

Q.2 What is peephole Optimization?

- ✓ ① Peephole optimization is an optimization tech. performed on a small set of instructions/program
- ✓ ② the small set is known as the peephole or window
- ✓ ③ Peephole optimization involves changing the small set of instructions to an equivalent set that has better performance.
- ✓ ④ simple but yet effective & improves the target code
- ✓ ⑤ Peephole optimization it is done by examining a sliding window of target code instructions & replace them with peephole shortens of faster sequence.
- ✓ ⑥ It can be done directly after intermediate code generation to improve intermediate Representation.

Characteristics of peephole optimization -

- 1) Eliminate Redundant loads & stores.
- 2) Eliminate Unreachable code
- 3) Flow of control optimization
- 4) Algebraic simplification & Reduction in strength
- 5) Use of Machine idioms.

① Eliminate Redundant loads & stores.

- ✓ In target program we can delete store instruction because whenever it is executed, the first instruction will ensure that value of a is already loaded in Register R_0

LD R_0, a -- ①

ST a, R_0 -- ②

If we see the instruction sequence (5)

LD R0, a
ST a, R0

in a target program, we can delete the store instruction because whenever it is executed, the first instruction will ensure

that the value of a has already been loaded into register R0. Note that if the store instruction had a label, we could not be sure that the first instruction is always executed before the second, so we

could not remove the store instruction. Put another way, the two instructions have to be in the same basic block for this transformation to be safe.

Redundant loads and stores of this nature would not be generated by the simple code generation algorithm of the previous section. However, a naive code generation algorithm like the one in Section 8.1.3 would generate redundant sequences such as these

if store instruction had a label, we could not remove sure that first instruction is always executed before second. So we could not remove store instruction. So we can put them in same basic block.

② Eliminate Unreachable Code -

Another opportunity for peephole optimization is removal of unreachable instruction. An unlabeled instruction immediately following an unconditional jump may be removed [Peephole optimization also used to eliminate jumps over jumps].

Example

if debug == 1 goto L1

goto L2

L1: print debug information

L2: /* Dead code */

debug is replaced with 0 then performing constant propagation code will be

if 0 != 1 goto L2

print debug information

L2: /* Dead code */

③ Flow of control optimization

Intermediate code generation algorithm frequently produce jumps to jump, jumps to conditional jumps or conditional jumps to jumps. These unnecessary jumps can be eliminated

Example

goto L1

goto L2

....

L1: goto L2

→ L1: goto L2

....

L2: a = b

....

L2: a = b

There are two jumps, we removed one jump & also we can optimize the code by removing label L1.

(6)

④ Algebraic specification & Reduction in strength.

- ✓ algebraic identities can also be used by pre-processor optimizer to eliminate three address statement

$$\text{Ex. } x = x + 0 \longrightarrow x = x * 1$$

also reduction in strength transformation can be applied & replace complex/expensive operation by simple/cheaper operation

$$\begin{array}{l} \text{EX. } x^2 = x * x \\ \text{complex } 2 * x = x + x \\ \text{expensive } x * 2 = x \ll 1 \\ \text{simple/cheap } x / 2 = x \gg 1 \end{array}$$

⑤ Use of Machine Idioms.

- ✓ Target machine have hw instructions to implement certain operations efficiently.

Detecting which operation should perform the use of the instructions can reduce execution time.

- ✓ Some machines have auto-increment & auto-decrement addressing mode.

Ex.

$$\text{auto increment } i = i + 1 \longrightarrow \text{INC } i$$

$$\text{Decrement } i = i - 1 \longrightarrow \text{DEC } i$$