Evaluación y selección de las compuertas (en un archivo PDF). Esto debe contener los datasheet de las compuertas empleadas y una cotización de cuánto saldría el circuito completo.

74ls32 compuerta OR

cantidad:62

puertas por encapsulado: 4;

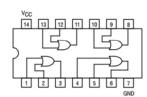
total, encapsulados: 16

precio 8US;

total:8US; entre 1 a 24 unidades;

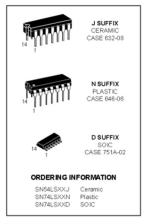


QUAD 2-INPUT OR GATE



SN54/74LS32

QUAD 2-INPUT OR GATE
LOW POWER SCHOTTKY



GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Тур	Max	Unit
Vcc	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
TA	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
Юн	Output Current — High	54,74			-0.4	mA
lo _L	Output Current — Low	54 74			4.0 8.0	mA

74ls04 compuerta NOT

cantidad:52

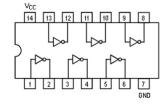
compuertas puertas por encapsulado: 6;

total, encapsulados: 9

precio 8US; entre 1 a 24 unidades;

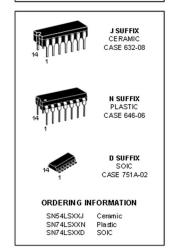


HEX INVERTER



SN54/74LS04

HEX INVERTER LOW POWER SCHOTTKY



GUARANTEED OPERATING RANGES

GUARANTEED OPERATING RANGES						
Symbol	Parameter		Min	Тур	Max	Unit
Vcc	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	٧
TA	Operating Ambient Temperature Range	54 74	–55 0	25 25	125 70	*C
Юн	Output Current — High	54,74			-0.4	mΑ
ЮL	Output Current — Low	54 74			4.0 8.0	mA

74ls08 compuerta AND

cantidad:17

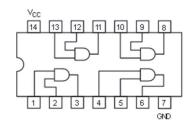
compuertas puertas por encapsulado: 4;

total, encapsulados: 34;

precio 5.96US; entre 25 a 99 unidades;

SN74LS08

Quad 2-Input AND Gate





ON Semiconductor Formerly a Division of Motorola

http://onsemi.com

LOW POWER SCHOTTKY

GUARANTEED OPERATING RANGES

Symbol	Parameter	Min	Тур	Max	Unit
Vcc	Supply Voltage	4.75	5.0	5.25	٧
TA	Operating Ambient Temperature Range	0	25	70	*C
Гон	Output Current - High			-0.4	mΑ
loL	Output Current - Low			8.0	mA





ORDERING INFORMATION

Device	Package	Shipping
SN74LS08N	14 Pin DIP	2000 Units/Box
SN74LS08D	14 Pin	2500/Tape & Reel

74LS86 compuerta XOR exclusiva

cantidad:8

compuertas por encapsulado: 4;

total, encapsulados:2

precio 8US; entre 1 a 24 unidades;

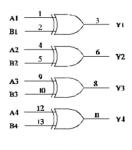
SL74LS86

Quad 2-Input Exclusive OR Gate

This device contains four independent 2-input Exclusive-OR gates. It performs the Boolean functions Y=A \oplus B=AB+AB in positive logic.



LOGIC DIAGRAM



PIN 14 = V_{CC} PIN 7 = GND

PIN ASSIGNMENT

AI [:•	14	. ∴ cc
ві [2	13	B4
Yı 🛚	3	12	Δ4
A2 [4		¥4
R2 [5	10	Bã
¥2 [6	9	- 43
CINID [7	δ	¥3

FUNCTION TABLE

Inputs		Output
A	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L



Total, en dólares: 29.96