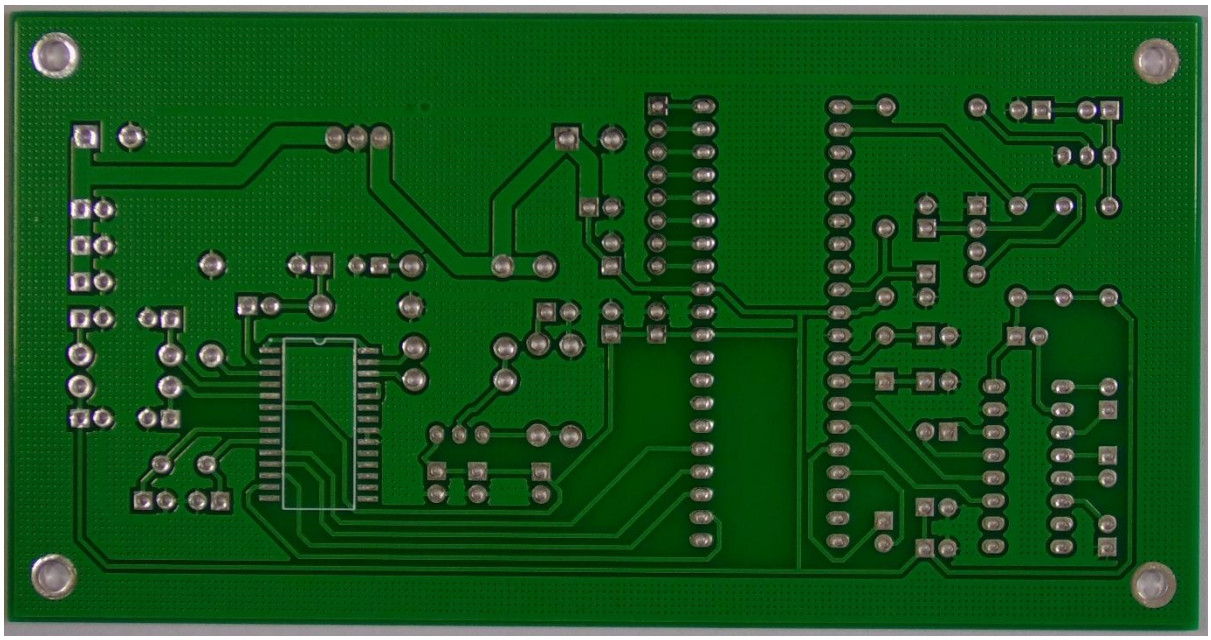
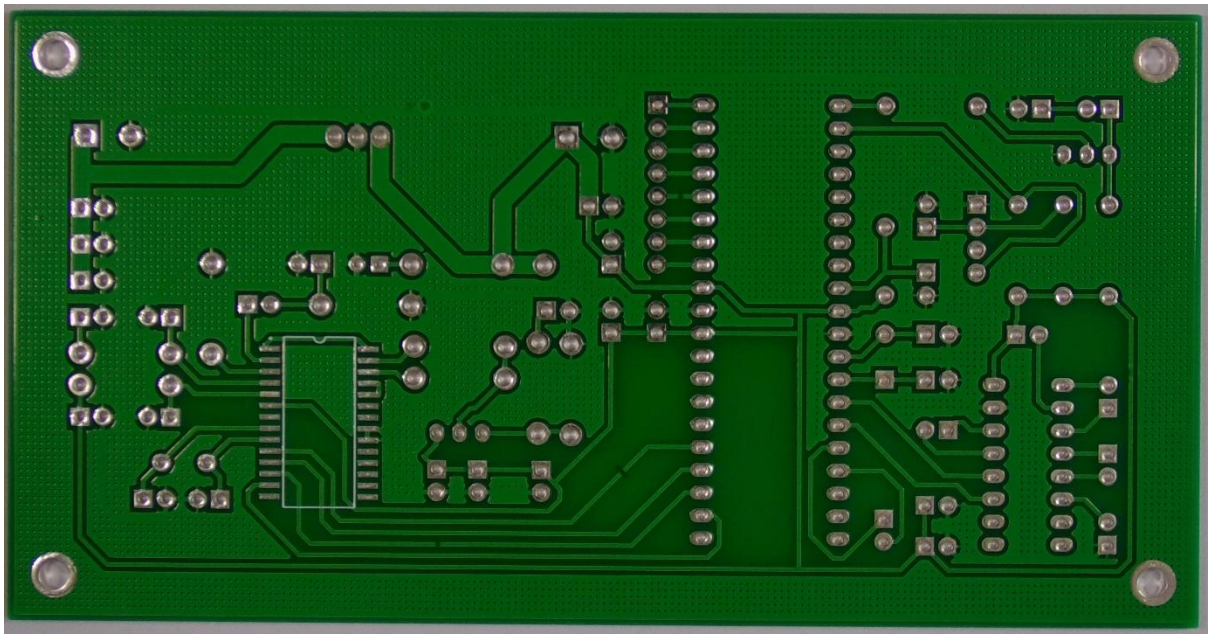
INPUT 1(OPEN CIRCUIT)

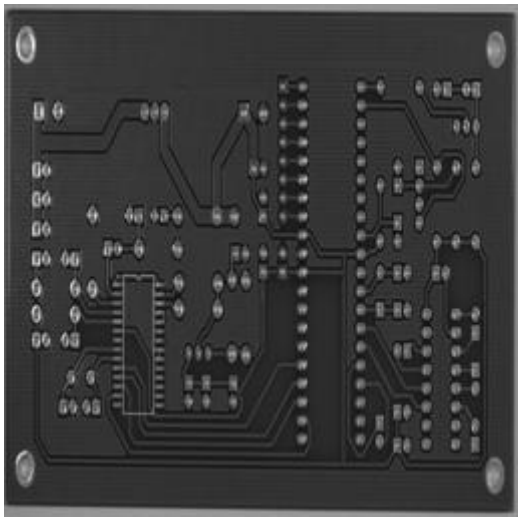
DEFECTLESS PCB:



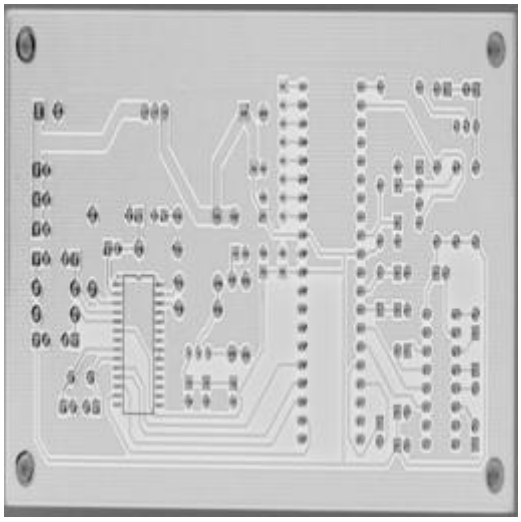
DEFECT PCB:



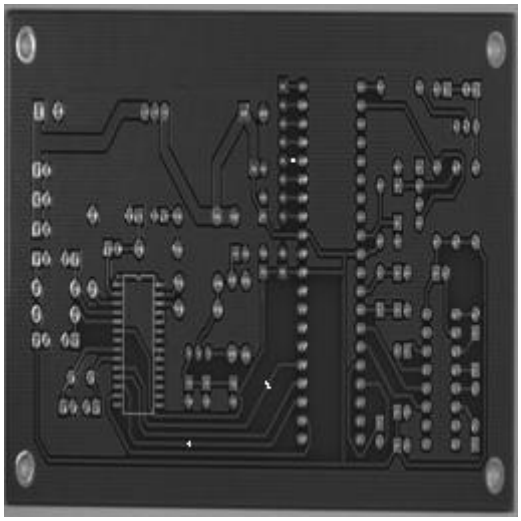
GRAYSCALE:



INVERT:

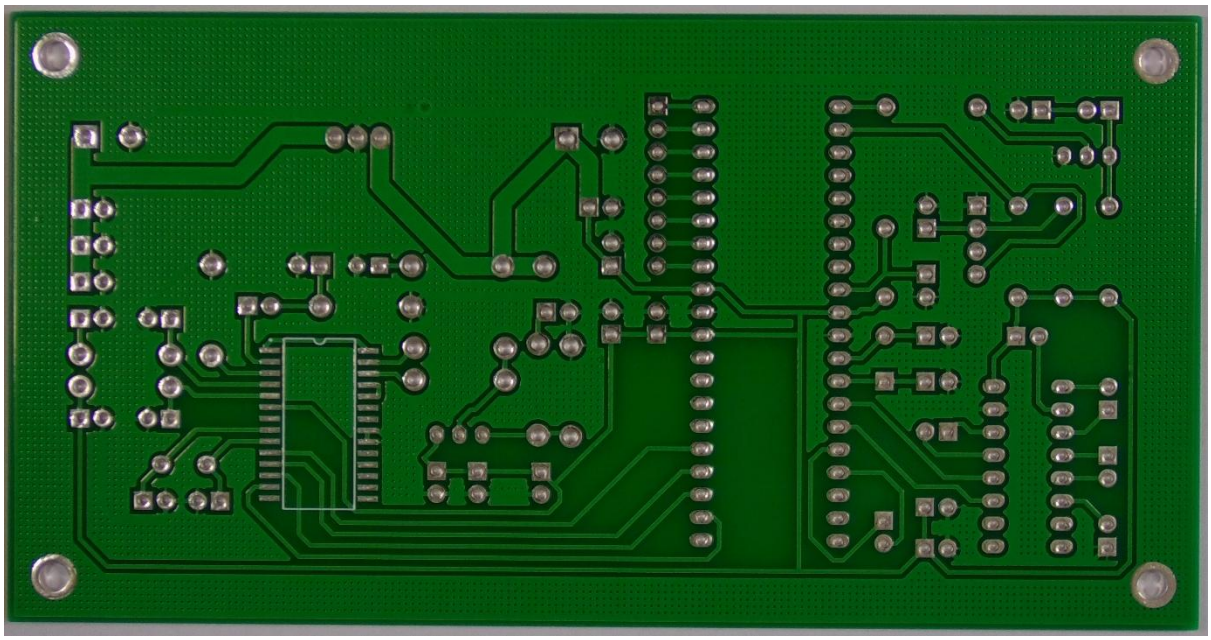


DEFECTED AREA:

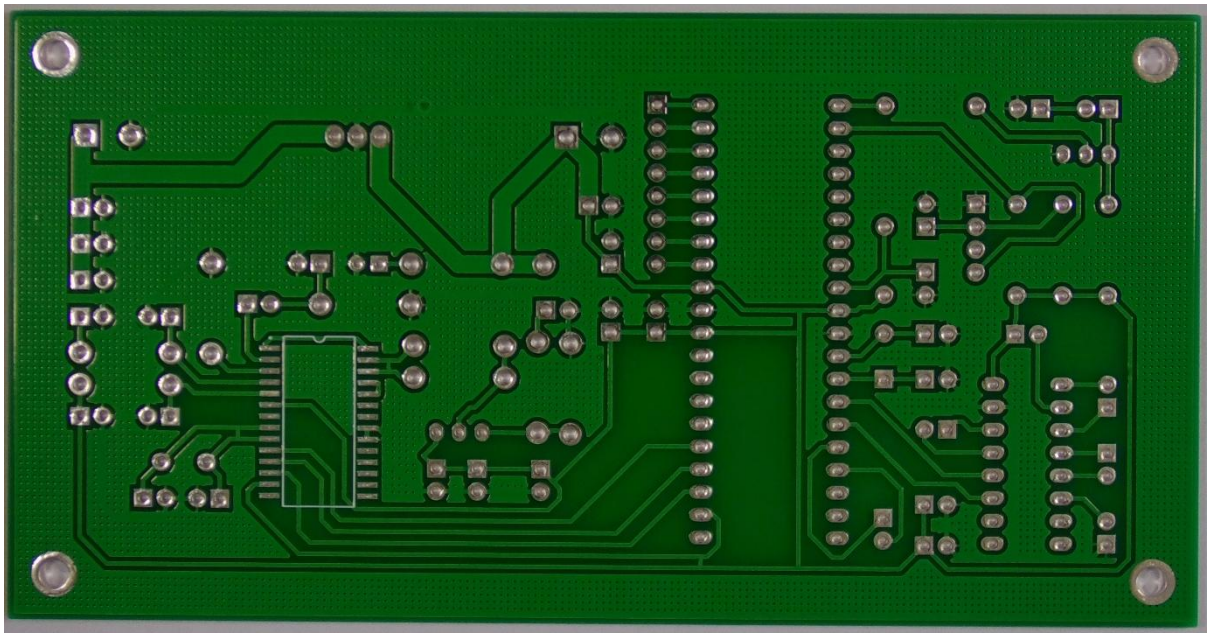


INPUT 2(SHORT CIRCUIT)

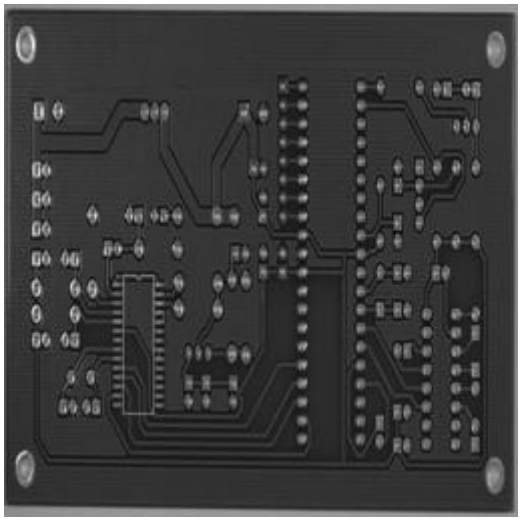
DEFECTLESS PCB:



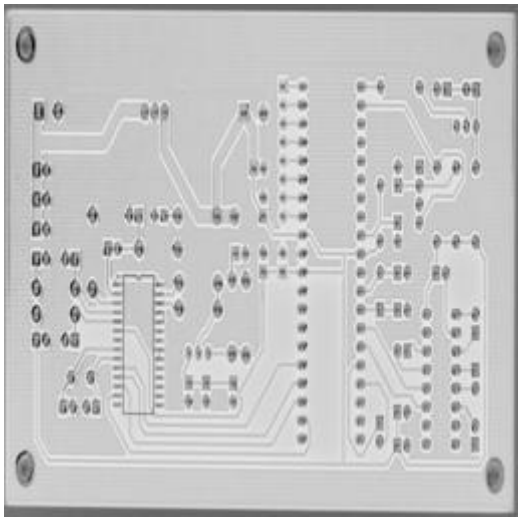
DEFECT PCB:



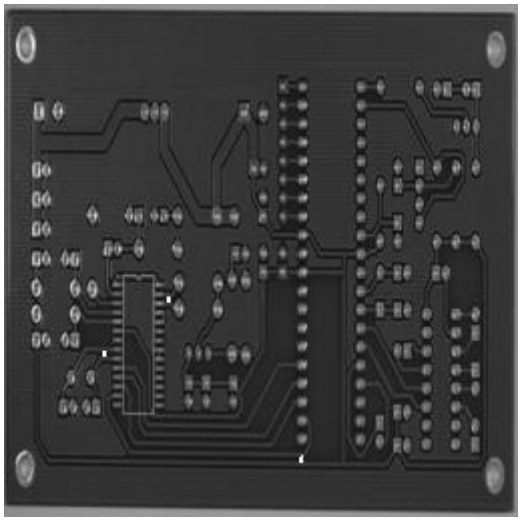
GRAYSCALE:



INVERT:

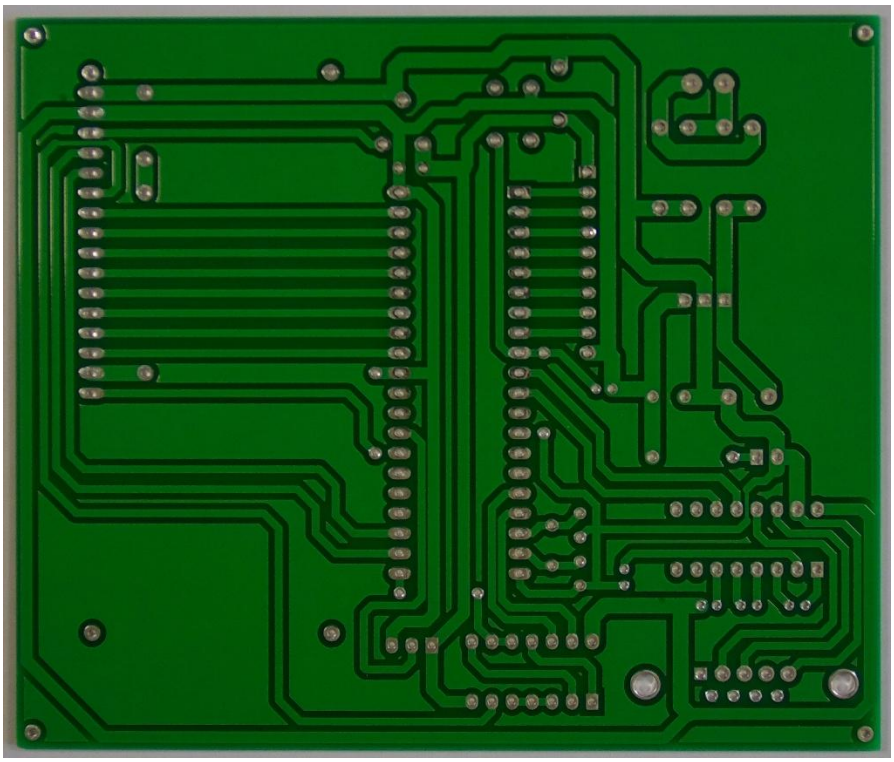


DEFECTED AREA:

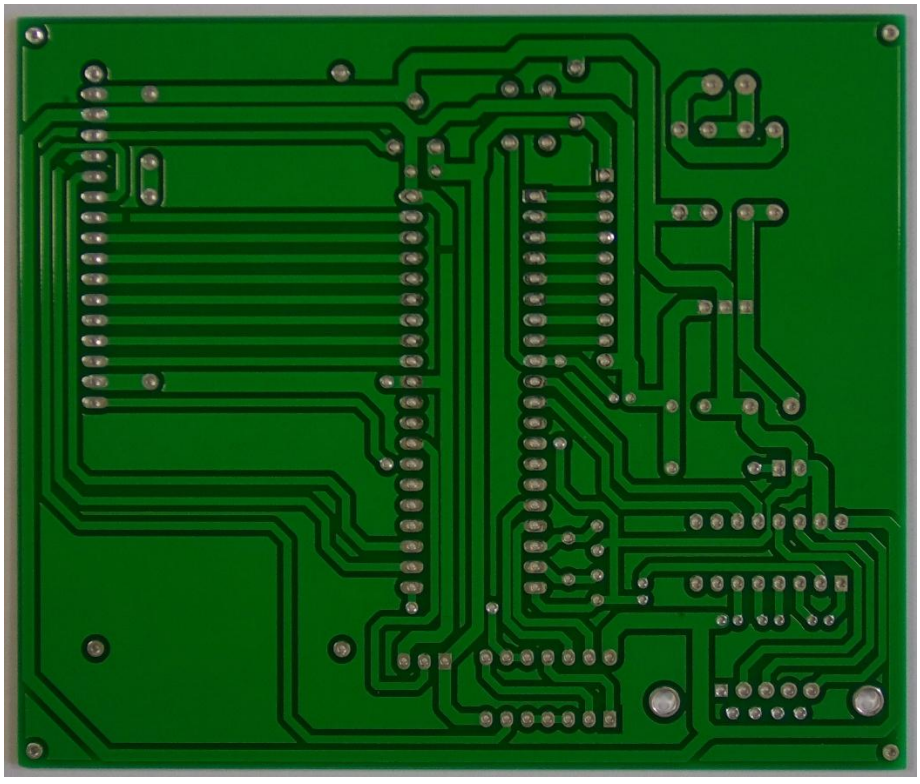


INPUT 3

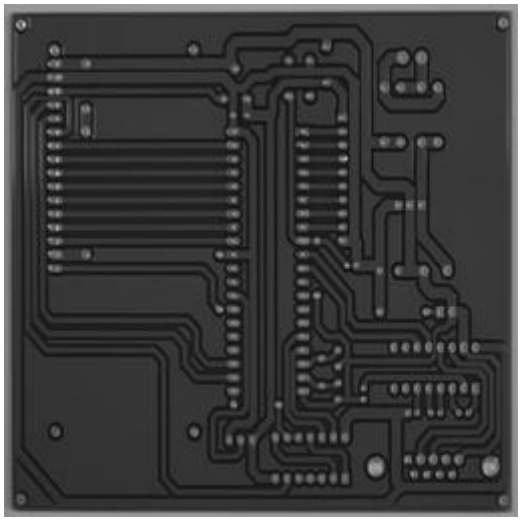
DEFECTLESS PCB:



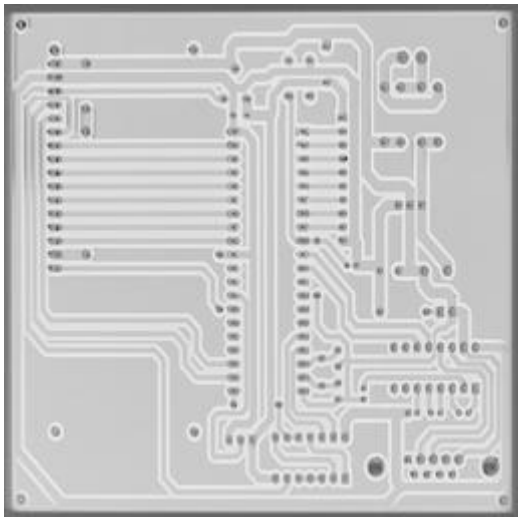
DEFECT PCB:



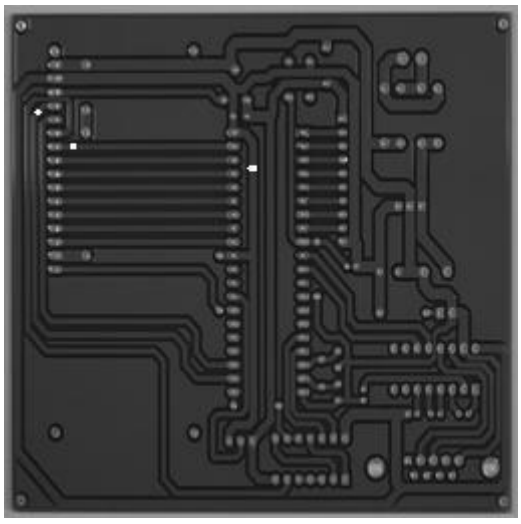
GRAYSCALE:



INVERT:

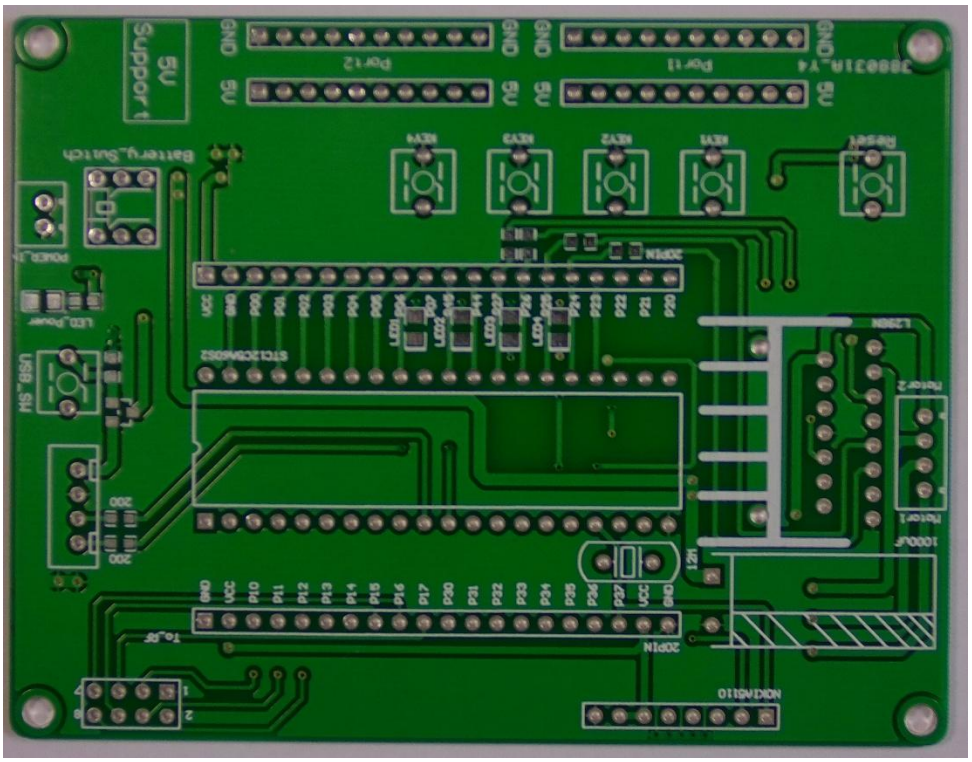


DEFECTED AREA:

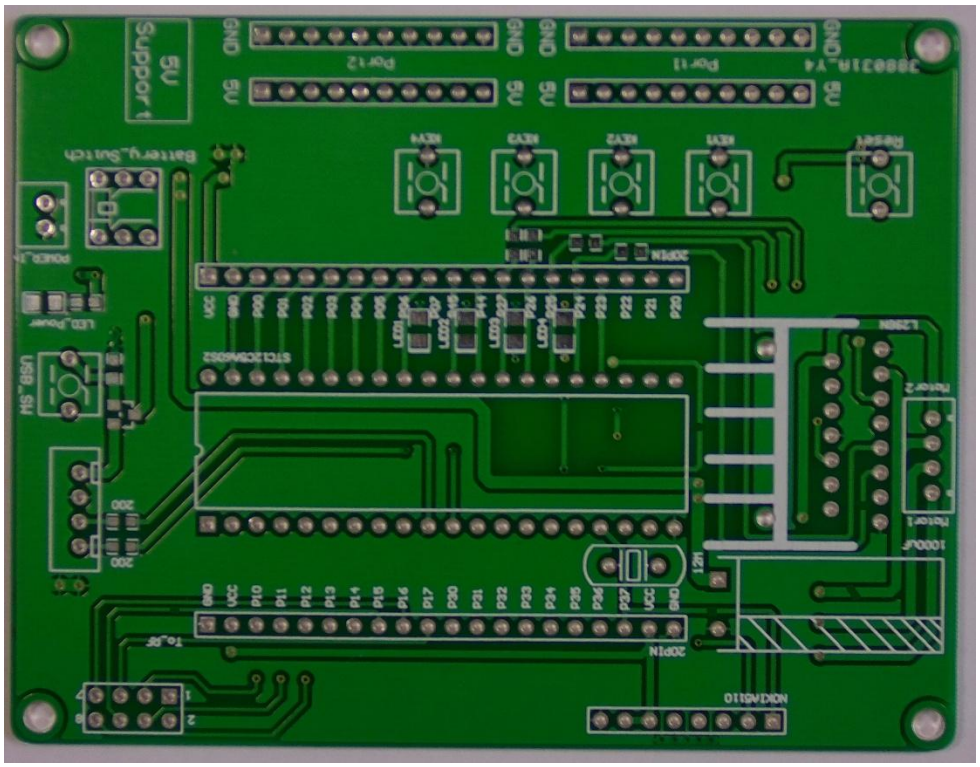


INPUT 4(OPEN CIRCUIT)

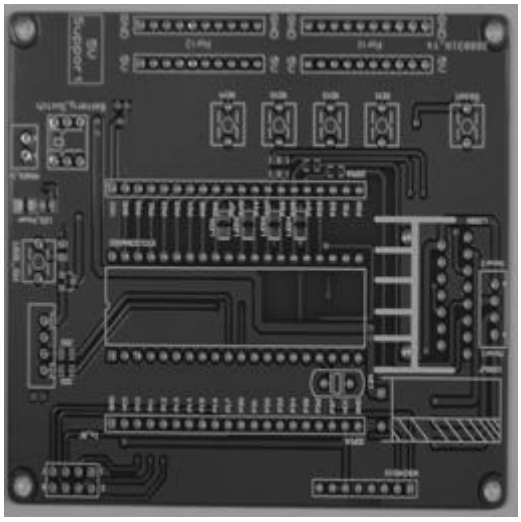
DEFECTLESS PCB:



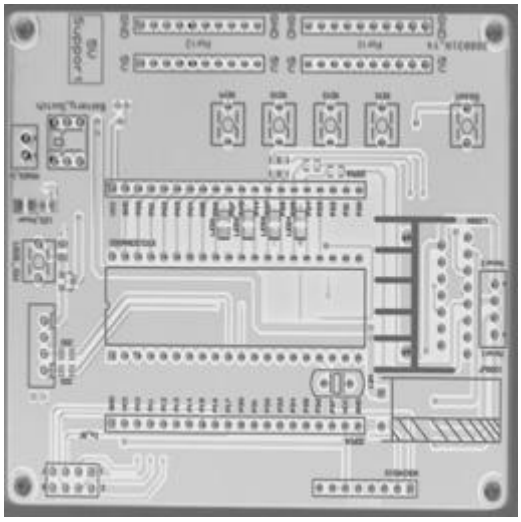
DEFECT PCB:



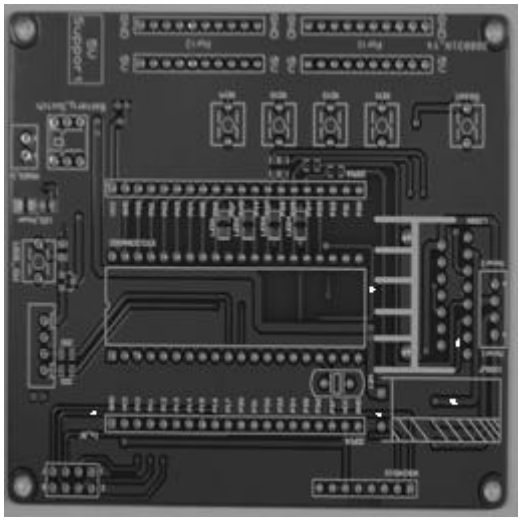
GRAYSCALE:



INVERT:

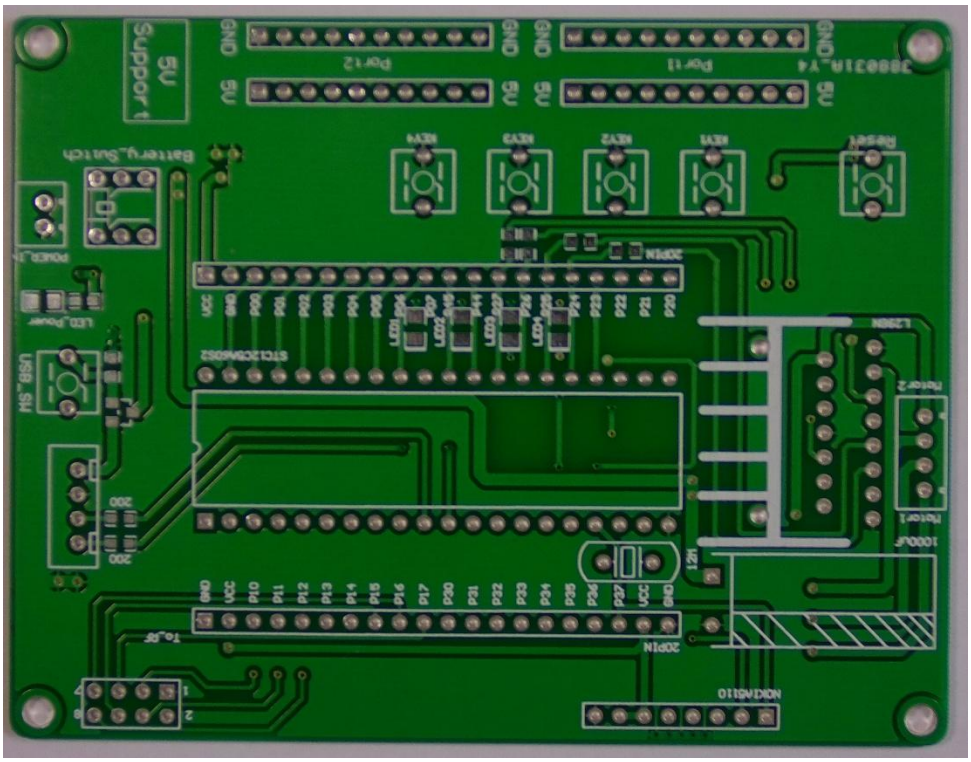


DEFECTED AREA:

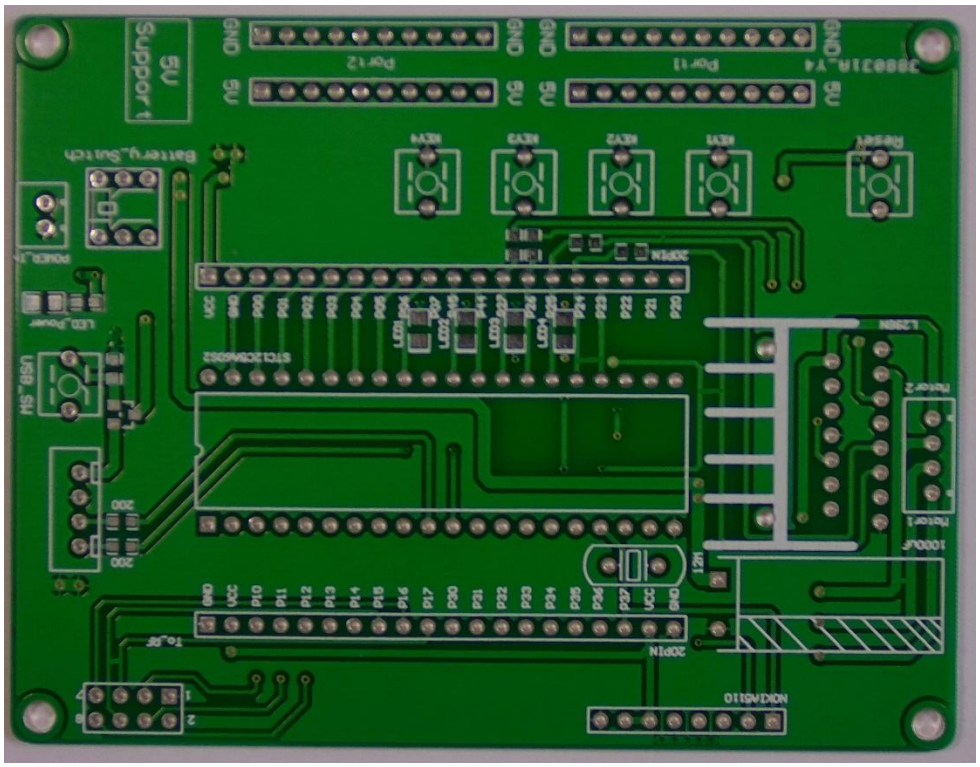


INPUT 5(SHORT CIRCUIT)

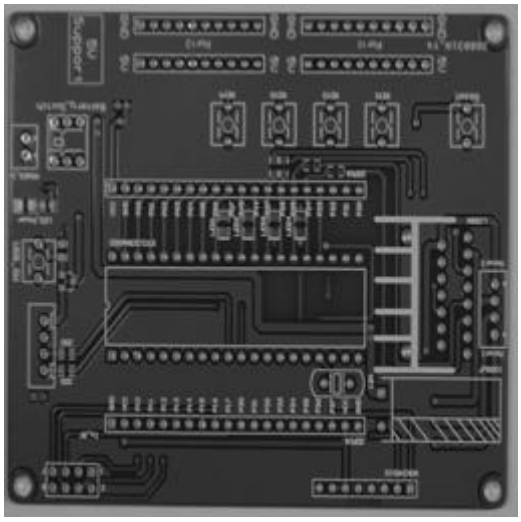
DEFECTLESS PCB:



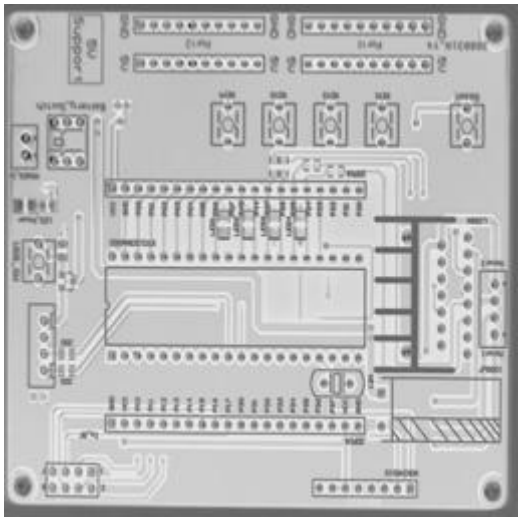
DEFECT PCB:



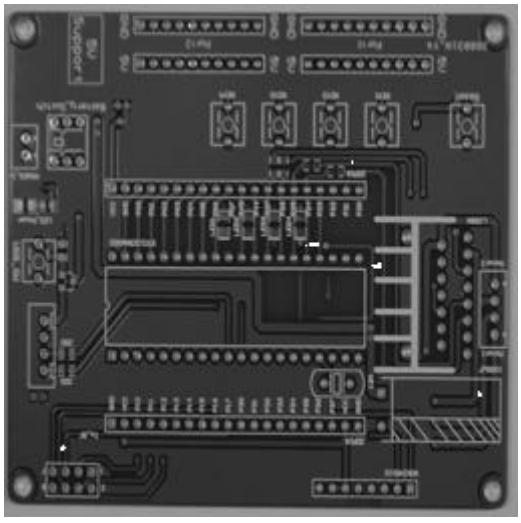
GRAYSCALE:



INVERT:

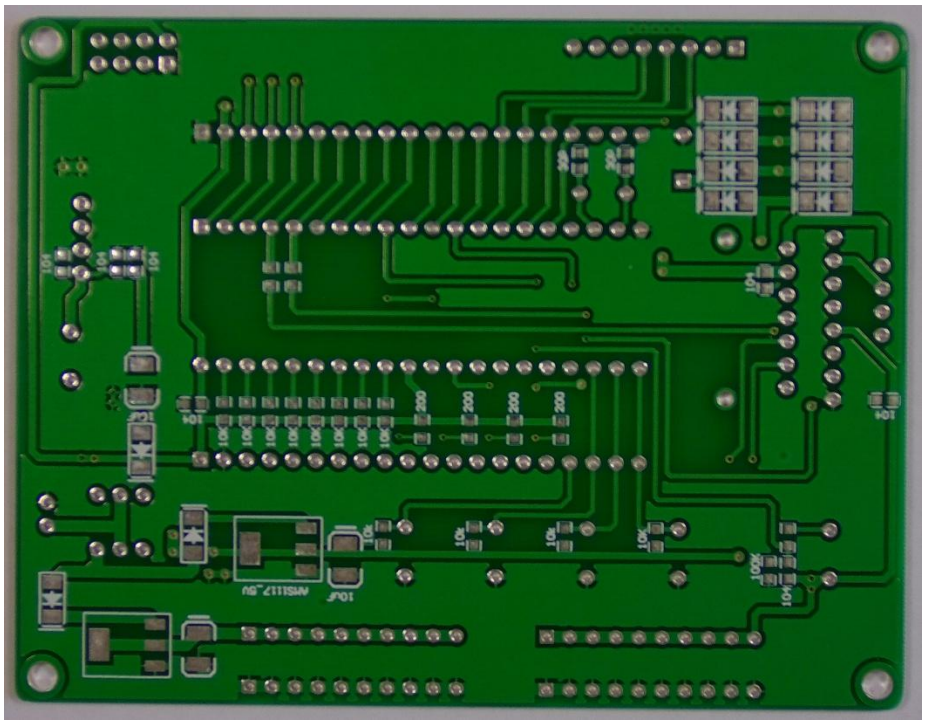


DEFECTED AREA:

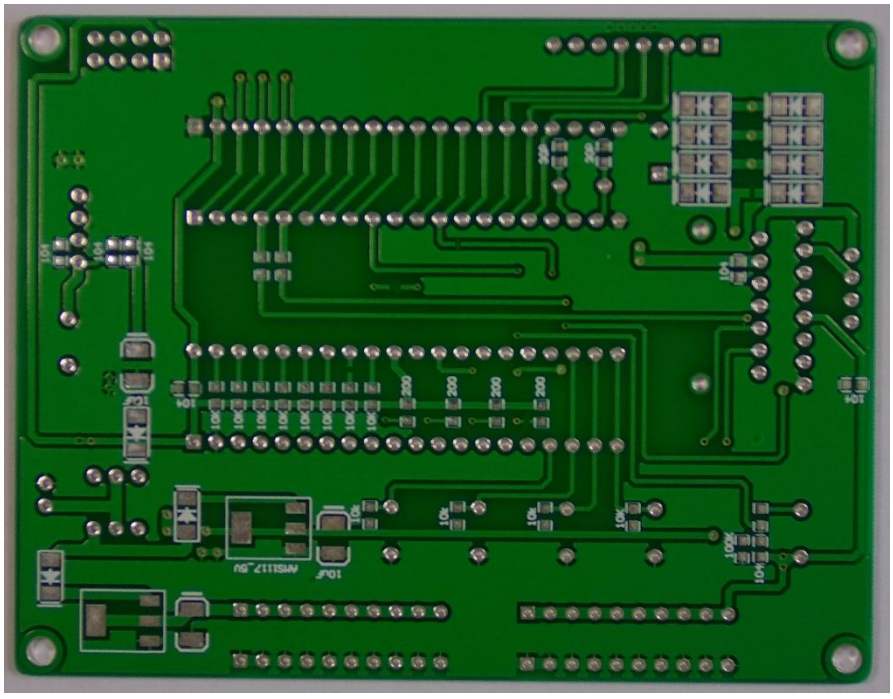


INPUT 6(OPEN CIRCUIT)

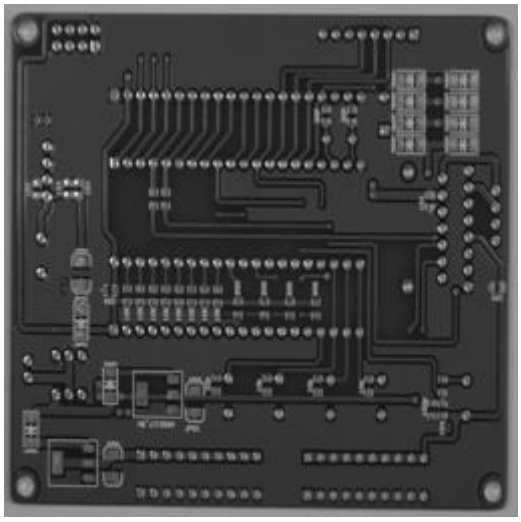
DEFECTLESS PCB:



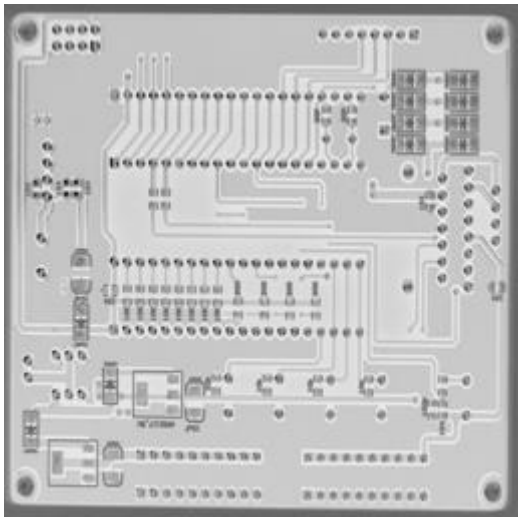
DEFECT PCB:



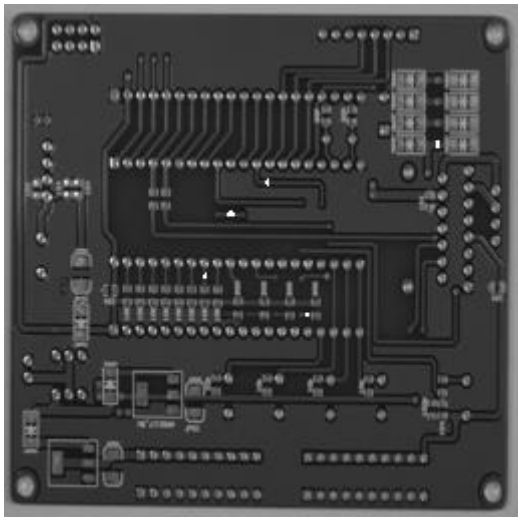
GRAYSCALE:



INVERT:

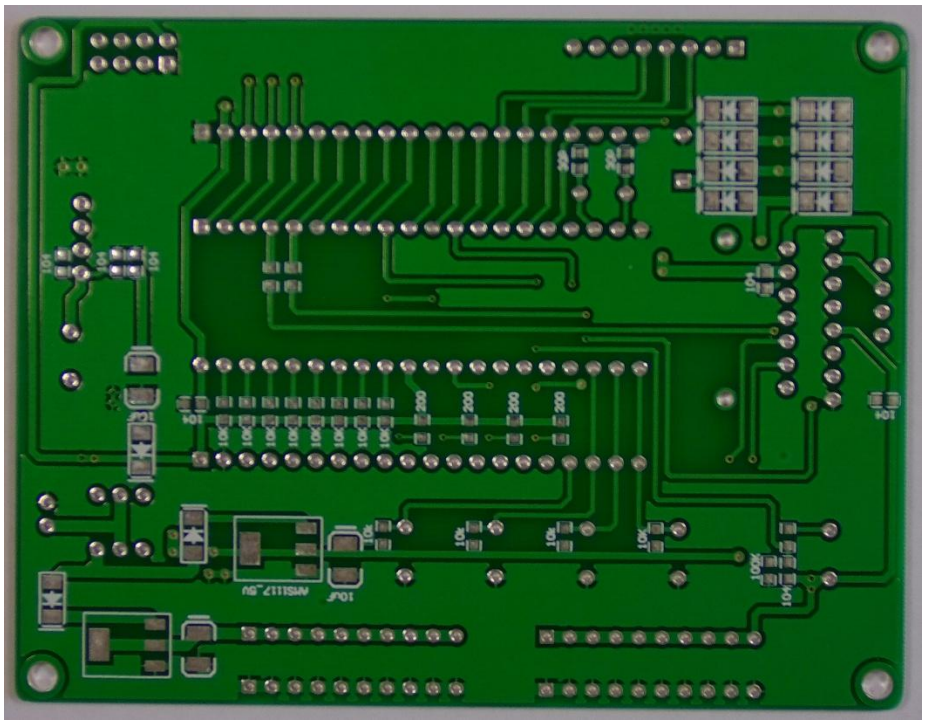


DEFECTED AREA:

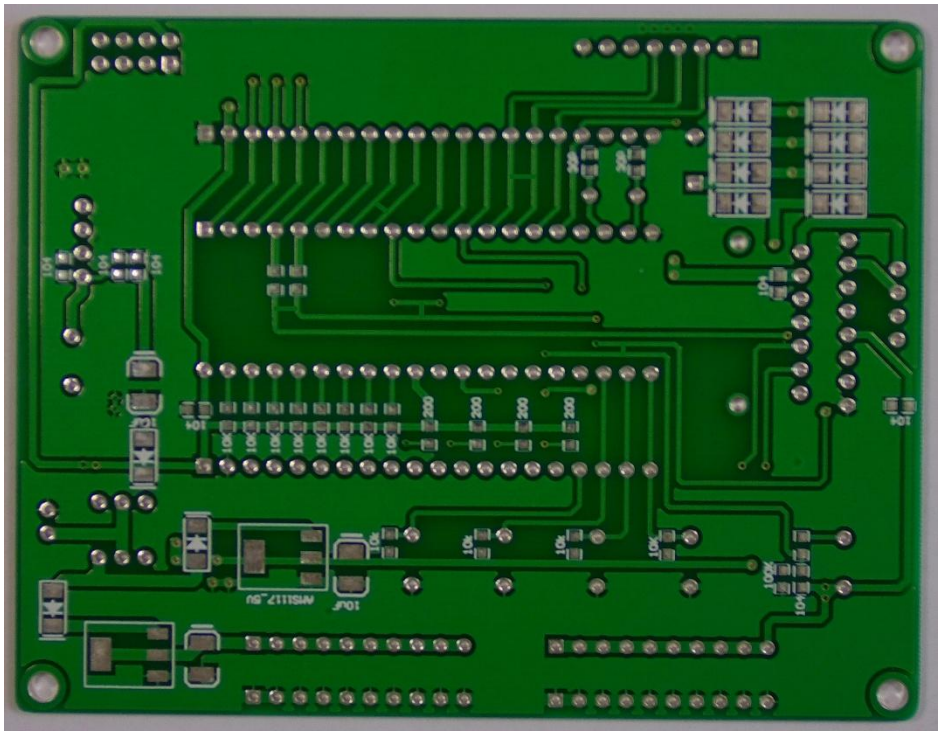


INPUT 7(SHORT CIRCUIT)

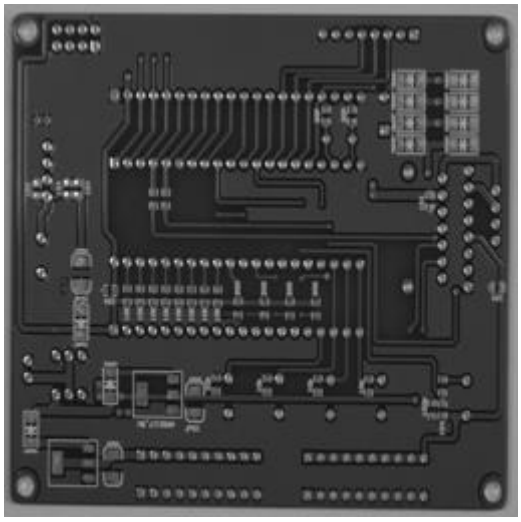
DEFECTLESS PCB:



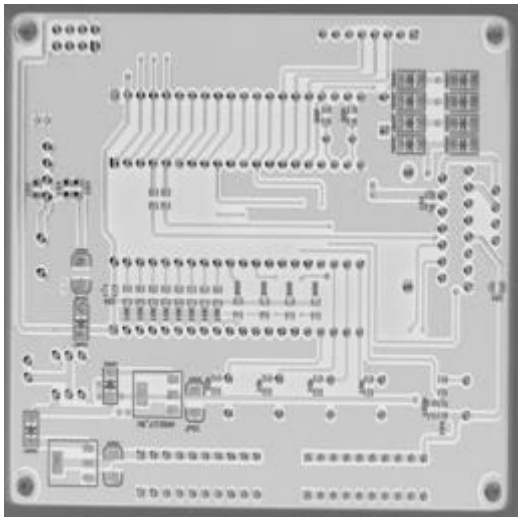
DEFECT PCB:



GRAYSCALE:



INVERT:



DEFECTED AREA: