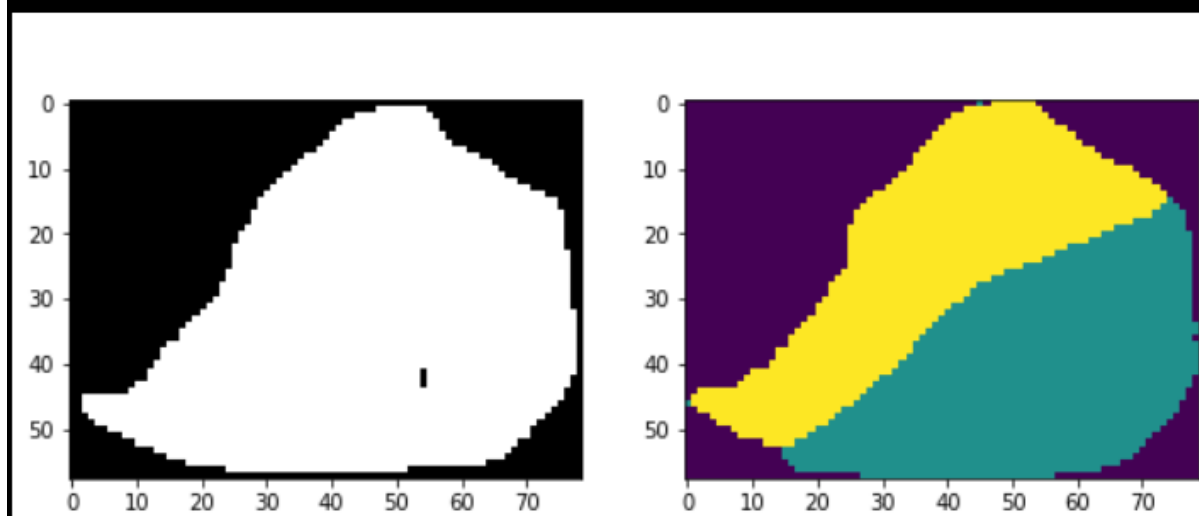


MICRO CT

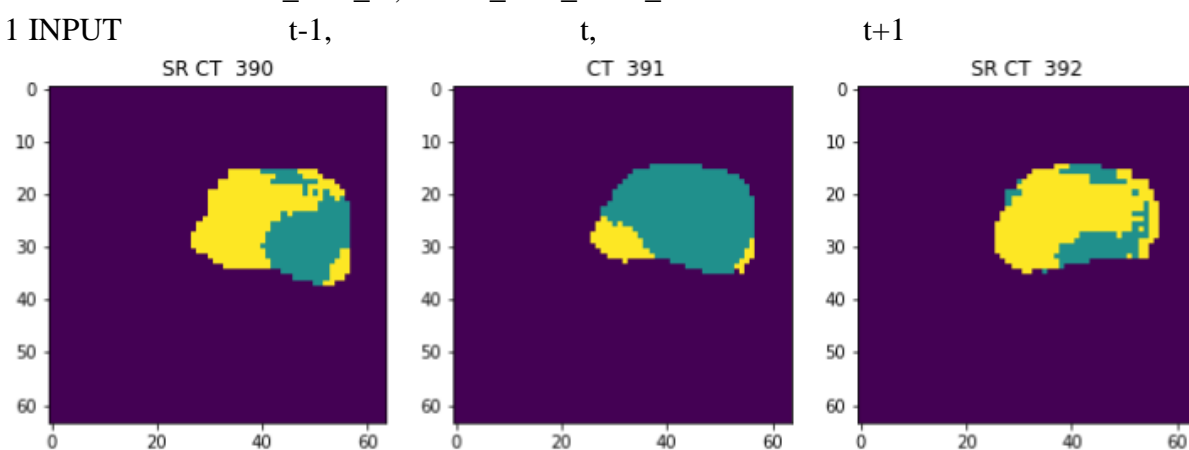
Real Annotations

SFA001_A.5.1.1_CTsr2.png

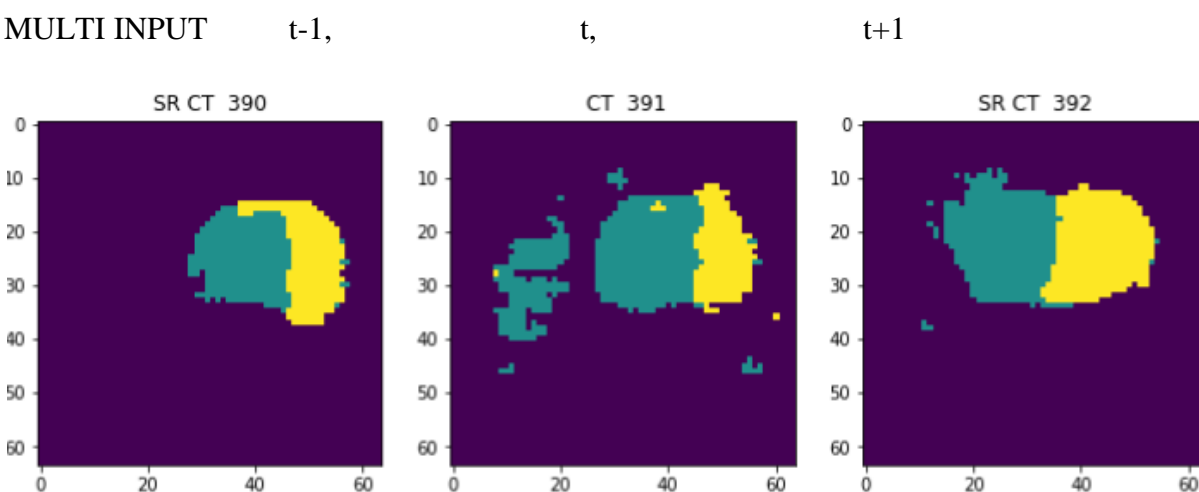


Results: folder model_unet_1 , model_unet_multi_3

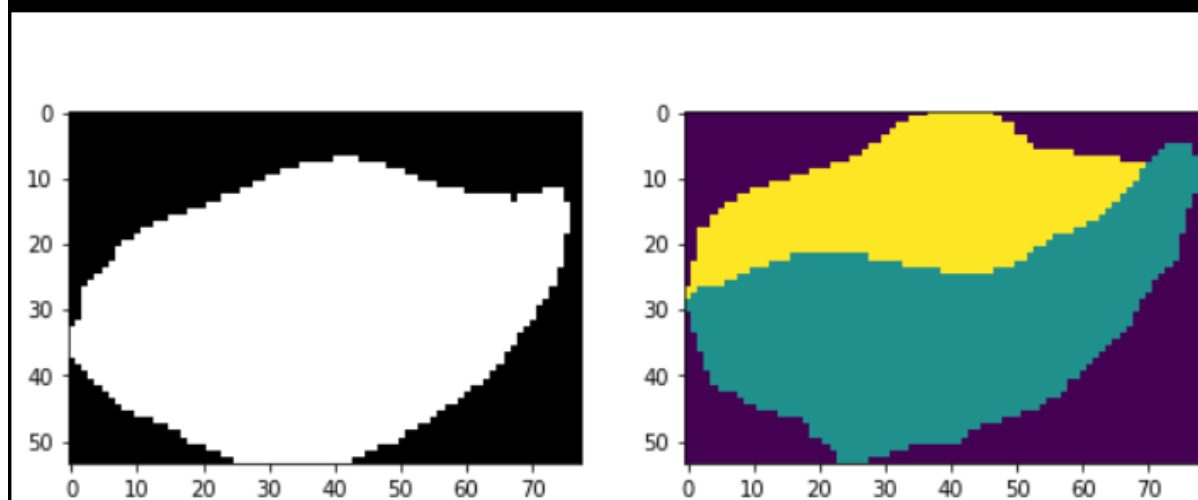
UNET 1 INPUT



UNET MULTI INPUT



SFA001_A.4.1.6_CTsr2.png

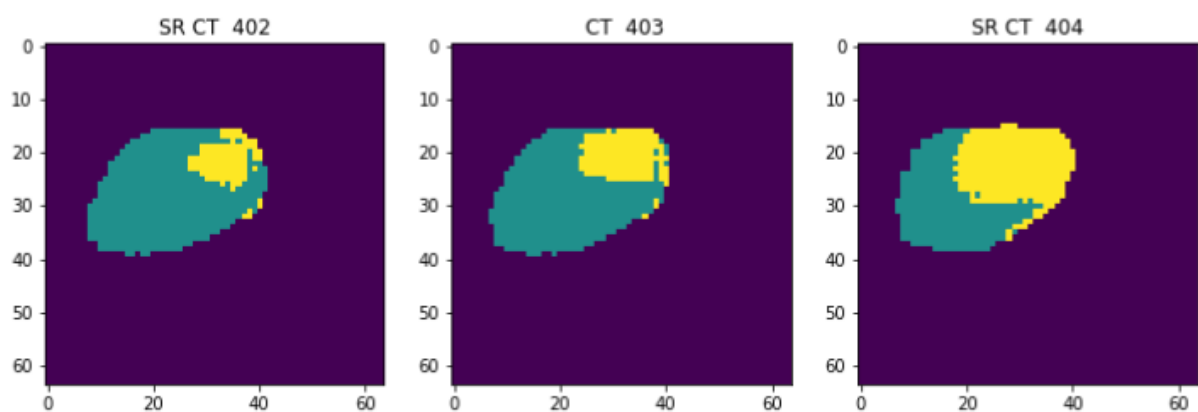


UNET 1 INPUT

t-1,

t,

t+1

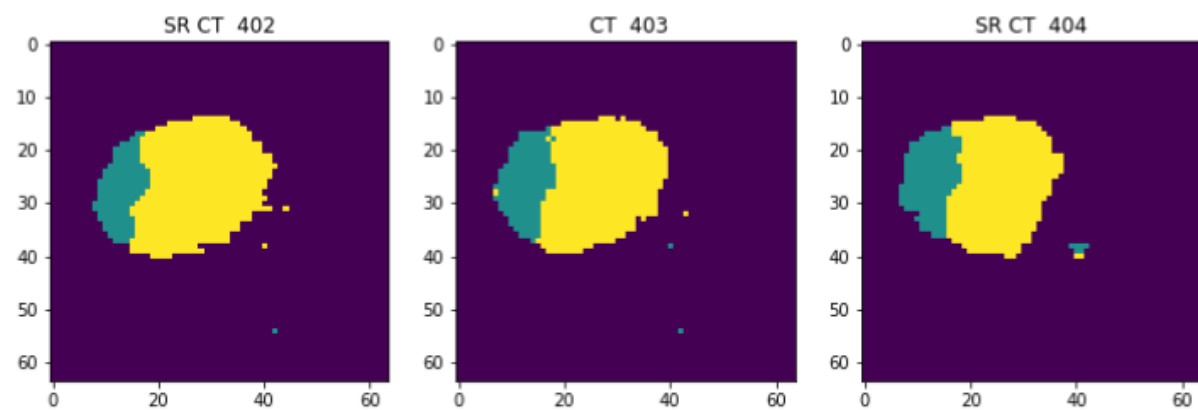


UNET MULTI INPUT

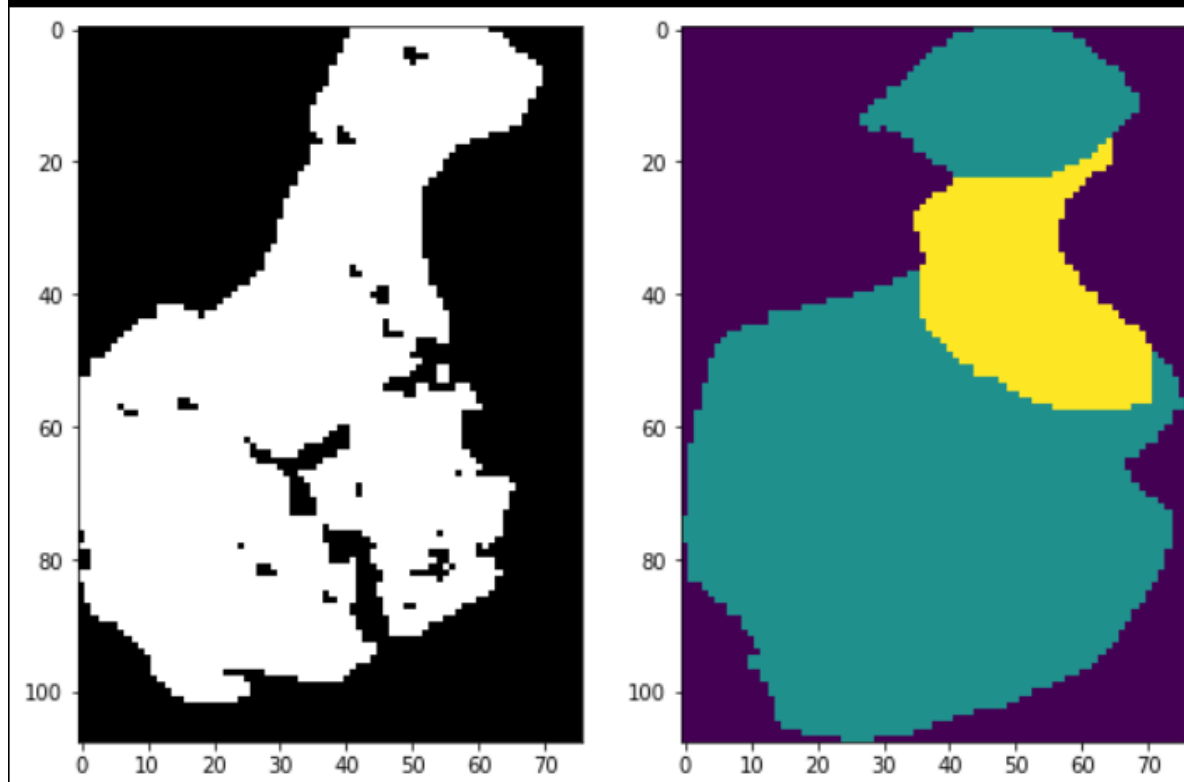
t-1,

t,

t+1



SFA001_A.4.1.5_CTsr2.png

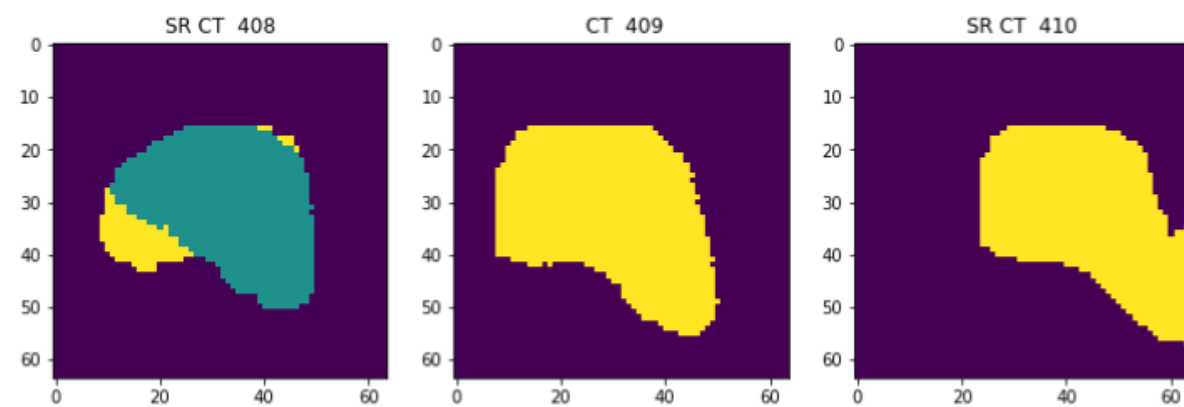


UNET 1 INPUT

$t-1$,

t ,

$t+1$

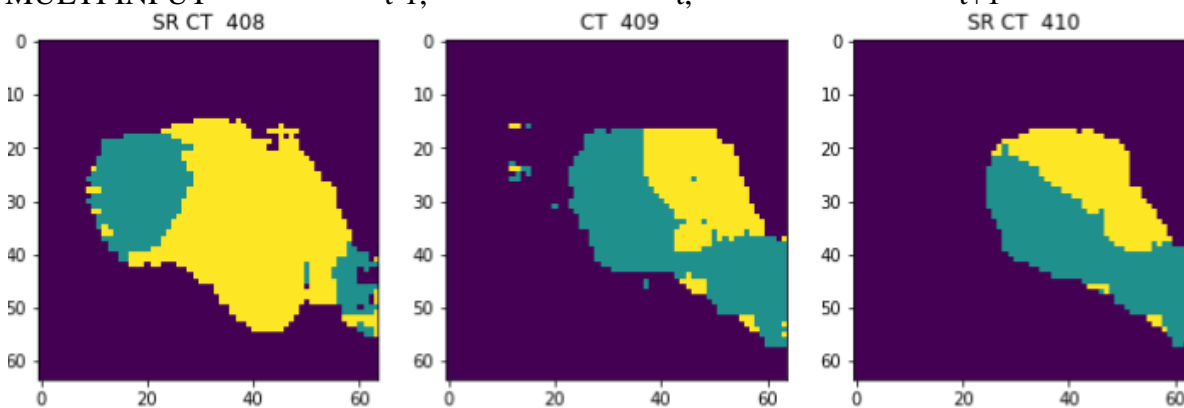


UNET MULTI INPUT

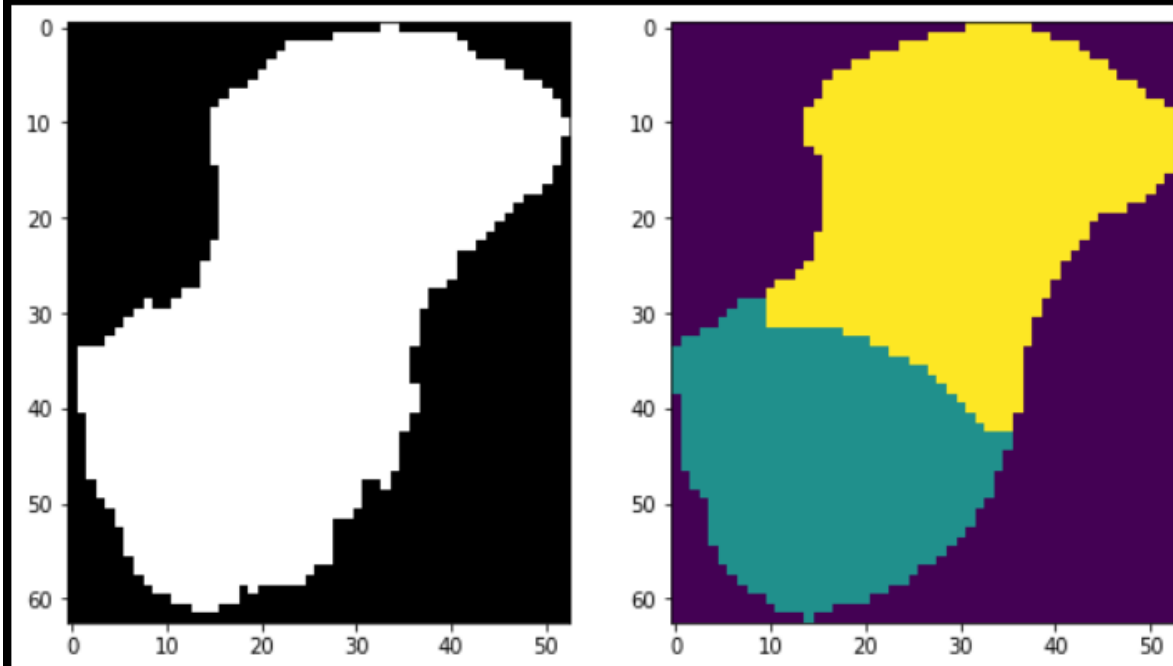
$t-1$,

t ,

$t+1$



SFA001_A.4.1.4_CTsr2.png

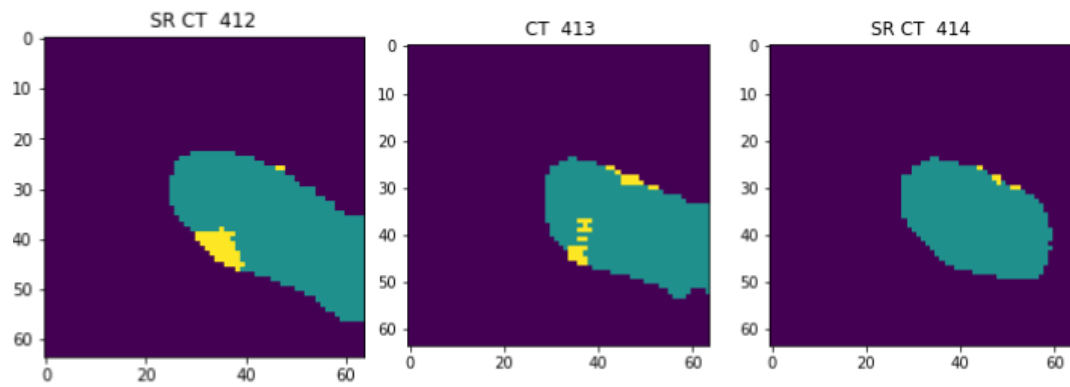


UNET 1 INPUT

t-1,

t,

t+1

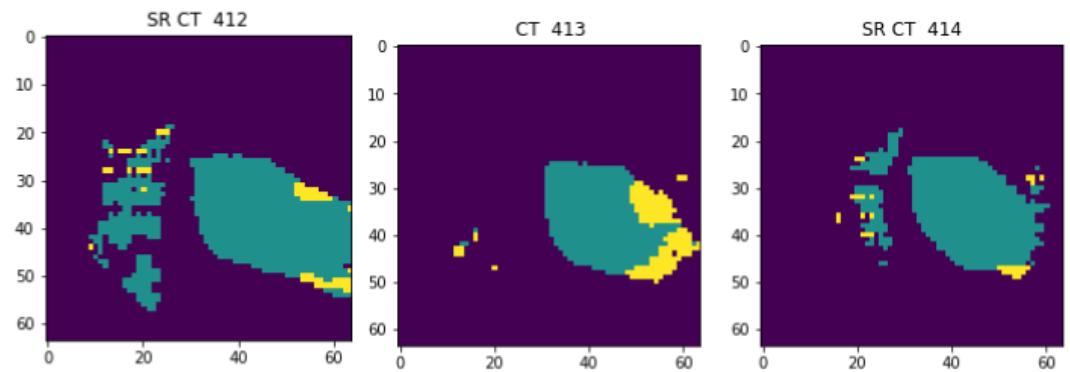


UNET MULTI INPUT

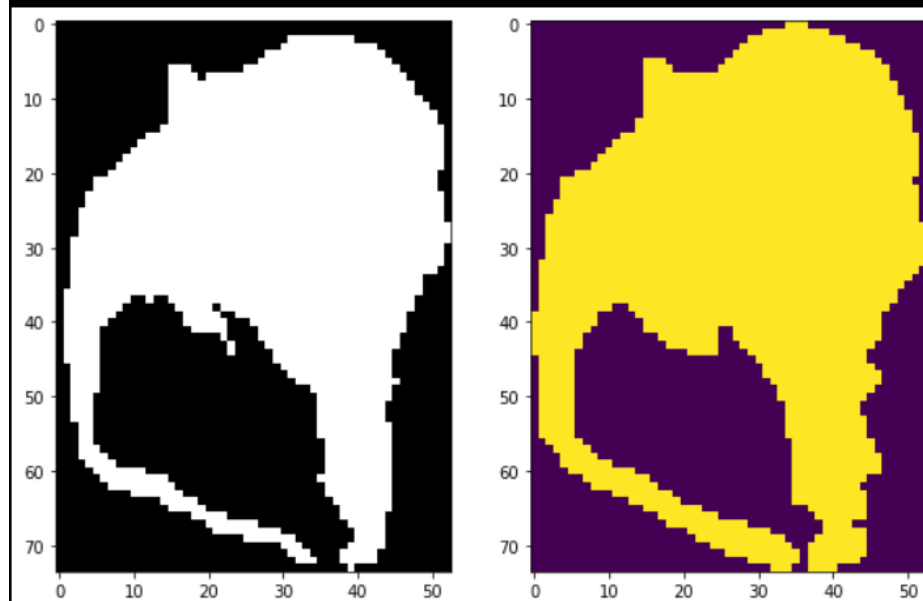
t-1,

t,

t+1



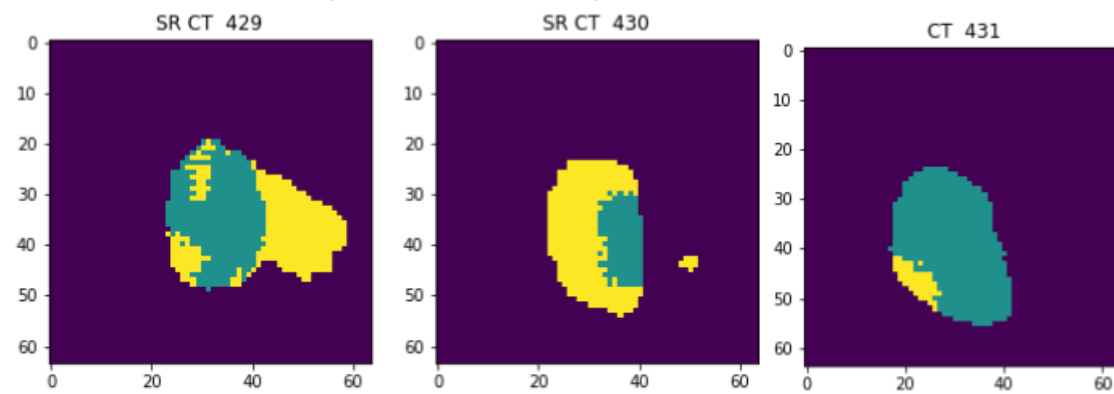
SFA001_A.4.1.1_CTsr2.png



UNET 1 INPUT t-1,

t,

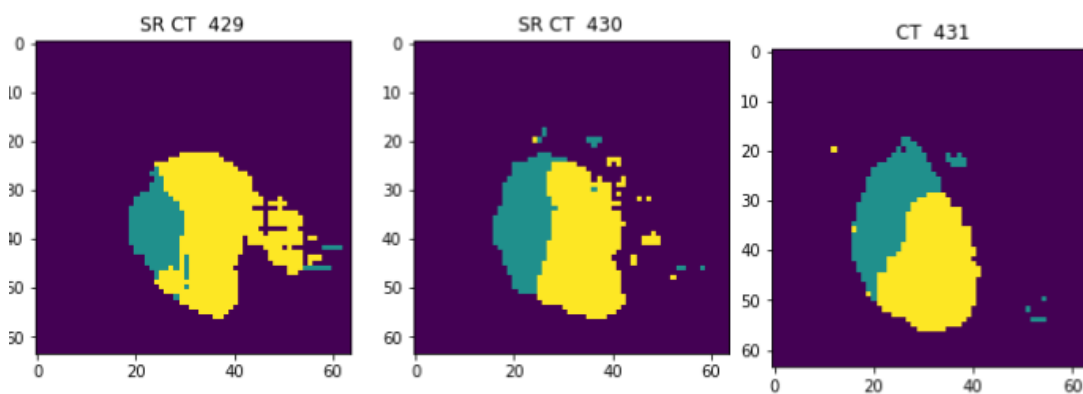
t+1



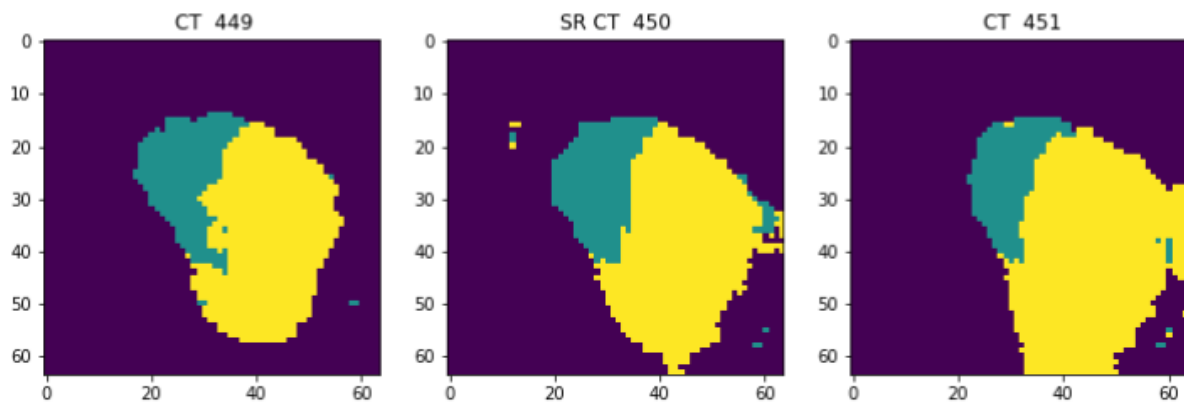
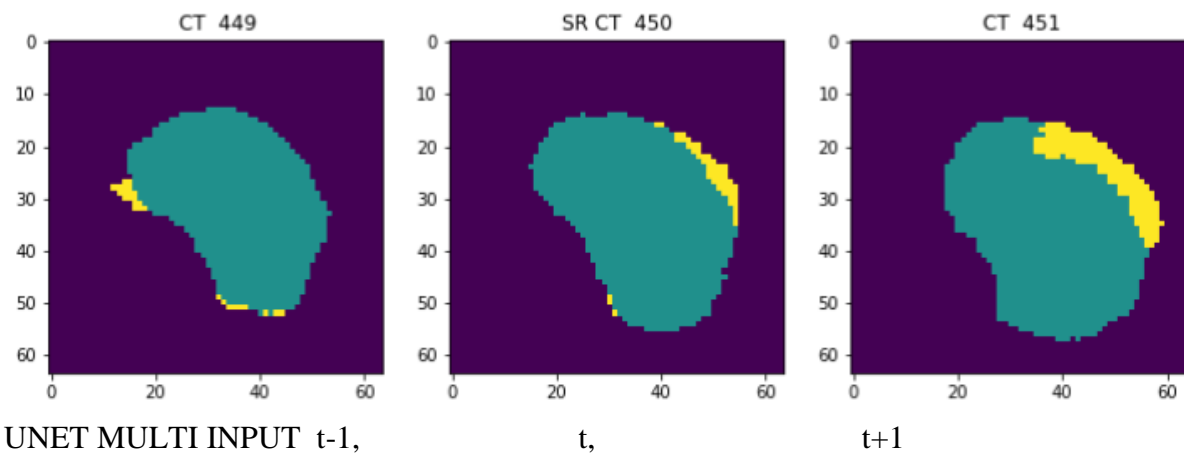
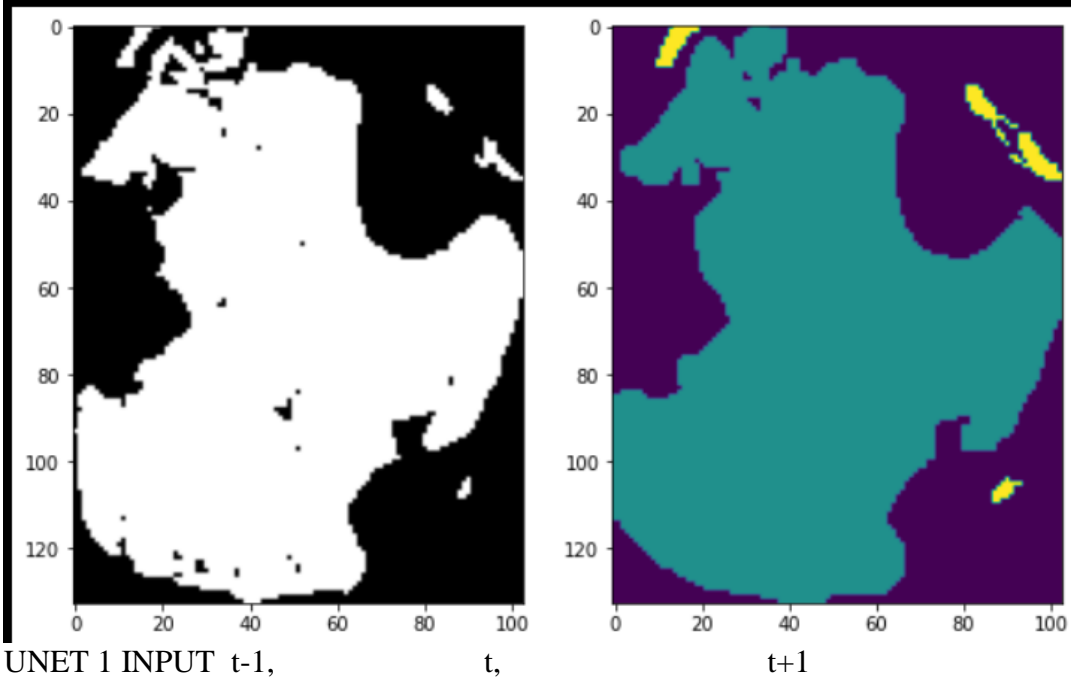
UNET MULTI INPUT t-1,

t,

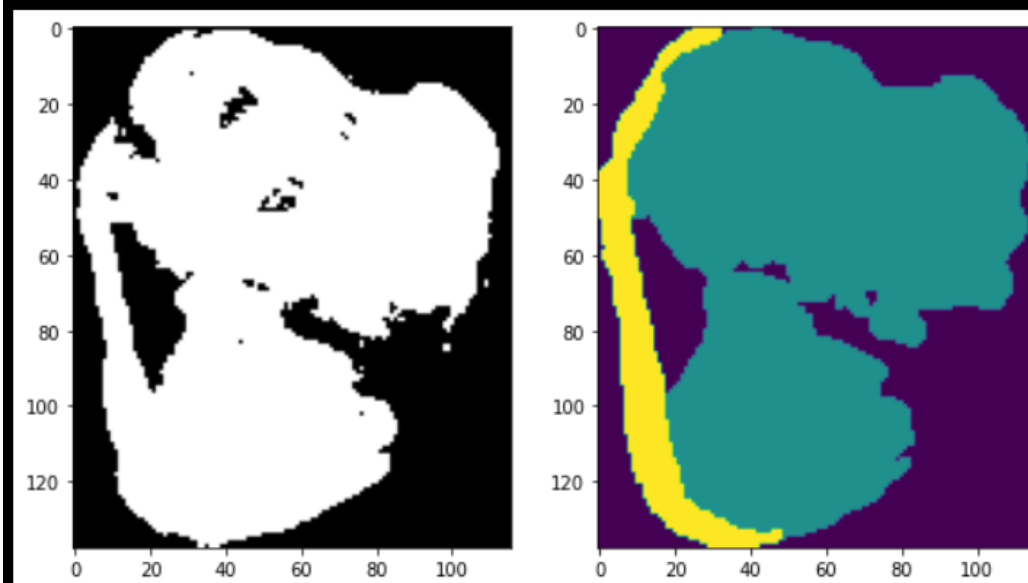
t+1



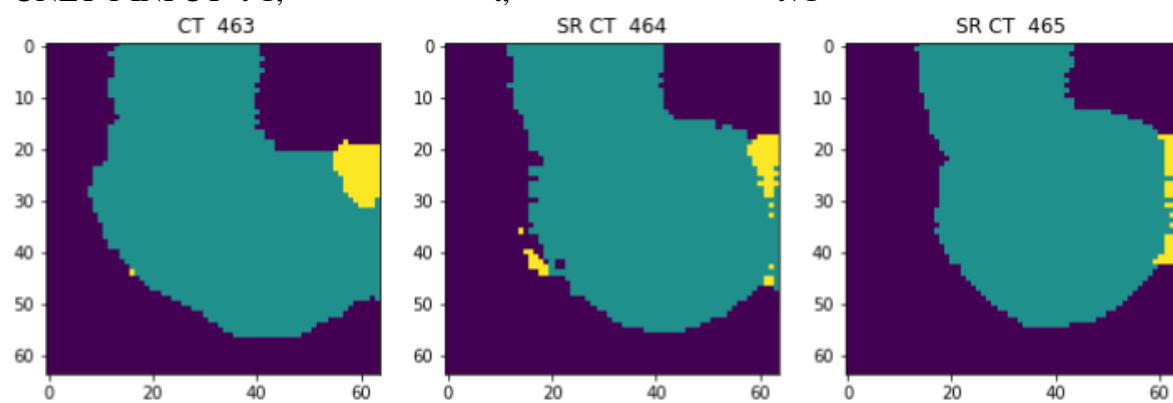
SFA001_A.3.1.5_CTsr2.png



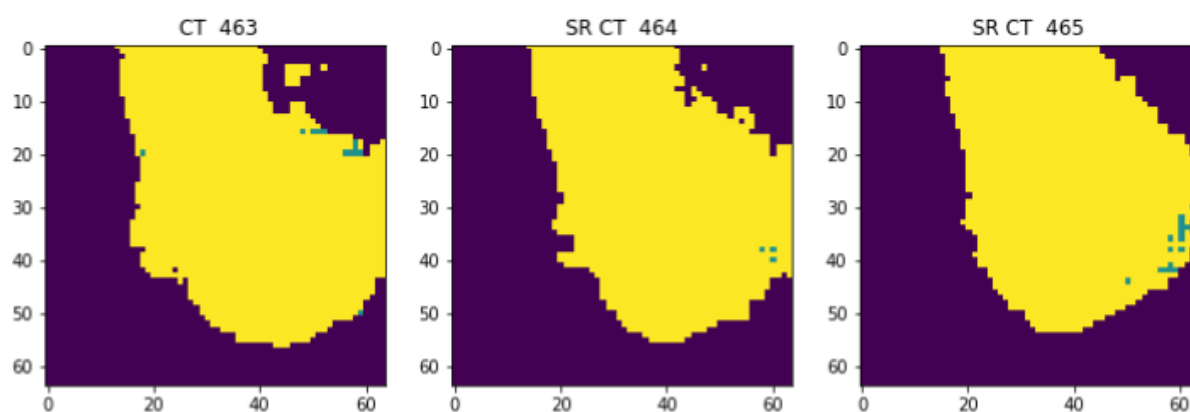
SFA001_A.3.1.3_CTsr2.png

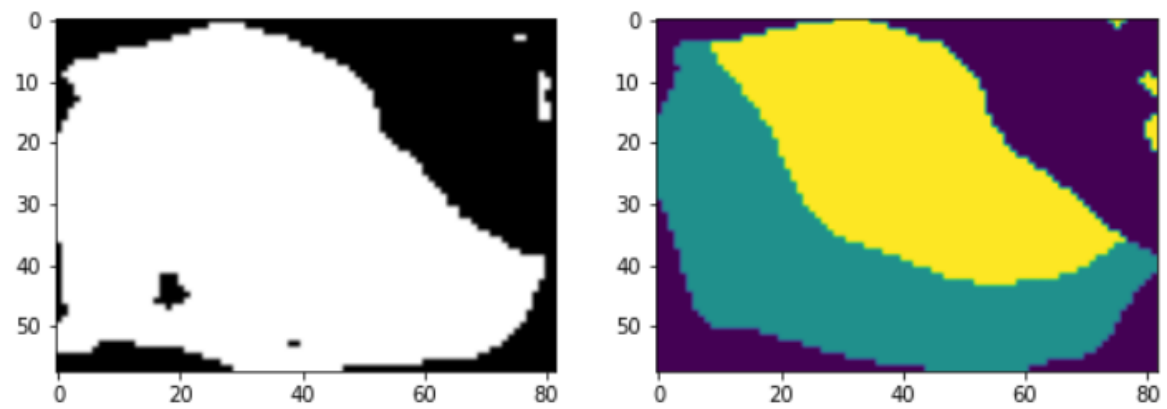


UNET 1 INPUT $t-1$, t , $t+1$

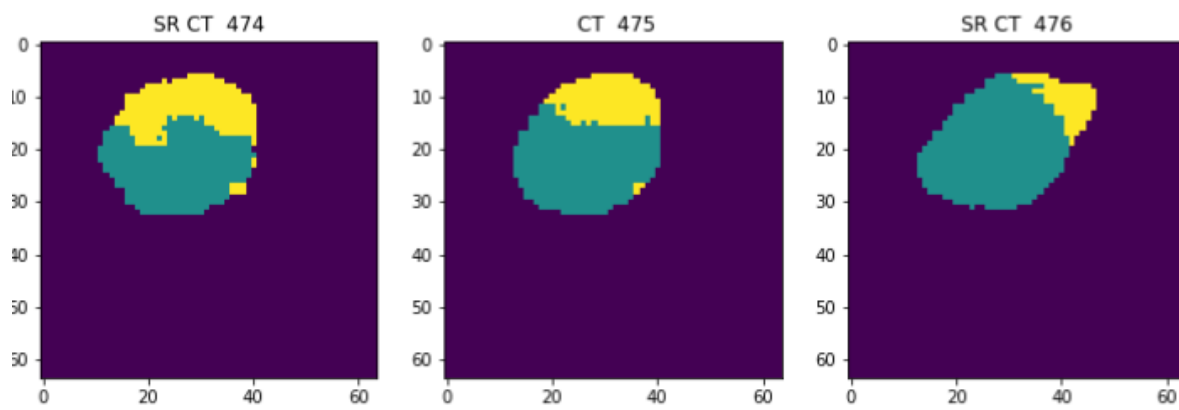


UNET MULTI INPUT $t-1$, t , $t+1$

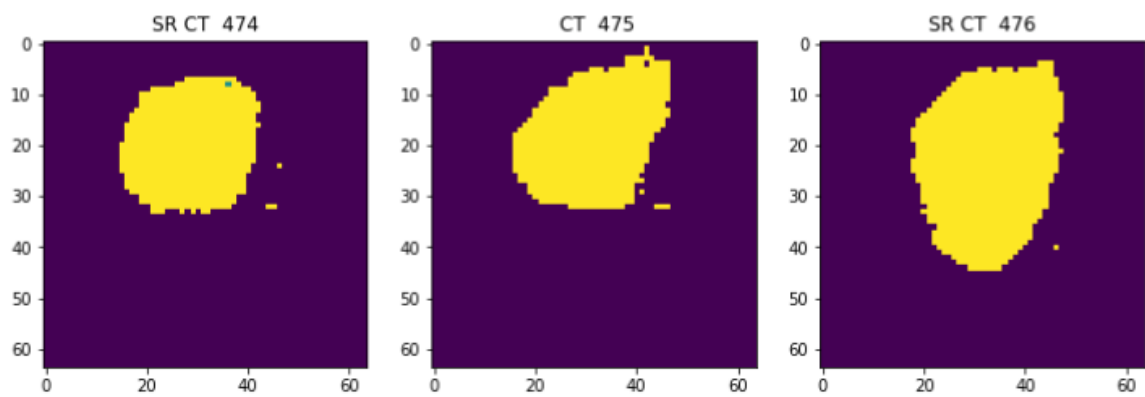




UNET 1 INPUT $t-1$, t , $t+1$



UNET MULTI INPUT $t-1$, t , $t+1$



SFA001_A.3.1.3_CTsr2.png

