CMSC216: Practice Exam 2B SOLUTION

Spring 2024 University of Maryland

Exam period: 20 minutes Points available: 40

Problem 1 (15 pts): Nearby is a C function col_update() with associated data and documentation. Re-implement this function in x86-64 assembly according to the documentation given. Follow the same flow provided in the C implementation. The comments below the colinfo_t struct give information about how it lays out in memory and as a packed argument.

Indicate which registers correspond to which C variables.

```
1 ### SOLUTION:
2 .text
3 .globl
                  col_update
5 # YOUR CODE BELOW
6 col_update:
           movl
                    0(%rdi),%esi
                                      # cur = info->cur
                    4(%rdi), %edx
           movl
                                      # step= info->step
           cmpl
                    $0,%esi
                                      # if(cur < 0)
9
                    .ERROR
           jle
10
           addl
                    $1,%edx
                                      # step++
11
                    $0x01, %esi
                                      # if(cur%2 == 1)
           testl
12
                    .EVEN
                                      # go to even case
13
           jz
  ## ODD CASE (fall through)
14
                    $3,%esi
           imull
                                      # odd: cur *= 3
15
           addl
                    $1,%esi
                                      # odd: cur += 1
16
                    .RETURN
                                      # jump over even
           jmp
17
  .EVEN:
18
                    $1,%esi
                                      # even: cur /= 2
           sarl
19
  .RETURN:
20
                    %esi,0(%rdi)
                                      # info->cur = cur;
           movl
21
                    %edx,4(%rdi)
           movl
                                      # info->step= step;
22
           movl
                    $0, %eax
                                      # success
23
                                      # return 0
           ret
24
  .ERROR:
25
                    $1,%eax
                                      # error case
          movl
26
                                      # return 1
           ret
27
```

```
1 typedef struct{
    int cur;
    int step;
4 } colinfo_t;
                         Byte | Packed |
              | Byte |
5 // |
 6 // | Field | Size | Offset |
                                   Bits |
 7 // |---
                   4 |
                                   0-31
8 // | cur
                            +0 I
9 // | step
                   4 |
                            +4 |
                                  32-63 l
int col_update(colinfo_t *info){
    // Updates current value and step in
    // colinfo_t pointed by param info. If
    // infor->cur is invalid, makes no changes
    // and returns 1 to indicate an
15
    // error. Otherwise performs odd or even
16
17
    // update on cur and increments step
    // returning 0 for success.
19
    int cur = info->cur;
20
    int step = info->step;
^{21}
    if(cur \le 0){
23
      return 1;
24
    }
    step++;
25
    if(cur % 2 == 1){
26
27
      cur = cur*3+1;
    }
28
    else{
29
30
      cur = cur / 2;
31
32
    info->cur = cur;
33
    info->step = step;
    return 0;
34
35 }
```

Problem 2 (15 pts): Below is an initial register/memory configuration along with snippets of assembly code. Each snippet is followed by a blank register/memory configuration which should be filled in with the values to reflect changes made by the preceding assembly. The code is continuous so that POS A is followed by POS B.

SOLUTION: INITIAL		s m m	addl %edi, %esi subq \$8, %rsp movl \$100, 4(%rsp) movl \$300, 0(%rsp) addl (%rsp), %eax # POS A			movq \$1, %rdi addl %esi, (%rsp,%rdi,4) leaq 8(%rsp), %rdi addl (%rdi), %eax # POS B		
REG	Value		REG	Value		REG	Value	
+				+			+	
rax	10		rax	310		rax	560	
rdi	20		rdi	l 20 l		rdi	#3032	
rsi	30		rsi	J 50 J		rsi	J 50	
rsp	#3032	l 1	rsp	#3024		rsp	#3024	
+				+			+	
MEM	Value		MEM	Value		MEM	Value	
+		. !		+			+	
#3032	250		#3032	250		#3032	250	
#3028	1		#3028	100		#3028	150	
#3024	2		#3024	300		#3024	300	
#3020	3		#3020	3		#3020	3	
+				+			+	

Problem 3 (10 pts): Rover Witer is writing an assembly function called compval which he will use in C programs. He writes a short C main() function to test compval but is shocked by the results which seem to defy the C and assembly code. Valgrind provides no insight for him. Identify why Rover's code is behaving so strangely and fix compval so it behaves correctly.

Sample Compile / Run:

```
> gcc compval_main.c compval_asm.s
> a.out
expect: 0
actual: 19
expect: 0
actual: 50
```

SOLUTION: The movq instruction at line 7 of compval writes 8 bytes. This is inappropriate as a 4-byte int is supposed to be written. Apparently the stack layout in main() has the variable actual at a memory address immediately below variable expect so that on writing 8 bytes, the low order 4 bytes correctly get written to actual but the high order 4 bytes (all 0's for small values) overwrite the variable expect leaving it as 0. The fix for this is to use movl %eax, (%rdx) which will write 4 bytes, filling only actual.

```
1 // compval_main.c
 2 #include <stdio.h>
 4 void compval(int x, int y, int *val);
 5 // compute something based on x and y
 6 // store result at int pointed to by val
 8 int main(){
    int expect, actual;
 9
10
     expect = 7 * 2 + 5;
                              // expected value
11
    compval(7, 2, &actual); // actual result
12
    printf("expect: %d\n",expect);
13
    printf("actual: %d\n",actual);
14
15
    expect = 5 * 9 + 5;
                              // expected value
16
     compval(5, 9, &actual); // actual result
17
18
    printf("expect: %d\n",expect);
    printf("actual: %d\n",actual);
19
    return 0;
^{21}
22 }
 1 # compval_asm_corrected.s
 2 .text
 3 .global compval
                                # imul/add should be 32-bit
                                # biggest problem is movq
 4 compval:
           imulq
                   %rdi,%rsi
                                # likely: imul %edi, %esi
 5
           addq
                   $5,%rsi
                                # likely: addl $5, %esi
 6
                   %esi,(%rdx) # was movq %rsi, (%rdx)
           movl
           ret
                                # MUST do a movl, not movq
```