## **DECA**

Section	Title	Page
01.0	Design Introduction	
1.01	Cover Page	1
1.02	Block Diagram	2
02.0	MAX 10 10M50DAF484	
02.01	MAX 10 BANK1 & BANK2	3
02.02	MAX 10 BANK3 & BANK4	4
02.03	MAX 10 BANK5 & BANK6	5
02.04	MAX 10 BANK7 & BANK8	6
02.05	MAX 10 Clocks & Configuration	7
02.06	MAX10 Power & GND	8
02.07	MAX10 Decoupling	9
03.0	Clock	
03.01	Clock	10
04.0	JTAG	
04.01	USB Blaster II	11
05.0	Expansion Port	
05.01	Expansion Headers - BBB Headers	12
06.0	Memory	
06.01	DDR3 SDRAM & QSPI Flash	13
06.02	SD Card	14

	Section	Title	Page
	07.0	Video & Audio	
	07.01	MIPI Interface	15
	07.02	HDMI TX	16
	07.03	Audio CODEC	17
	0.80	Ethernet	
	08.01	Ethernet	18
	09.0	USB PHY	
	09.01	USB PHY	19
	10.0	Analog Interface	
09.02	10.01	SMA Connectors & Differential Amplifier	20
	11.0	Sensors Sensors	
	11.01	Accelerometer	21
	11.02	Gesture, Humidity, Temperature Sensors	22
	12.0	User Interface	
	12.01	LED & BUTTON & SWITCH	23
	13.0	System Power	
	13.01	1.2V & 1.5V & 1.8V & 5V	24
	13.02	2.5V & 2.8V & 3.3V	25
7			

Copyright (c) 2014 by Tensio Tachnologies Inc. Taleans. All right researed. As dight researed. Depart of this charmatic design may be reproduced, duplicated, or used without the prior written permission of Tensio.						
Title						
	DECA					
Size	Document Number Re	ev V				
В	Cover Page	С				
Date:	Wednesday, March 11, 2015 Sheet 1 of 25					

5















































