INDIAN INSTITUTE OF TECHNOLOGY, KHARAGPUR
Computer Science and Engineering

Switching Circuits and Logic Design (CS21002)
Class Test – I (Spring)

Name: _____   *Roll number:* _____

Date: Wed, Jan 20, 2021   *Marks:* 20   *Time:* 9:10-10:00am (FN)

Answer ALL the questions using xournal or similar software to edit the PDF

Q1: You are to design a two-digit decimal adder, using a binary coded decimal (BCD) representation. In BCD representation, each decimal digit is represented by its corresponding four bit binary representation. Bit combinations representing values of decimal ten or greater are not valid BCD patterns. Thus, a two-digit decimal number is represented by two sets of four bits. For example the decimal number 25 is represented in BCD as `0010 0101`.

A two-digit decimal adder takes two decimal numbers represented as BCD and produces a two-digit BCD sum and a single carry-out bit (9 bits total). For example, The numbers 25 (`0010 0101`) and 75 (`0111 0101`) add to 100, i.e., sum: `0000 0000` and carry-out: `1`.

Your roll number is of the form $nx_1DDnnnnx_0$. Consider the decimal number $x_1x_0 =$_____.

(a) Write down the steps for adding a pair of two-digit decimal numbers represented as BCD. Explain your steps with the example of adding x_1x_0 and 69.   10

(b) Suppose you have 4-bit binary adders, producing a binary sum and a carry-out. Use these adder blocks, along with logic gates, to design a two-digit BCD adder circuit. 10