

Date: 16/2/21

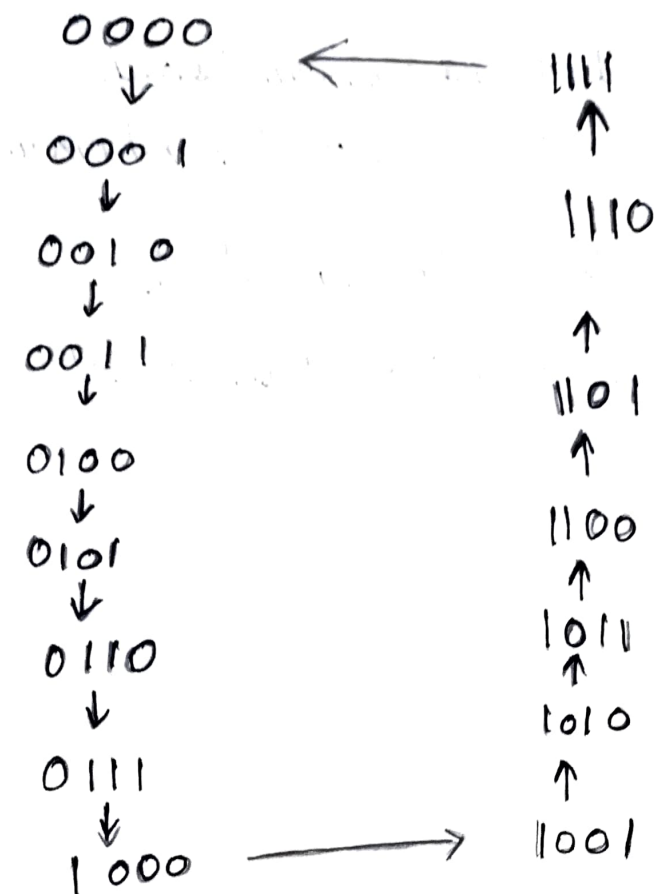
Name: Design a mod16 Asynchronous up counter

Aim: Design a mod16 Asynchronous up counter.

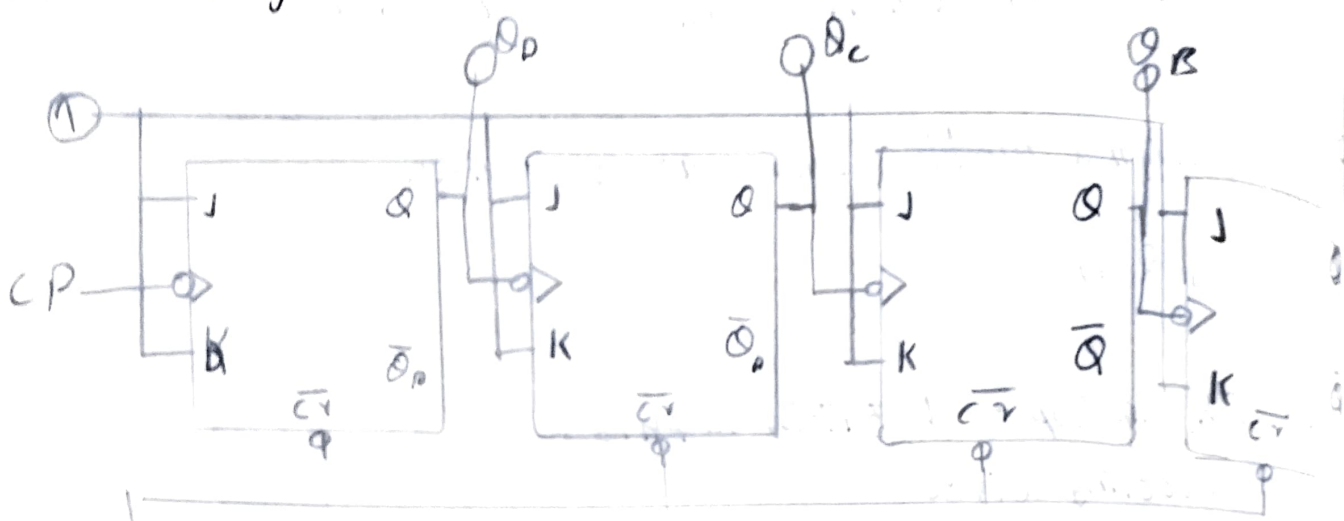
Apparatus Required:

1. JK flip flop - IC 7473/IC 7476
2. connecting wires

State diagram:



Circuit diagram:



Practical procedure:

- 1) ICs are placed on the connecting board.
- 2) Connections are made as per the designed circuit.
- 3) Power supply to the board is turned on.
- 4) Circuit is verified as per the truth table of circuit.

clock:

Observations and

Counter is a device which stores (and sometimes displays) the number of times a particular event or process has occurred, often in relationship to a clock signal. Counter are used in digital electronics for counting purpose, they can count specific event happening in the circuit.

Asynchronous counter we don't use universal clock, only first flip flop is driven by main clock and the clock input of rest of the following flip flop is driven by output of pervious flip flops.

MOD Counters are cascaded counter circuits which count to a set modulus value before resetting. The job of a counter is to count by advancing the contents of the counter by one count with each clock pulse.

Name: kaushik Gupta

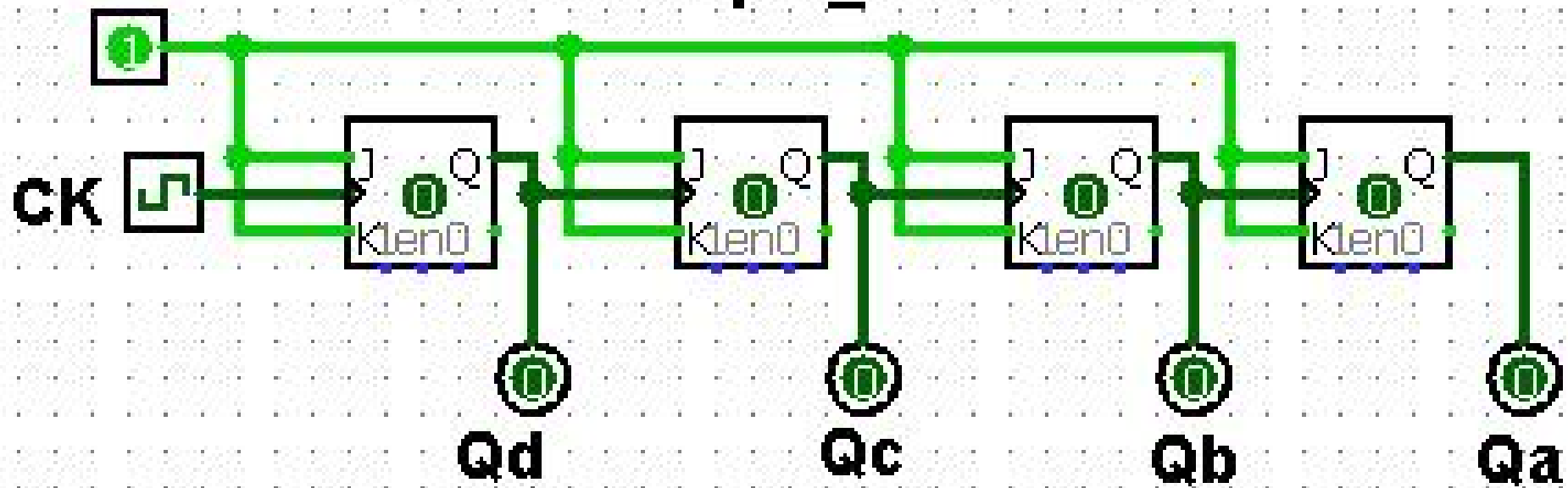
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Sign:

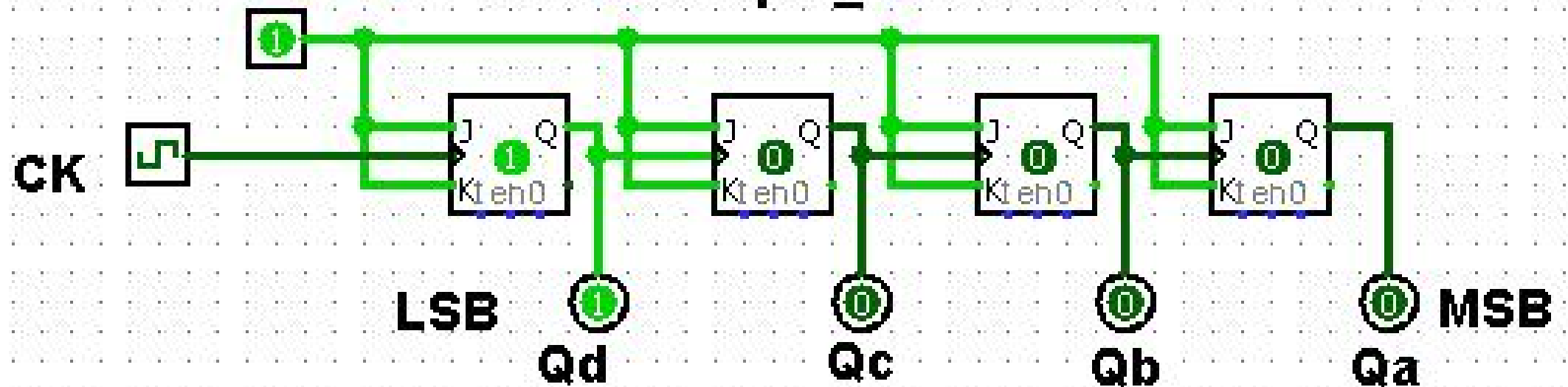
Kaushik

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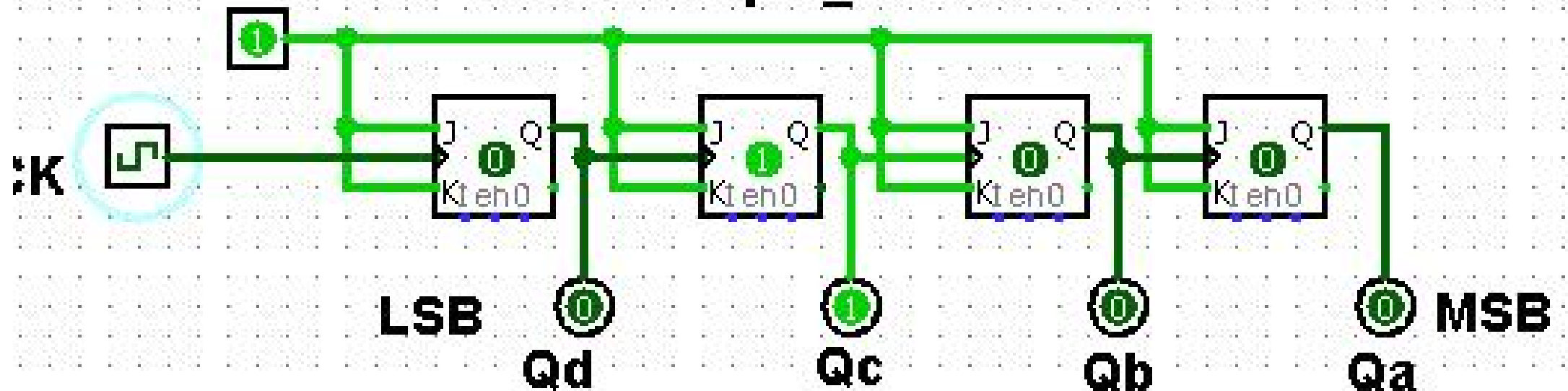
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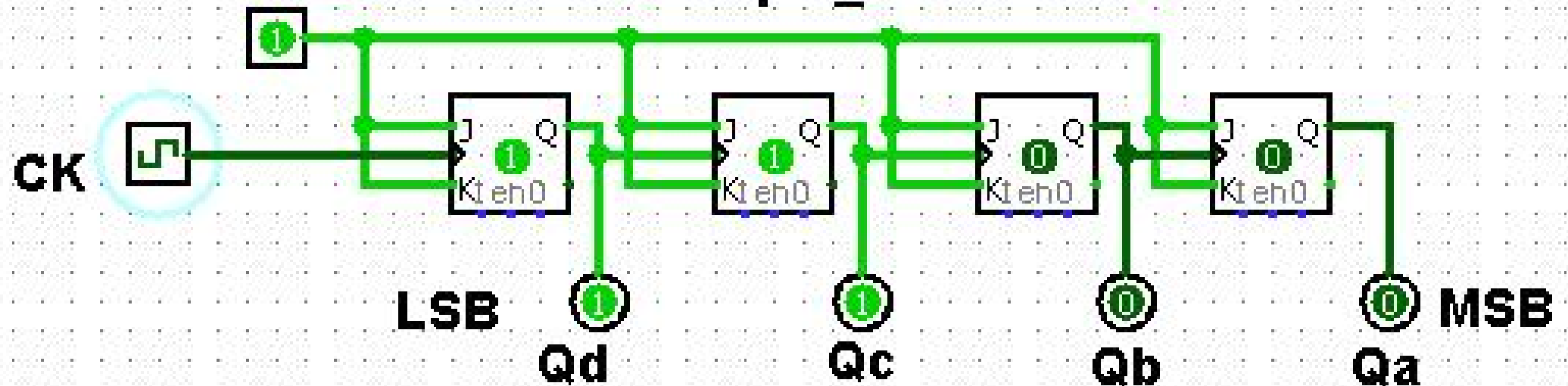
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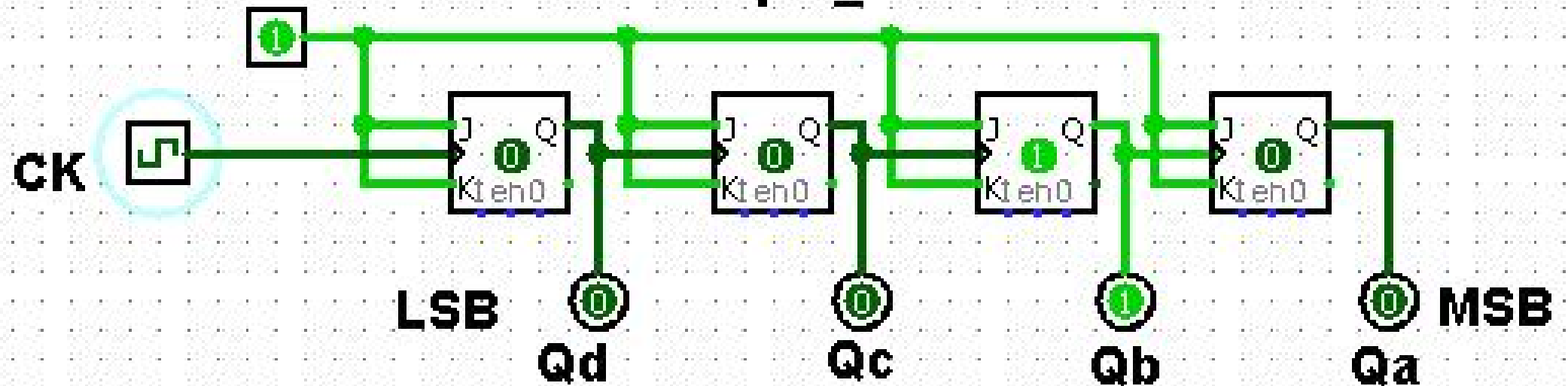
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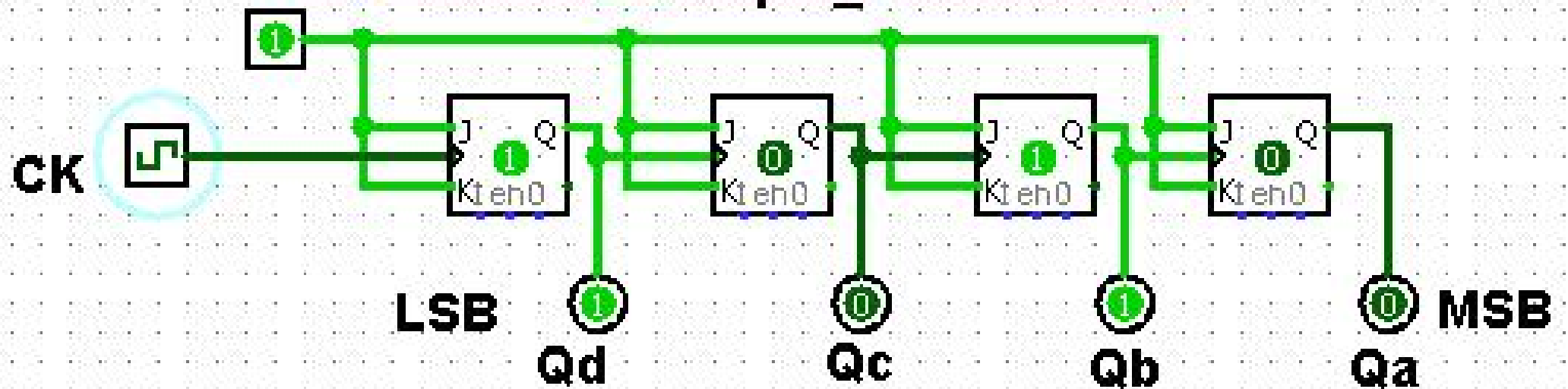
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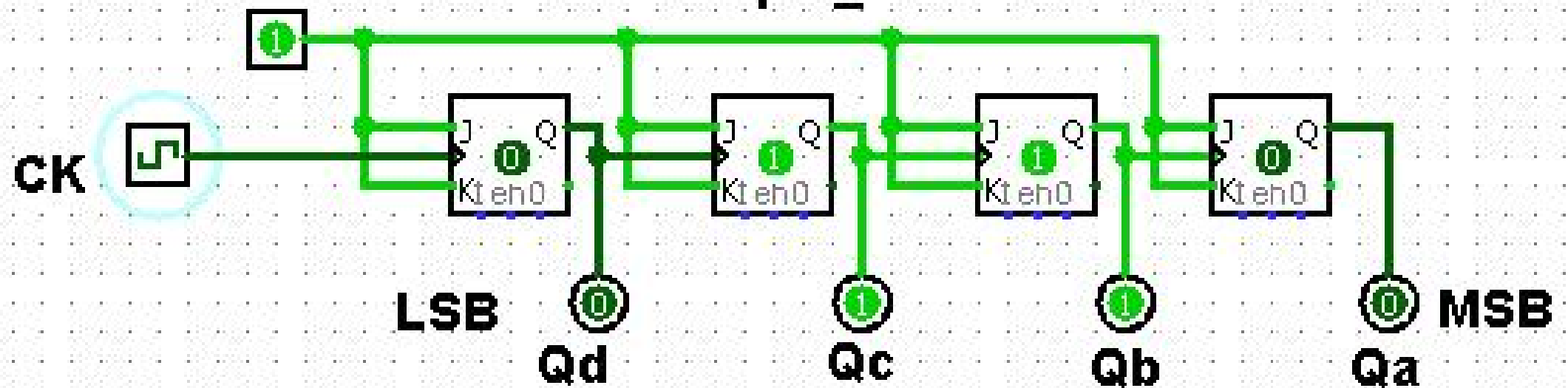
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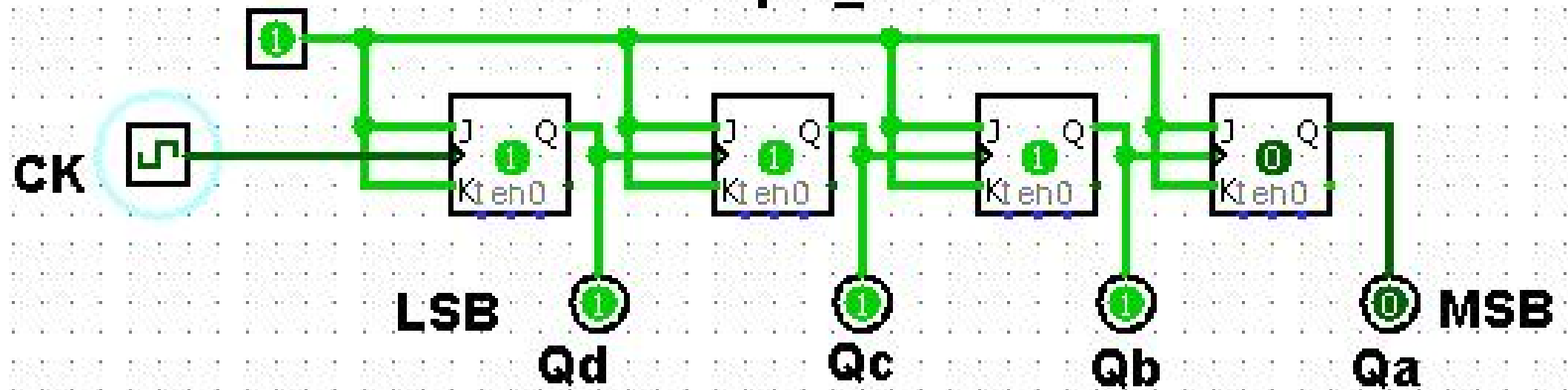
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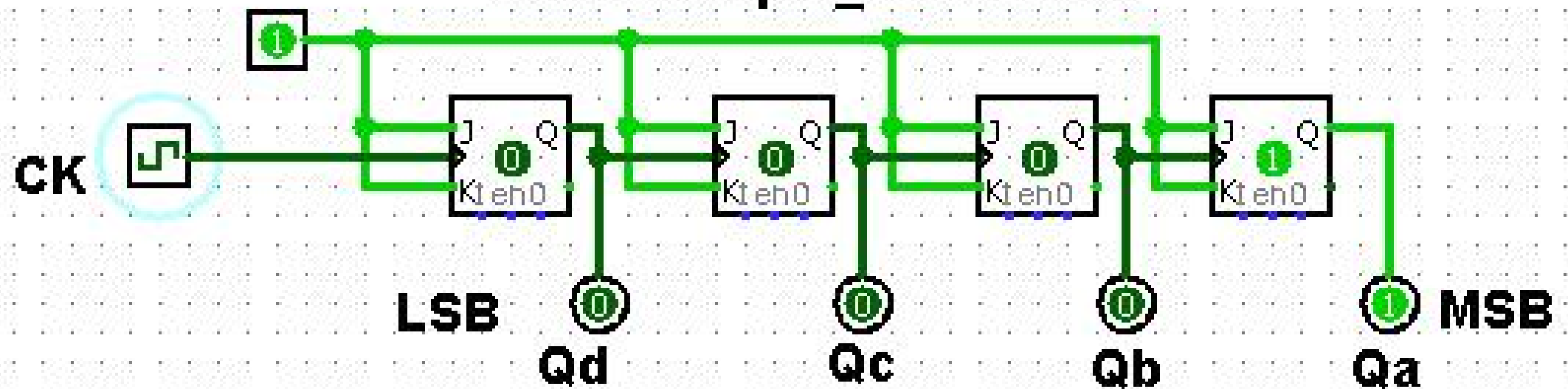
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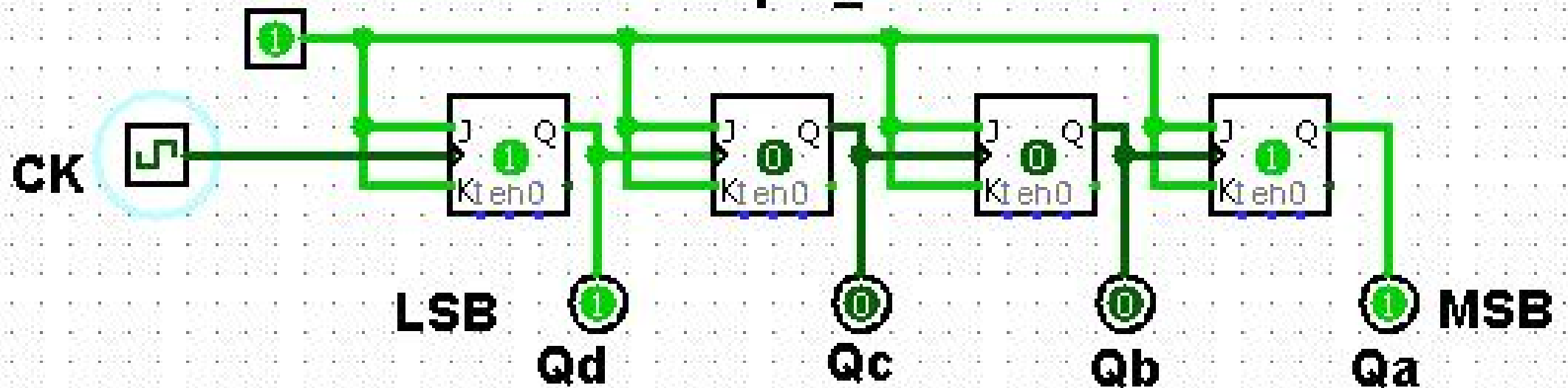
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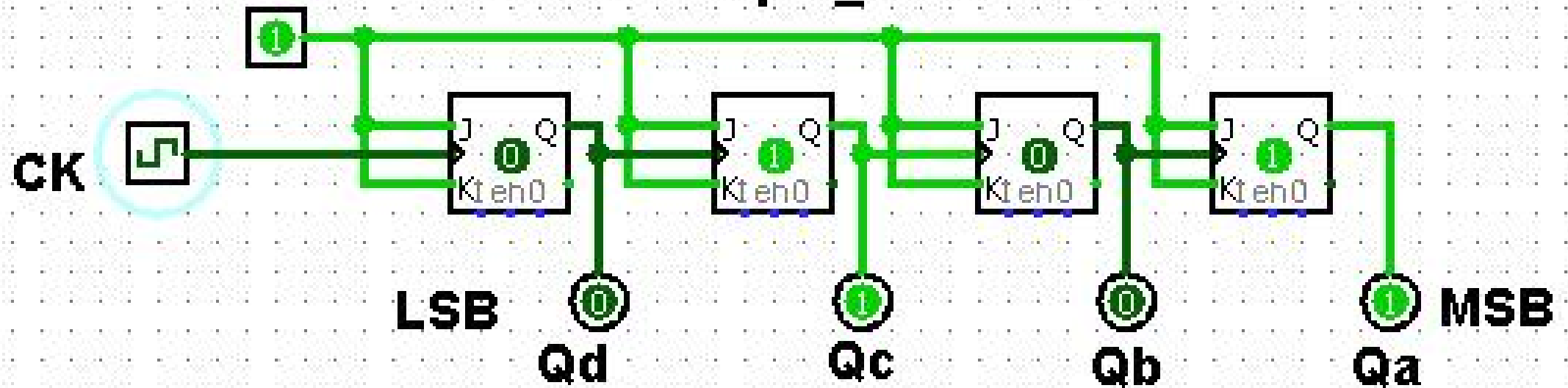
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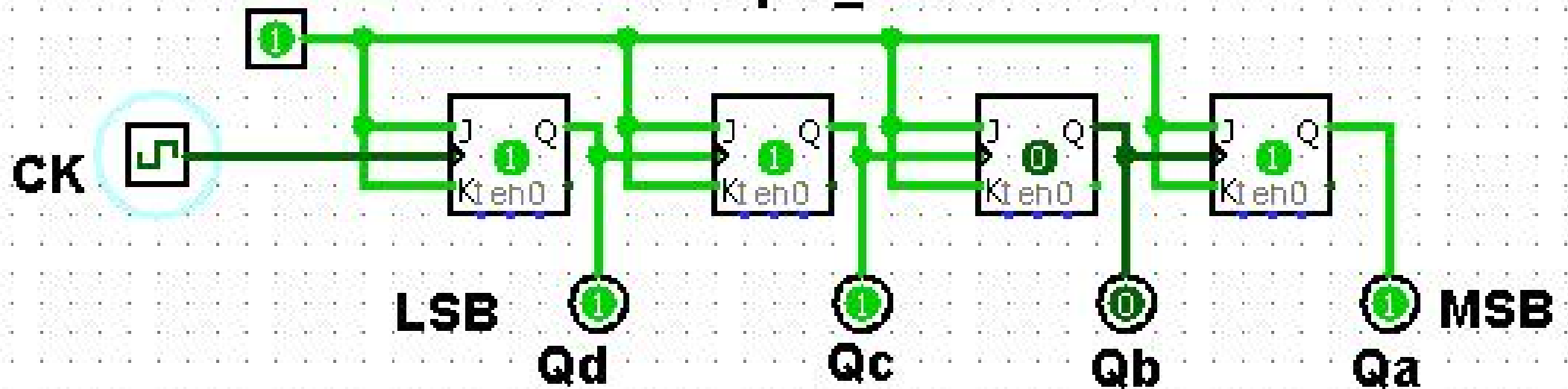
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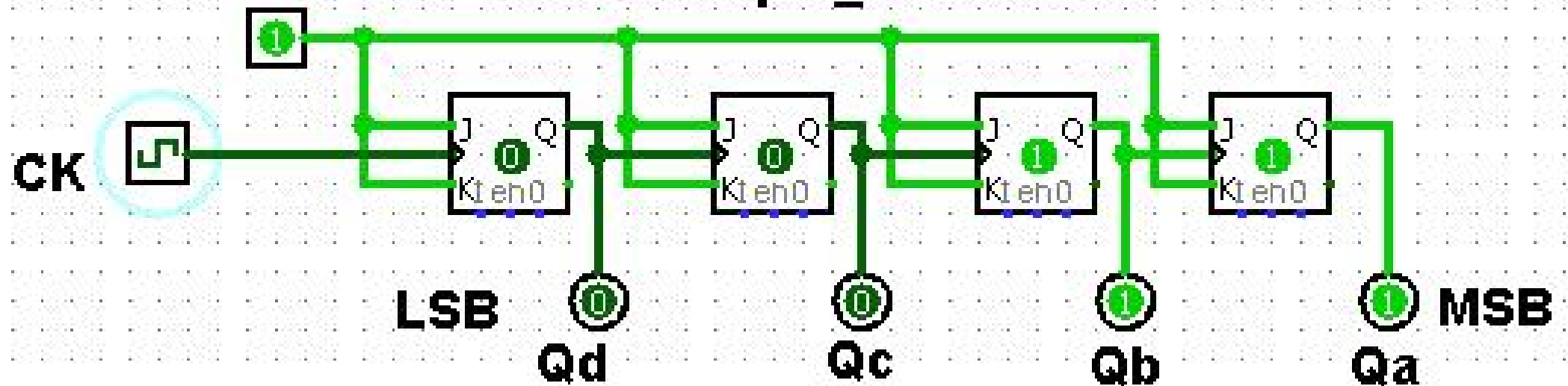
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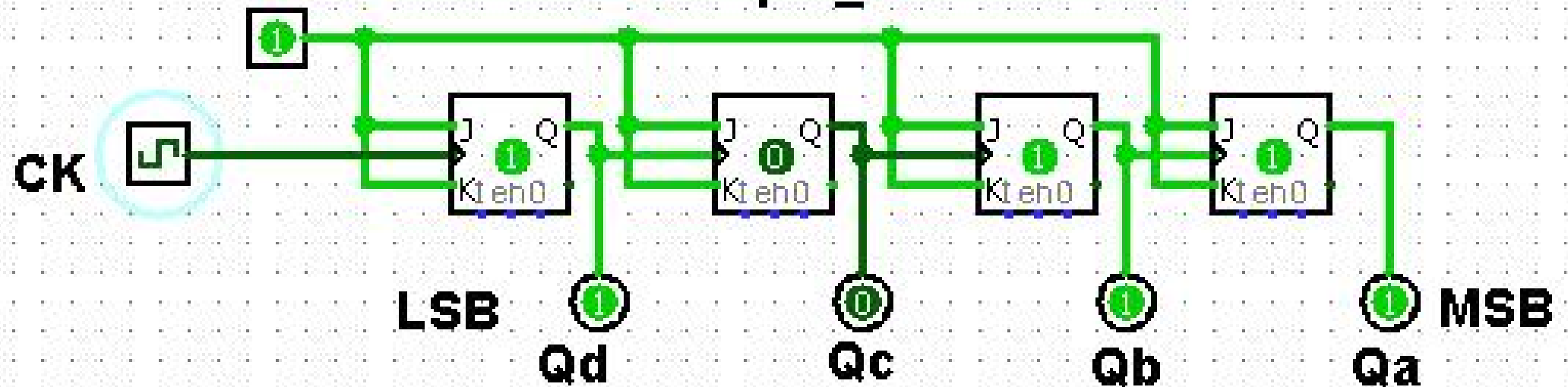
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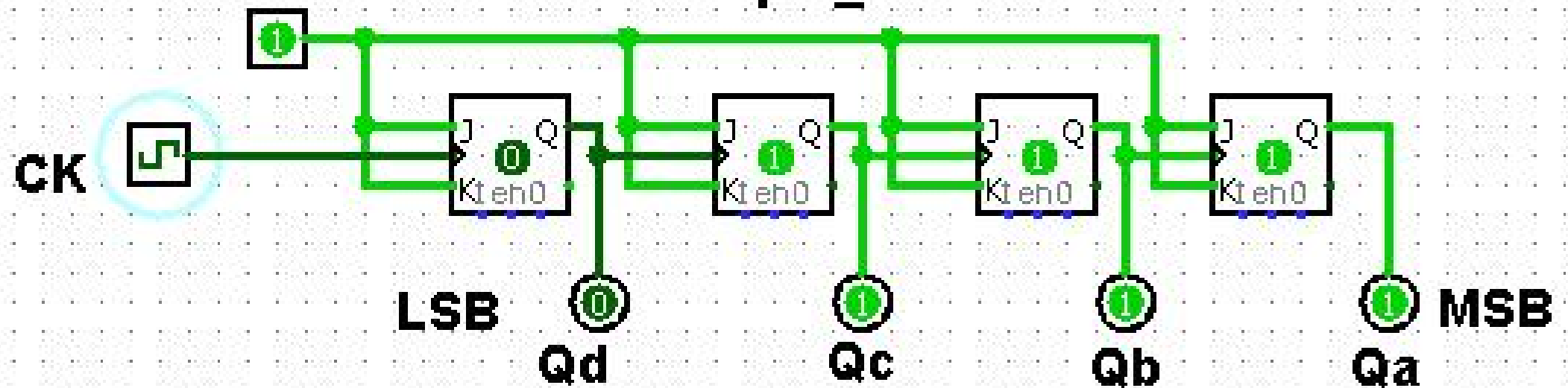
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