CMP:11

Date:

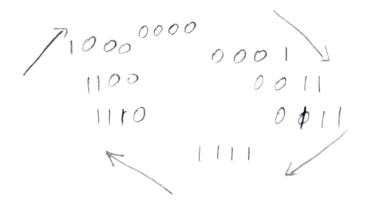
Aim: To construct 4 bit Johson counter or twisted coonter.

Aparatus: - J K flip Slop

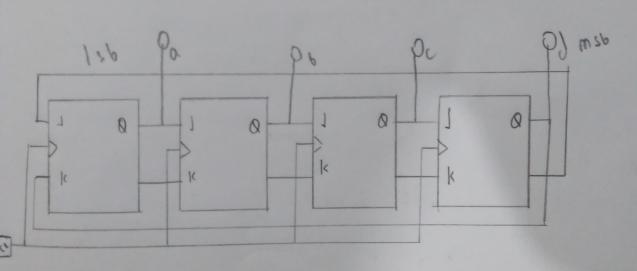
- Connecting wire

- Power Supply.

Theory: Johnson Counter or twisted counter is another Shist register with a teedback enactly some as ring counter. Enecpt for this the the last inverted output is connected back to the first Slip Ilop

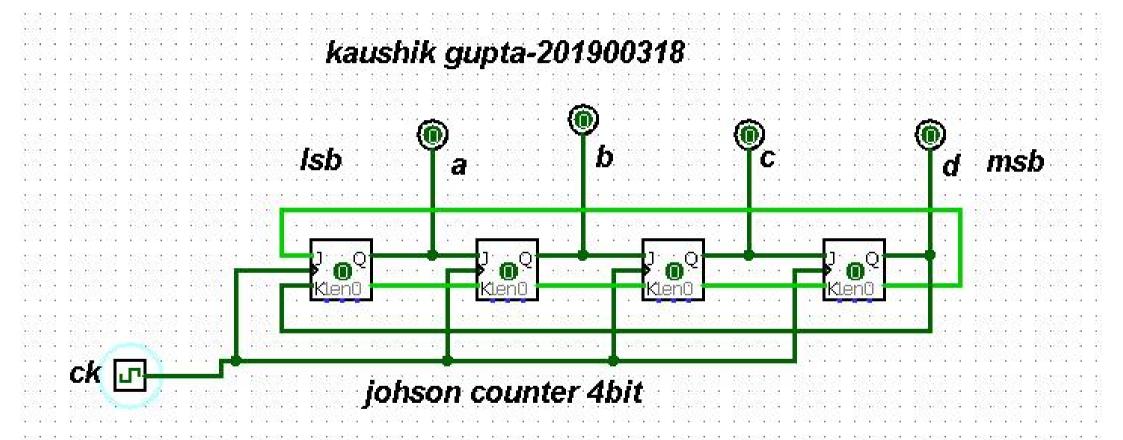


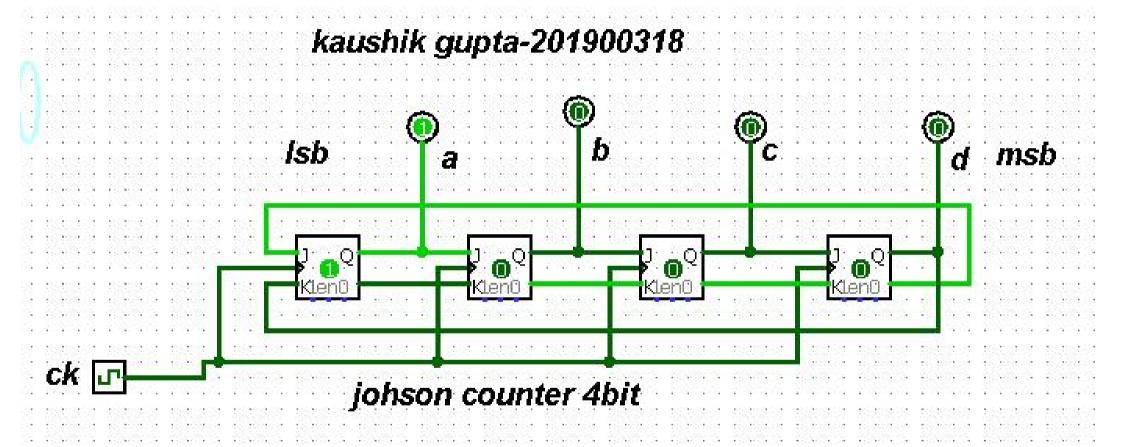
Presentstate	Nent state	excitation table.
0,000000	Quan QBn Qcn Qo	Jaka Joko Jcke Joko
0000	00.01	0 × 0 × 0 × 1 ×
0001	00 1 1	0 × 0 × 1 × X 0
0011	10111	0 × 1 × × 0 × 0
		1x \$0 x0 x0
		x0 x 0 x 0 x 1
1111	1110	XO X O XO
1110		xo xo x1 ox
1110	1100	
1100	1000	X 0 X1 0 L 0 +
11 0		XI OF OL OX
1000	0000	×I
0000	+ + + ×	
0100	XX XX	

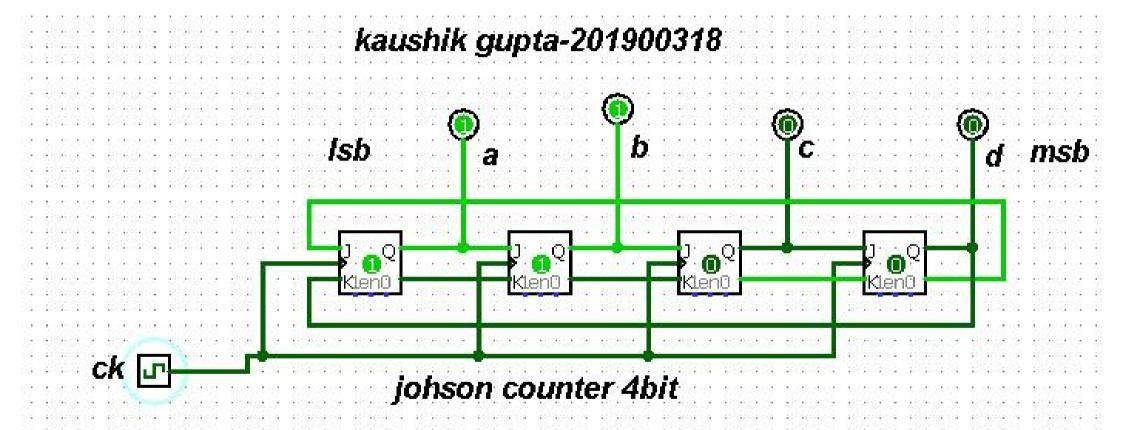


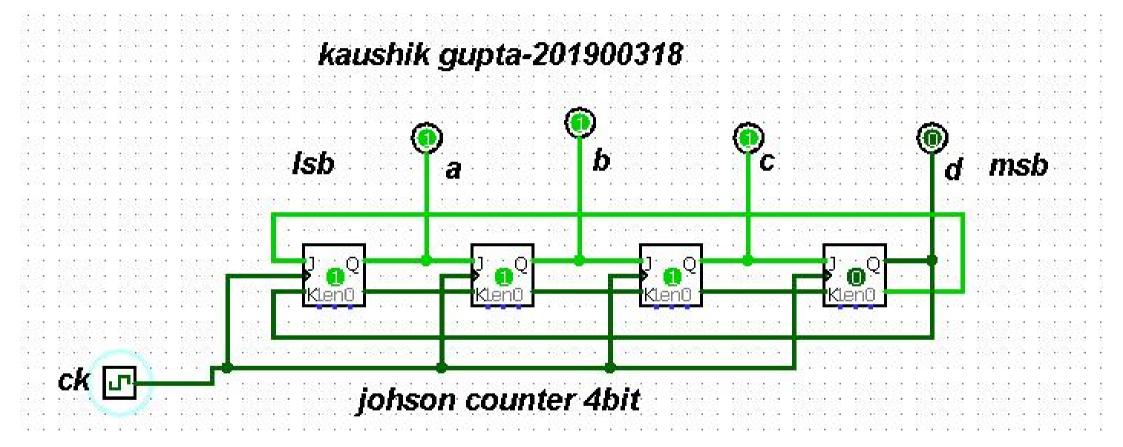
Practical procedure:

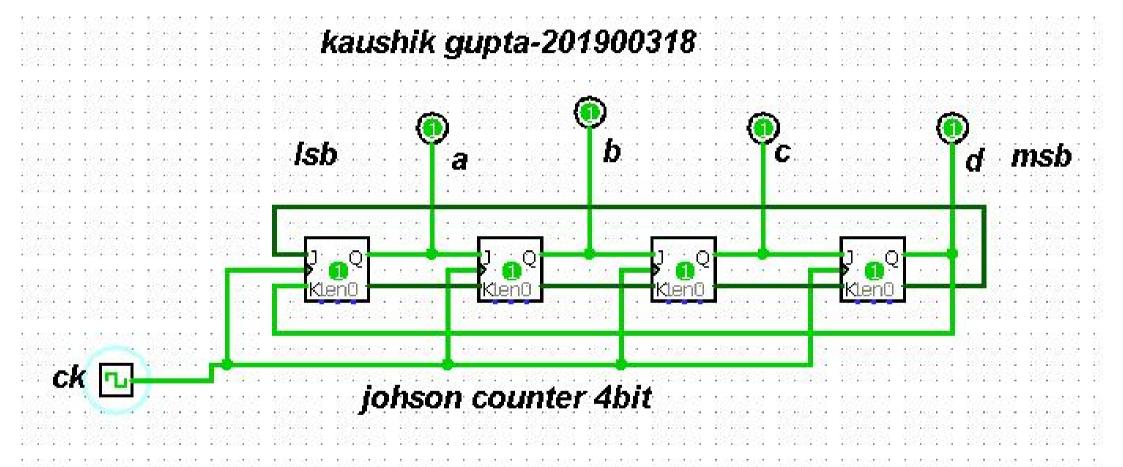
- 1) Ic's are placed on bread board.
- 2) make proper connections & make sore of a bigent Power Supply.

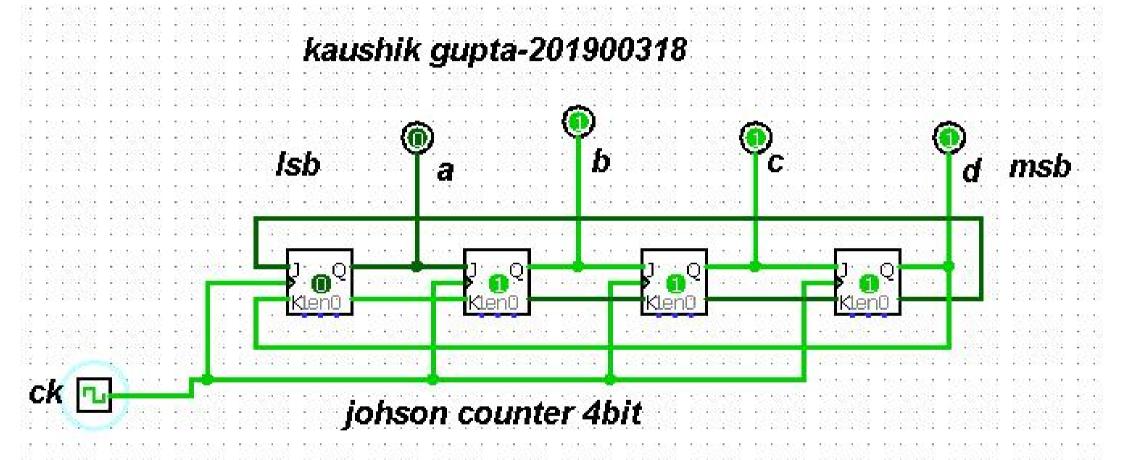


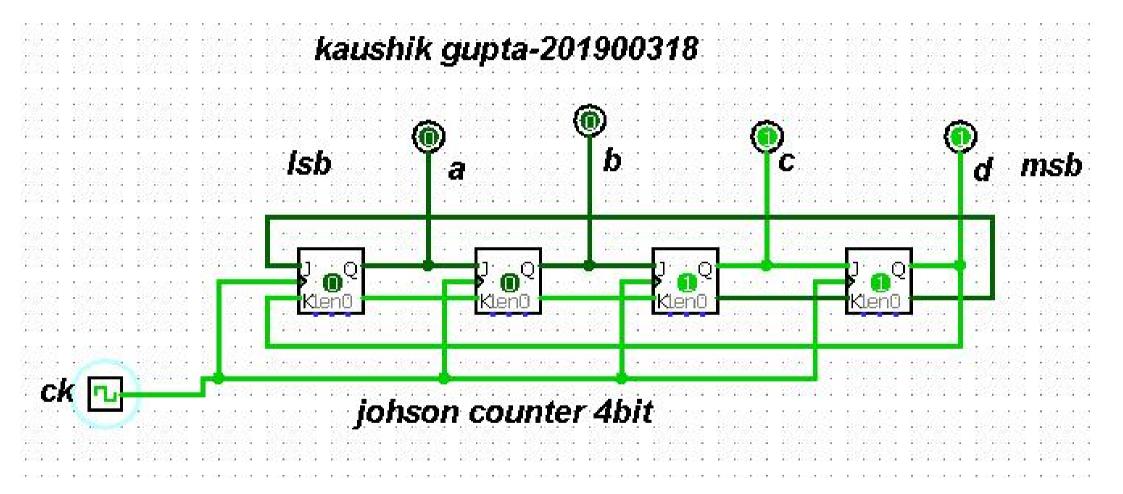




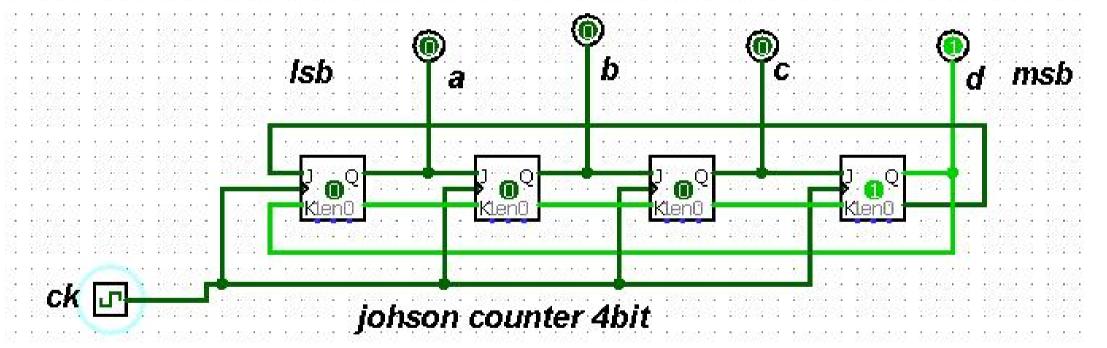








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Student's Conclusion:

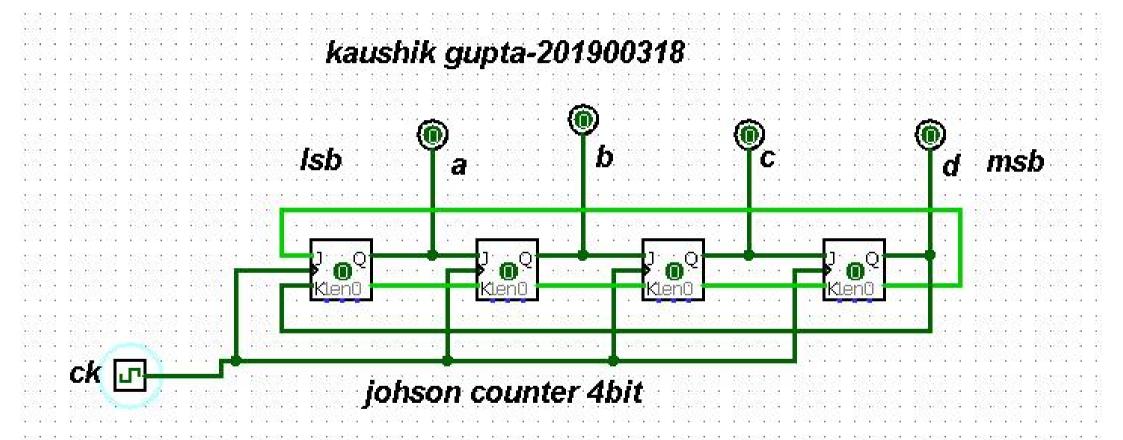
- Counter is a device which stores (and sometimes displays) the number of times a
 particular event or process has occurred, often in relationship to a clock signal.
 Counter are used in digital electronics for counting purpose, they can count specific
 event happening in the circuit.
- Initially, all the flip flops in ring counter are reset to 0 by applying CLEAR signal.
 Before applying the clock pulse, we apply the PRESET pulse to the flip flops which assigns the value '1' to the ring counter circuit. For each clock signal, the data circulates among all the 4 flip flop stages of ring counter.
- These 4 staged ring counters is called Mod 4 ring counter or 4-bit ring counter. To circulate the data correctly in the ring counter, we must load the counter with required values like all 0's or all 1's.

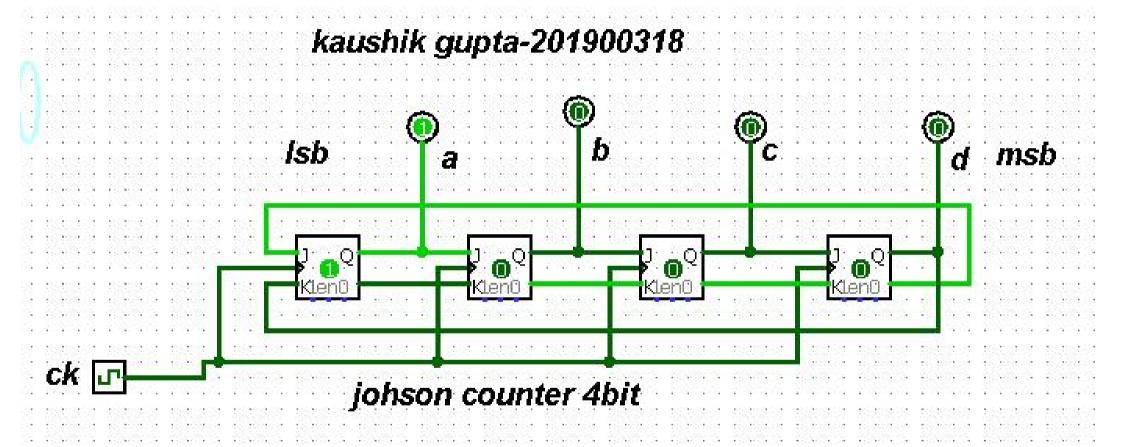
Name: Kaushik Gupta

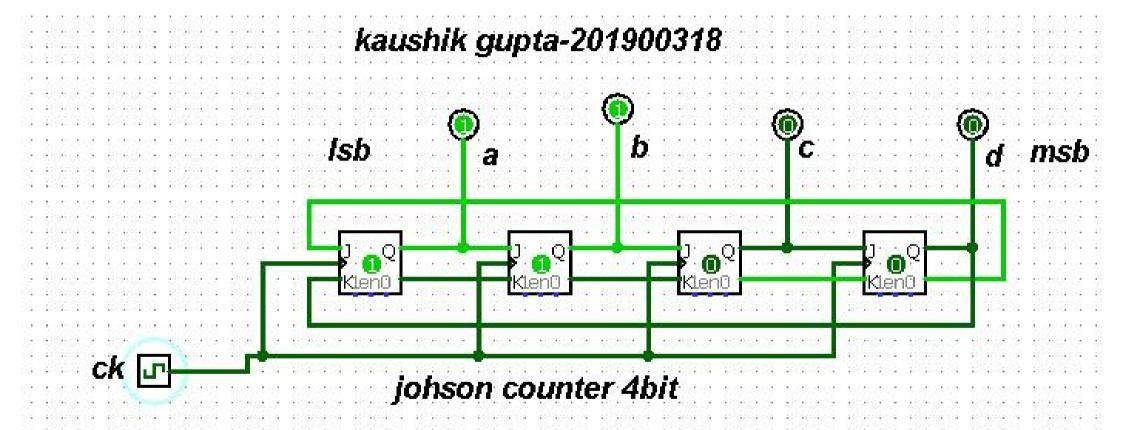
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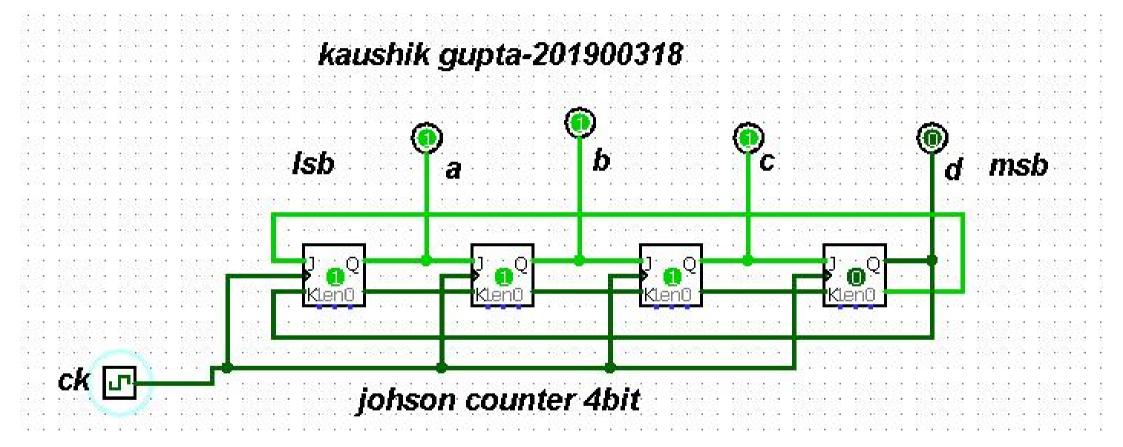
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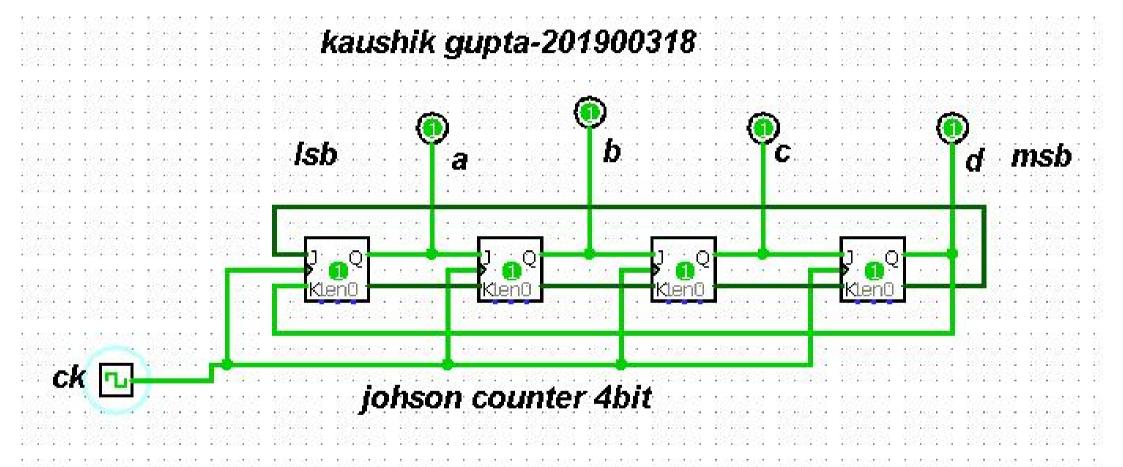
Sign:kaushik

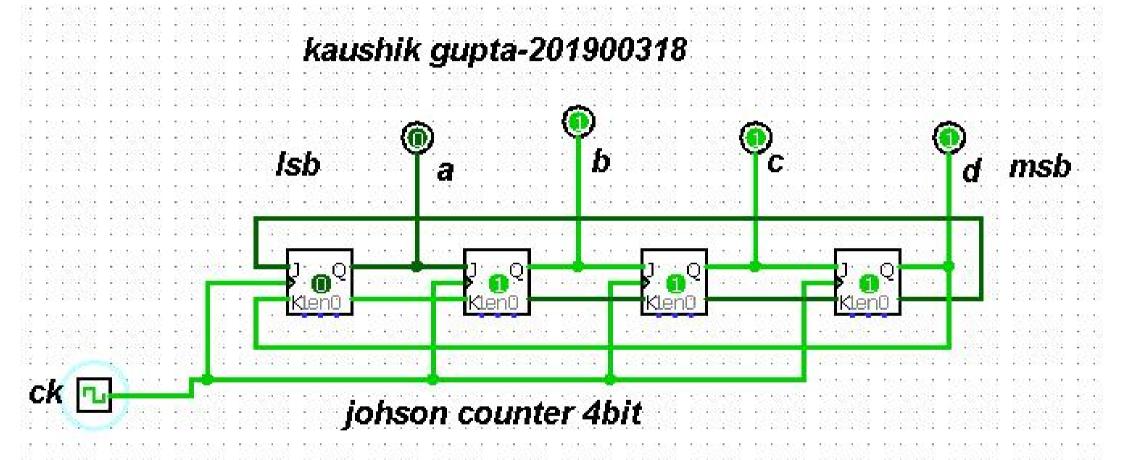


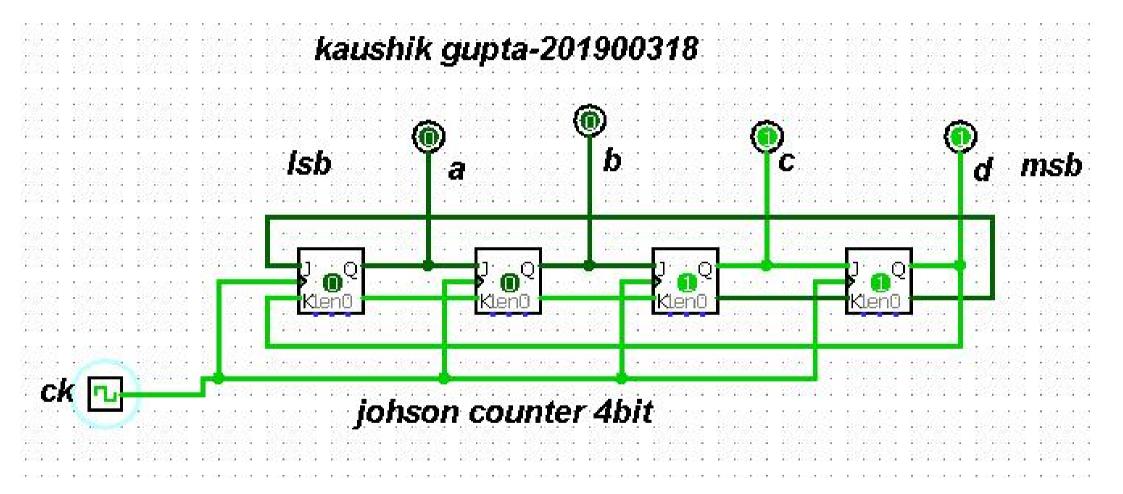












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