

USRP Rev 4.2

10-2-2006

CLOCK IN

CLOCK OUT

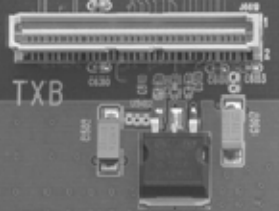
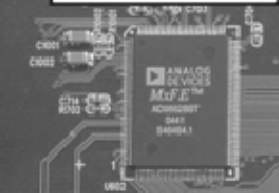
RXB

TXA

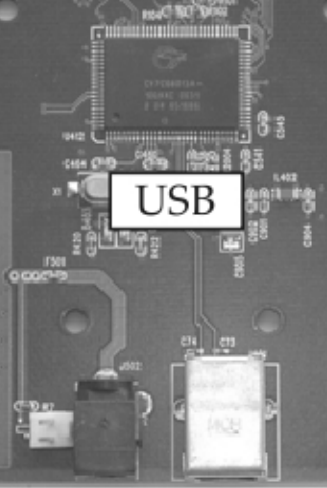
FPGA

ADC/DAC

ADC/DAC



USB



Ettus Research LLC  
USRP Ser#  
Tested: 10/10/06