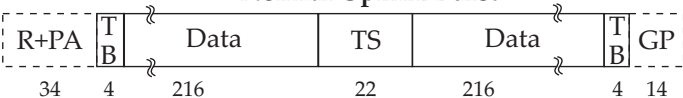
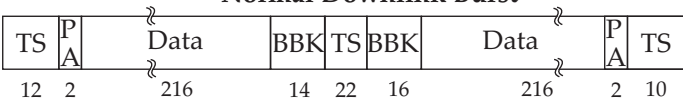


1 full slot = 510 bit periods = 255 symbol periods = 14.17 ms

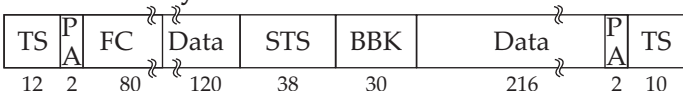
Normal Uplink Burst



Normal Downlink Burst



Synchronization Downlink Burst



R+PA Ramping + PA linearize TB

Tail Bits

GP Guard Period

TS Training Sequence

PA Phase Adjustment

STS Synchronization TS

FC Frequency Correction

BBK Broadcast Block