

**PSM**

Generate  
VHDL

**Template  
Script**

Generate  
TCL

**Functional Code:**

tx\_chain.vhd  
rx\_chain.vhd  
subsystem.vhd  
timing\_ctrl.vhd  
...

**Custom  
Skript:**  
model.tcl

**Platform Specific**

**Netlists:**  
sdrioring.vhd  
vpbe.ngc  
vpfe.ngc  
...

**Binary:**  
model.bin