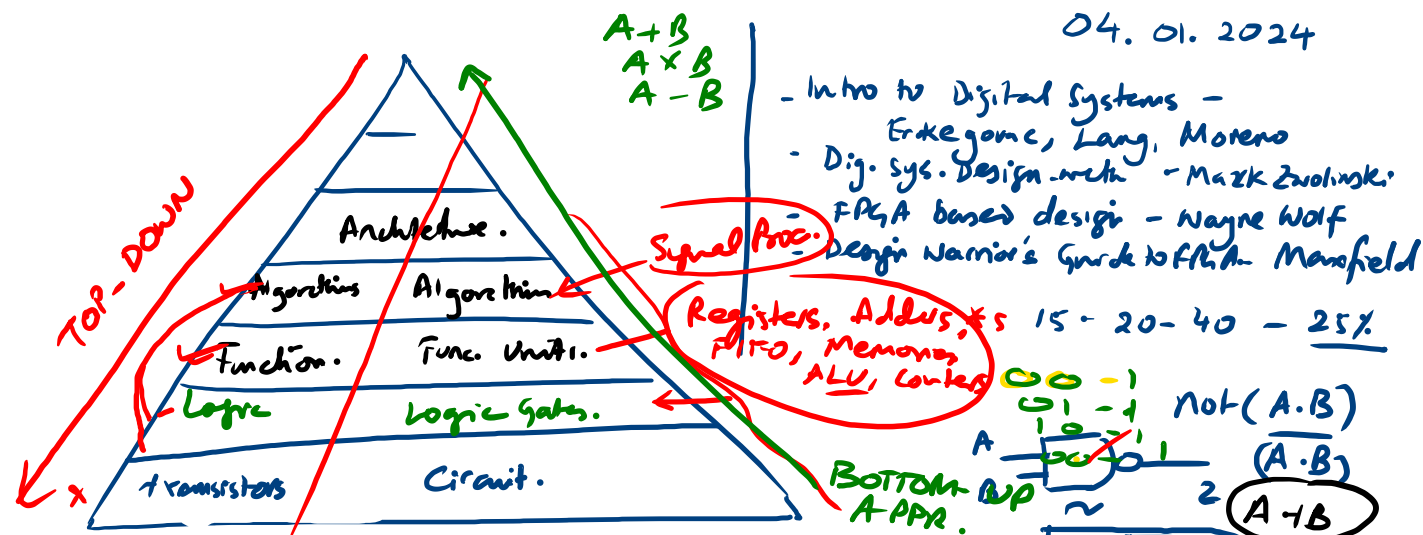
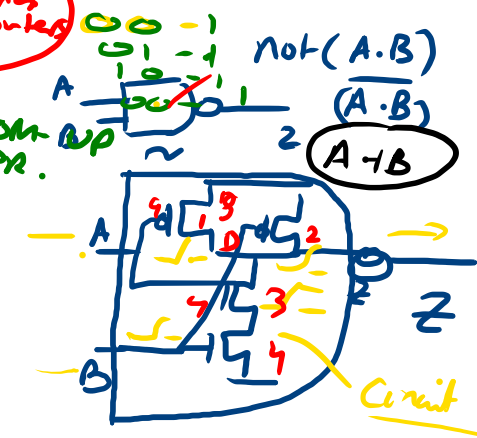


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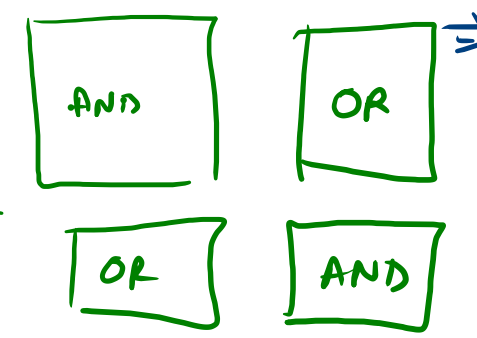
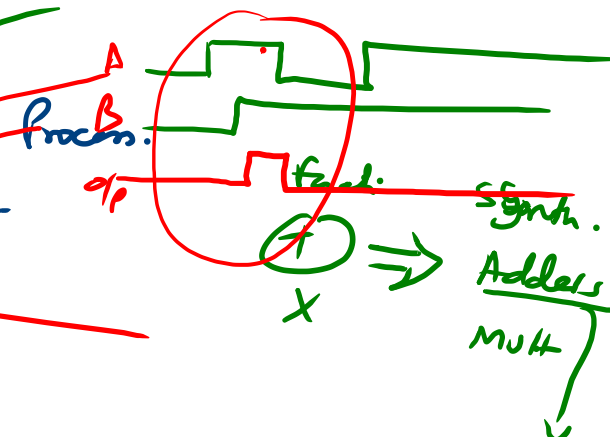
- Intro to Digital Systems - Erkegome, Lang, Moreno
- Dig. Sys. Design meth - Mark Zwolinski
- FPGA based design - Wayne Wolf
- Design Warrior's Guide to FPLA - Mansfield



Proc. C. Complex

Xilinx Altera Actel

- Specification
- Prel. study / Idear
- Functional Descriptions
- Digital Design
- Design Descriptions
- Implement (Opt, Improve)
- Verify
- Synthesis
- Manufacture.



$$(A+B+C)(D+E+F) \text{ POS}$$

$$(AB+BC)(\overline{CD}+\overline{EF}) \text{ SOP}$$

AND OR AND

- PLD:
- Floating Gate
  - SRAM
  - Antifuse Tech.

