

Embedded Hardware Design. 3-0-2-4

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FB2, 2104

M - 10-1050
W - 10-1050
Th - 12-1250

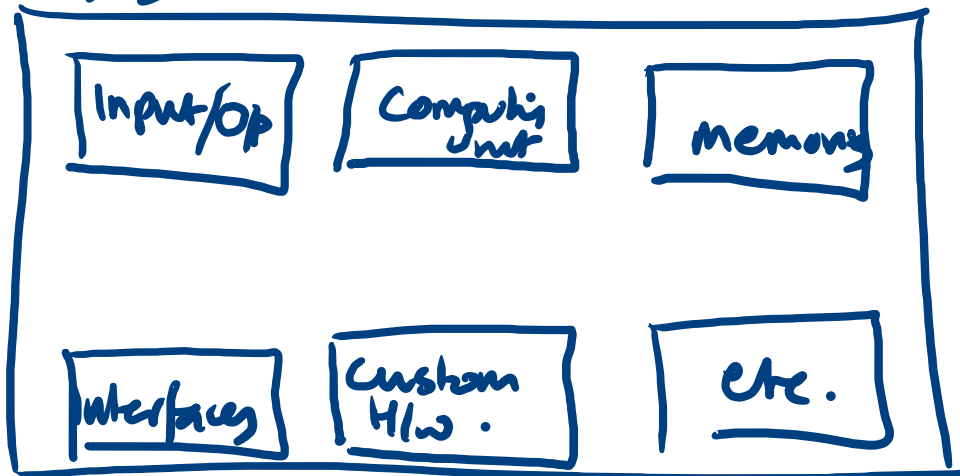
BMi - Sec A LT2
YAg - Sec B LT3

Theory - 75 %
Practice - 25 %

CAD:

- Intro.
- Prog. Logic
- Arithmetic Circuits.
- Encoding
- Architecture.
- Test/JTAG

Dig. System:



4004.
8008 } And
SOP. $(A+B)(C+D)$ PLA
POS $(A+B)(C+D)$ PAL

Digital System

