
Embedded Hardware Design – EL203

LAB_9

Aim: To perform different Embedded Task using Verilog HDL and FPGA.

Lab Task

1. LED and Switch Interfacing on FPGA board.

Verify

(a) Bitwise, (b) Logical, (c) Reduction, (d) Equality, (e) Relational, (f) Conditional, (g) Concatenation and Replication, (h) Shift and (i-l) Arithmetic Operators using FPGA
Keep UCF file same for all the modules.



