

EL203 Embedded Hardware Design

LAB_7

Aim: To perform different Tasks using Verilog HDL.

1. An embedded system required JK flip-flop to implement a task. Write and verify HDL of JK flip-flop using behavioral description in Verilog.

2. (i) For an automated embedded system, design 4-bit loadable up/down binary counter using if-else statements

(ii) Write a test-bench to check functionality of the module in 2(i).