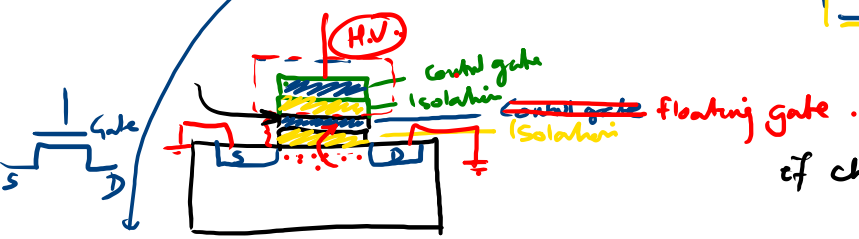
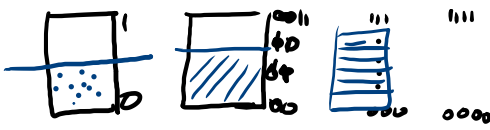


08.01.2024

PLD:



SLC, MLC, TLC, QLC

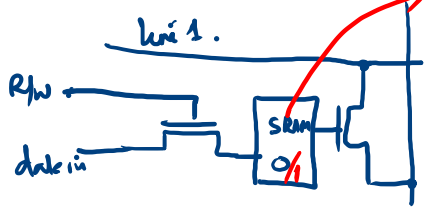
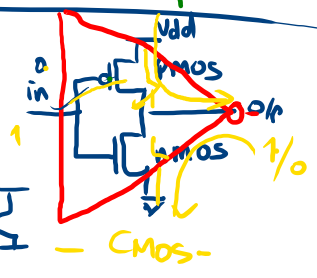
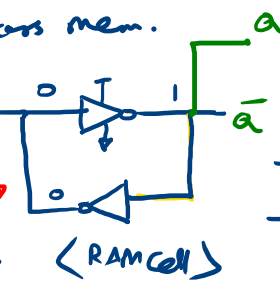


$$Q = -dC$$



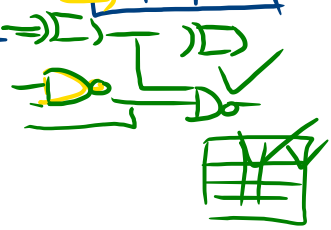
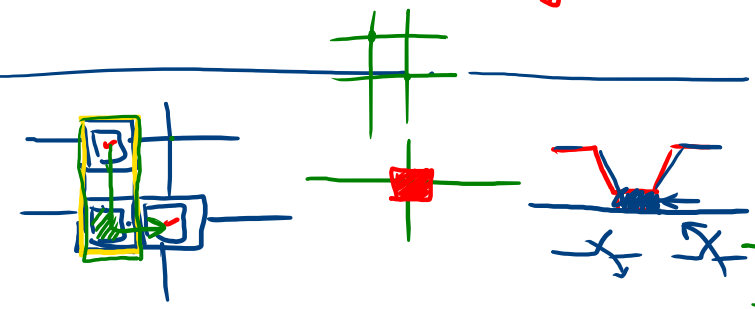
SRAM: Static Random Access Mem.

read/write.  
data-in



Before After

	A	B	0/1
→	0	0	0
→	0	0	1
→	0	1	0
→	0	1	1
→	1	0	0
→	1	0	1
→	1	1	0
→	1	1	1



Basic PLD:

- PLAs — Both AND & OR are programmable — SOP
- PALs — AND is prog. OR is fixed — POS

