KAUSHLESH CHAURASIYA

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EDUCATION

DELHI TECHNOLOGICAL UNIVERSITY   New Delhi
Bachelor of Technology   Dec 2021 – May 2025
Major in Electronics and Communication Engineering;

Technical Skills and Interest:

Languages: C++, C , Verilog , VHDL , Python
Core Concepts: Digital & Analog Electronics, VLSI Design, CMOS Circuit Design, Static Timing Analysis(STA) , Computer Architecture, Operating Systems, Data Structures & Algorithms, Threading, Inter-process Communication
Tools: Xilinx ISE Design Suite, Logisim, LT Spice, MATLAB, Multisim, Cisco Packet Tracer
Telecom & Networking Skills: Analog & Digital Communication, Wireless Communication Basics, TCP/IP, Socket Programming, Signal Processing

PROJECTS:

ALU 8bit using behavioral modeling in Verilog | *Verilog, Xilinx ISE Design Suite*   *Mar 2024 – May 2024*

- o   Designed and verified an 8-bit Arithmetic Logic Unit (ALU) using Verilog and Xilinx ISE Design Suite 14.7. Implemented various arithmetic and logical operations with precision, demonstrating a strong understanding of RTL design and digital design principles. The project focused on improving the execution speed of different operations within the ALU

Digital IC Trainer Kit | *Verilog, Digital Design*   *Jan 2024 – Mar 2024*

- o   Designed and developed a comprehensive digital logic trainer kit for experimentation with fundamental logic gates and advanced digital circuits, including flip-flops and multiplexers facilitating hands-on experimentation with combinational and sequential circuits, enhancing understanding of hardware architecture.

TripEase | *C++ , File Handling , OOP , Data Structures*   *Nov 2023 – Dec 2023*

- o   Built a console-based Travel Management System in C++ with modular OOP design, managing over 1,000 booking records.
- o   Implemented persistent data storage using custom file handling for efficient booking data access.
- o   Enhanced memory usage and streamlined user input flow, resulting in faster booking operations during testing.

ACHIVEMENTS:

Western Digital INNOVATION BAZAR *(Sep 2024)*
- o *Pitched an innovative idea at the APAC Innovation Bazaar and was recognized as one of India's top 10 groups for presenting exceptional solutions. The idea revolved around improving data storage optimization in digital systems, showcasing analytical skills, and proficiency in communication and collaboration...*

ADDITIONAL SKILLS

- **Web Development:** *HTML, CSS, React.js, Next.js, Tailwind CSS, Node.js, Express.js, REST APIs*
- **Databases**: *MongoDB, MySQL*

Extracurricular Activities:

Extension and Outreach *(Social Cause):*
- o *Led a team of 3 to create a productivity platform enhancing focus for 20+ students, achieving a top 5% ranking for innovative social impact ideas under Professor Vijay Pandey.*

Desh Ke Mentor : *Mentored 8 students from Delhi Government schools as part of a Delhi Government initiative*.

Represented the Branch Cricket Team during [2022–2023], showcasing teamwork, leadership, and strategic planning skills.