

CUs Busy



DRAM BW



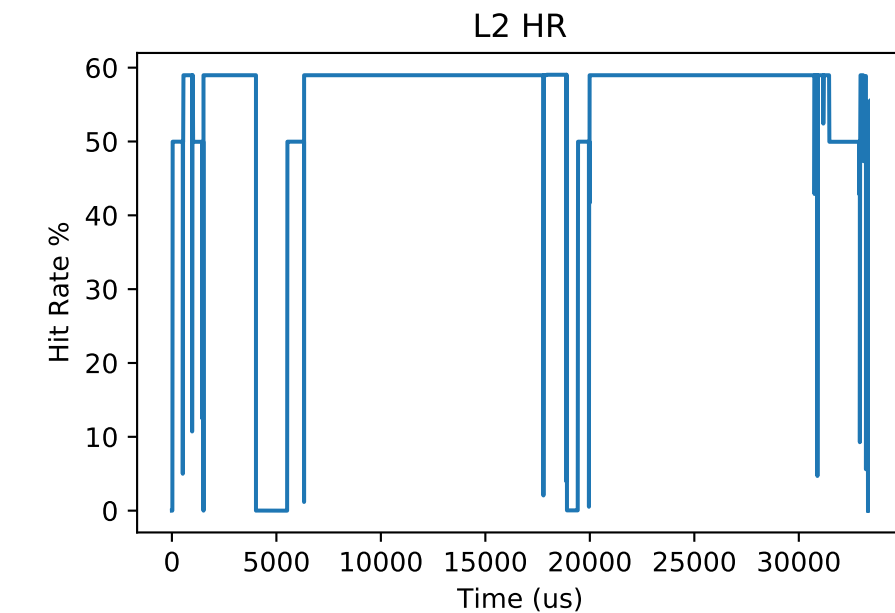
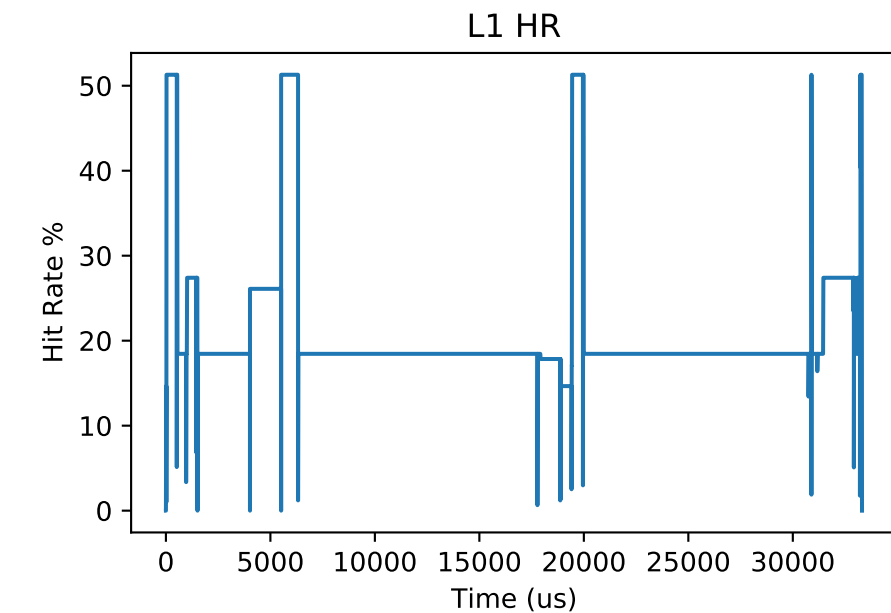
Metrics for bootstrapping



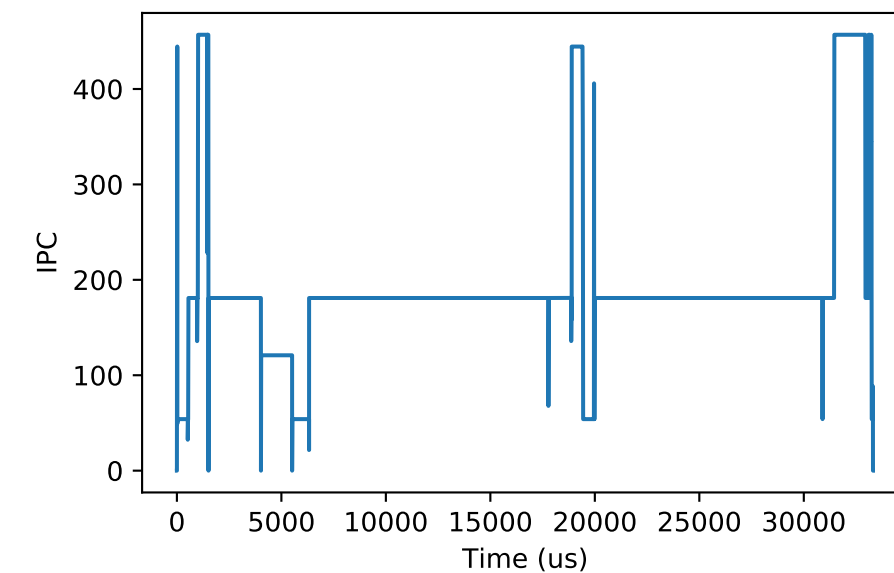
L1 BW



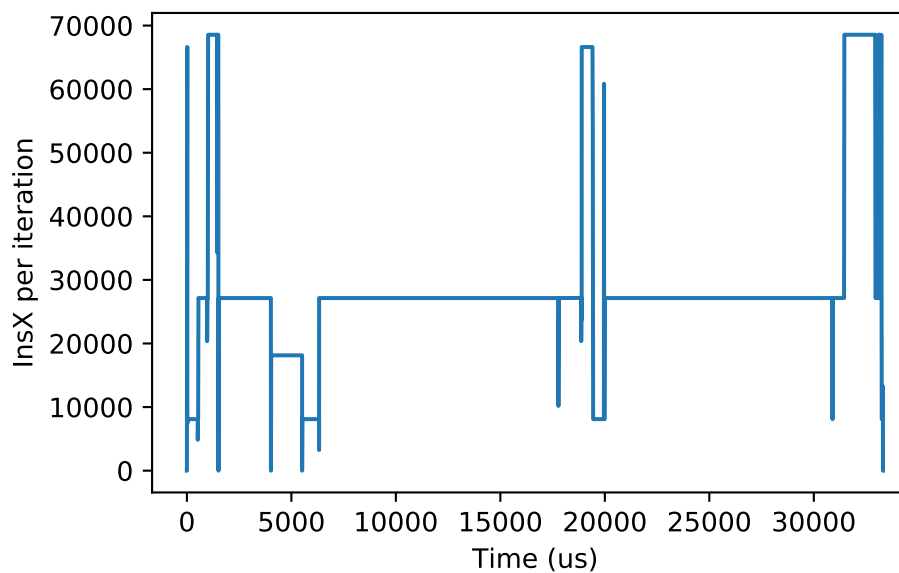
L2 BW



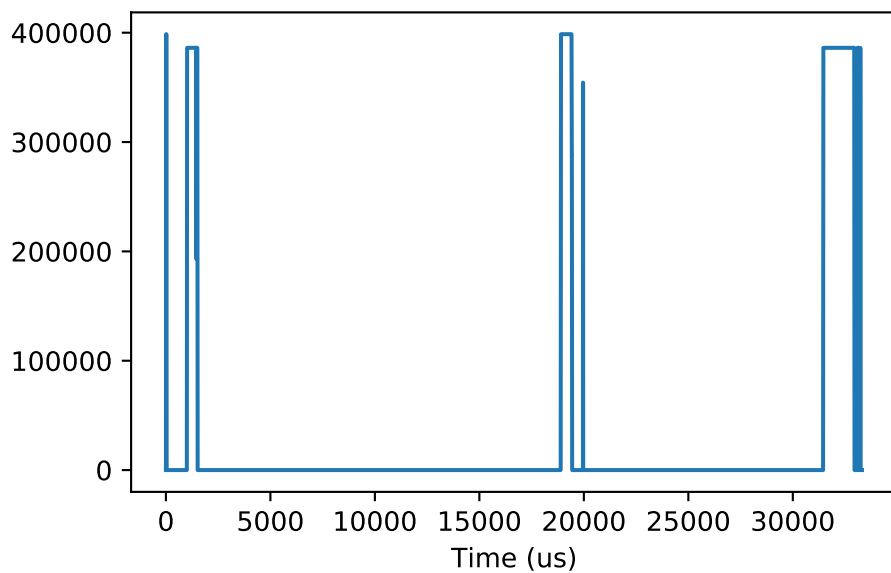
IPC



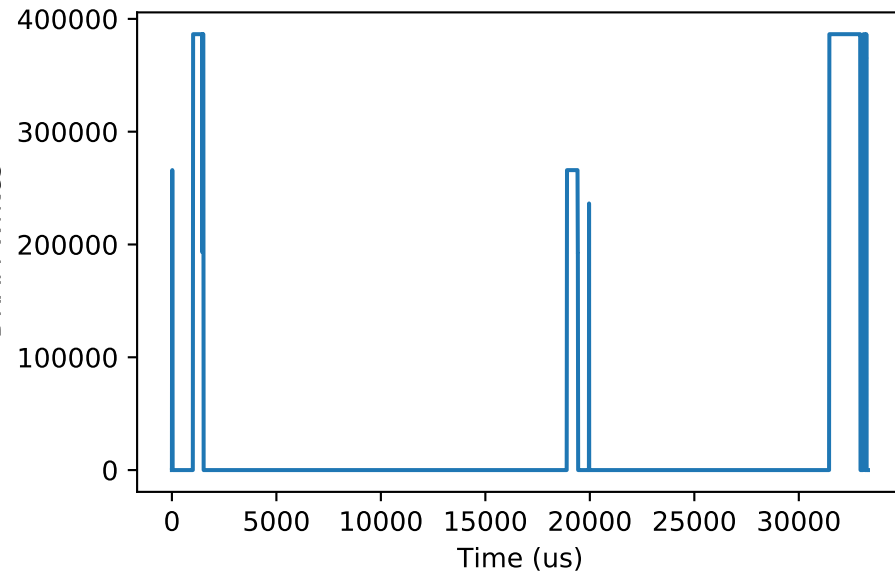
Instructions



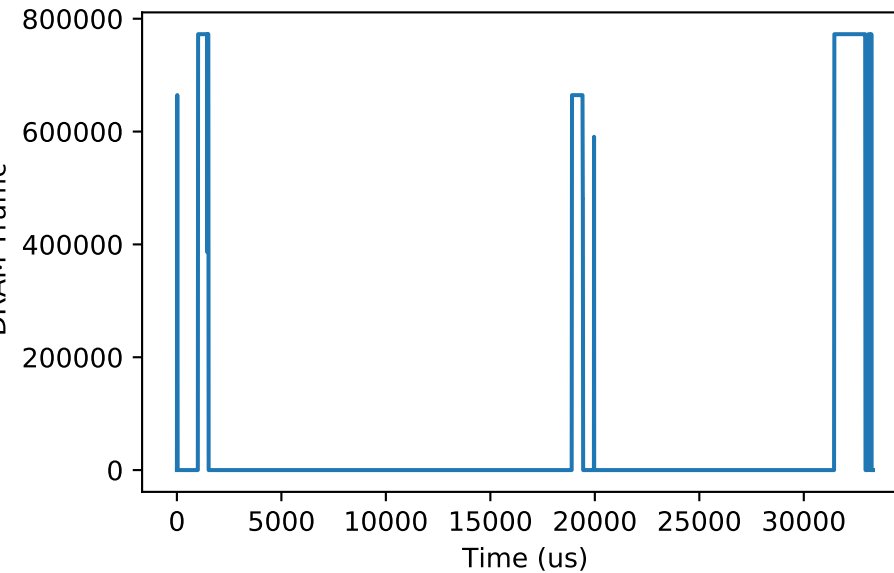
DRAM Reads



DRAM Writes



DRAM Traffic



LDS Utilized (MB)

