

CUs Busy



DRAM BW



Metrics for bootstrapping



L1 BW



L2 BW

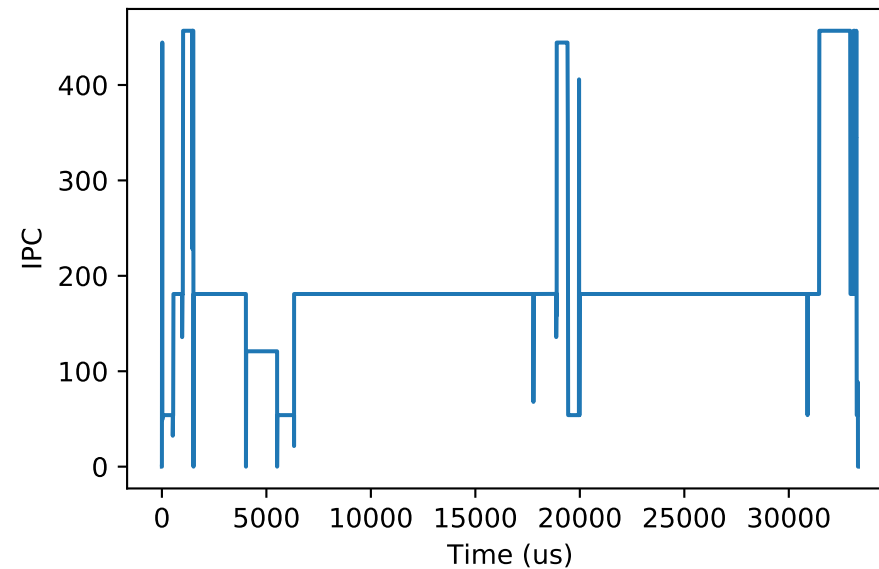
L1 HR



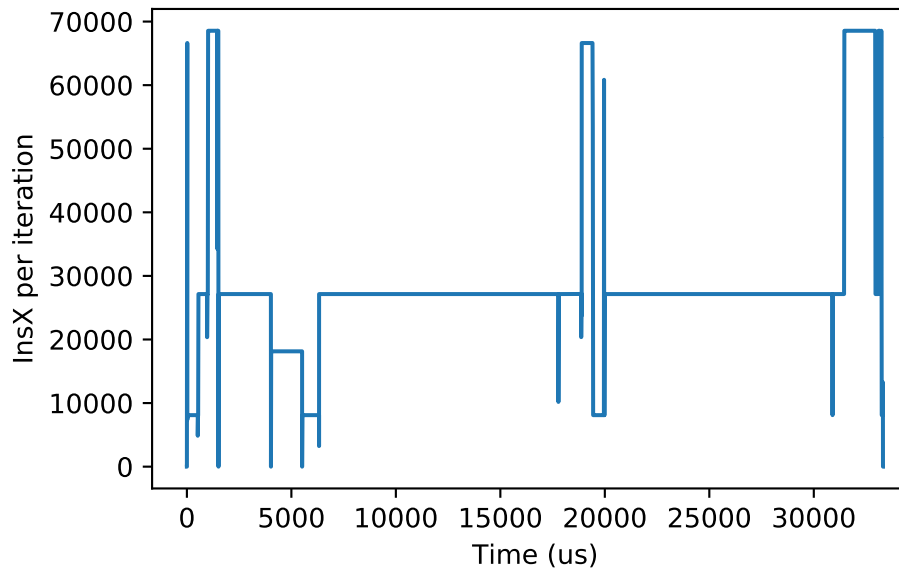
L2 HR



IPC



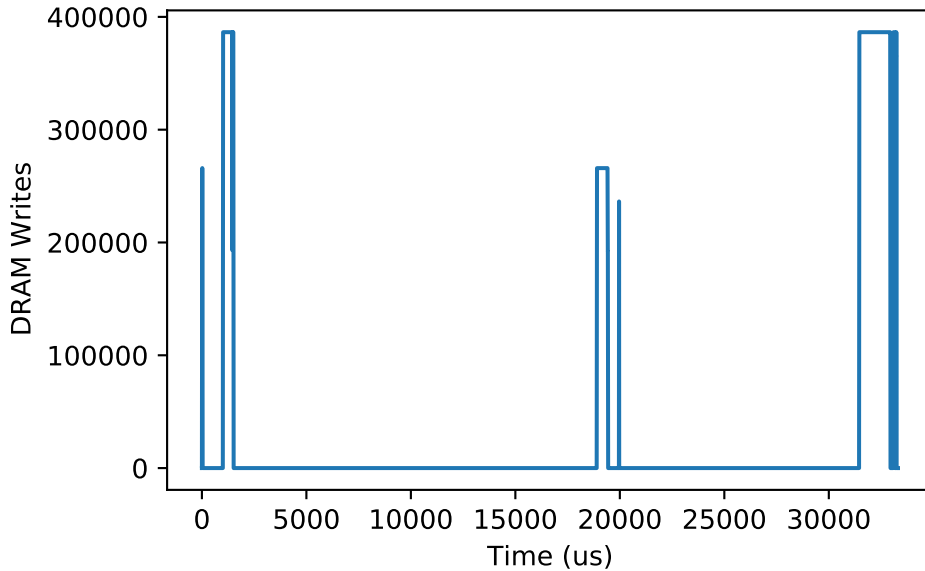
Instructions



DRAM Reads



DRAM Writes



DRAM Traffic



LDS Utilized (MB)

