

MINIMAL EXAMPLE OF MSU DISSERTATION TEMPLATE

by

First Middle Last

A comprehensive exam submitted in partial fulfillment  
of the requirements for the degree

of

Doctor of Philosophy

in

Electrical Engineering

MONTANA STATE UNIVERSITY  
Bozeman, Montana

August 2022

©COPYRIGHT

by

First Middle Last

Creative Commons Attribution 4.0 International License 

# TABLE OF CONTENTS

1. INTRODUCTION .....	1
REFERENCES.....	3

## LIST OF FIGURES

Figure	Page
1.1 Example duck.....	2

## ABSTRACT

Lorem ipsum dolor sit amet, consectetur adipiscing elit. Ut purus elit, vestibulum ut, placerat ac, adipiscing vitae, felis. Curabitur dictum gravida mauris. Nam arcu libero, nonummy eget, consectetur id, vulputate a, magna. Donec vehicula augue eu neque. Pellentesque habitant morbi tristique senectus et netus et malesuada fames ac turpis egestas. Mauris ut leo. Cras viverra metus rhoncus sem. Nulla et lectus vestibulum urna fringilla ultrices. Phasellus eu tellus sit amet tortor gravida placerat. Integer sapien est, iaculis in, pretium quis, viverra ac, nunc. Praesent eget sem vel leo ultrices bibendum. Aenean faucibus. Morbi dolor nulla, malesuada eu, pulvinar at, mollis ac, nulla. Curabitur auctor semper nulla. Donec varius orci eget risus. Duis nibh mi, congue eu, accumsan eleifend, sagittis quis, diam. Duis eget orci sit amet orci dignissim rutrum.

## INTRODUCTION

Fusce mauris. Vestibulum luctus nibh at lectus. Sed bibendum, nulla a faucibus semper, leo velit ultricies tellus, ac venenatis arcu wisi vel nisl. Vestibulum diam. Aliquam pellentesque, augue quis sagittis posuere, turpis lacus congue quam, in hendrerit risus eros eget felis. Maecenas eget erat in sapien mattis porttitor. Vestibulum porttitor. Nulla facilisi. Sed a turpis eu lacus commodo facilisis. Morbi fringilla, wisi in dignissim interdum, justo lectus sagittis dui, et vehicula libero dui cursus dui. Mauris tempor ligula sed lacus. Duis cursus enim ut augue. Cras ac magna. Cras nulla. Nulla egestas. Curabitur a leo. Quisque egestas wisi eget nunc. Nam feugiat lacus vel est. Curabitur consectetur. Random references [1, 2, 3].

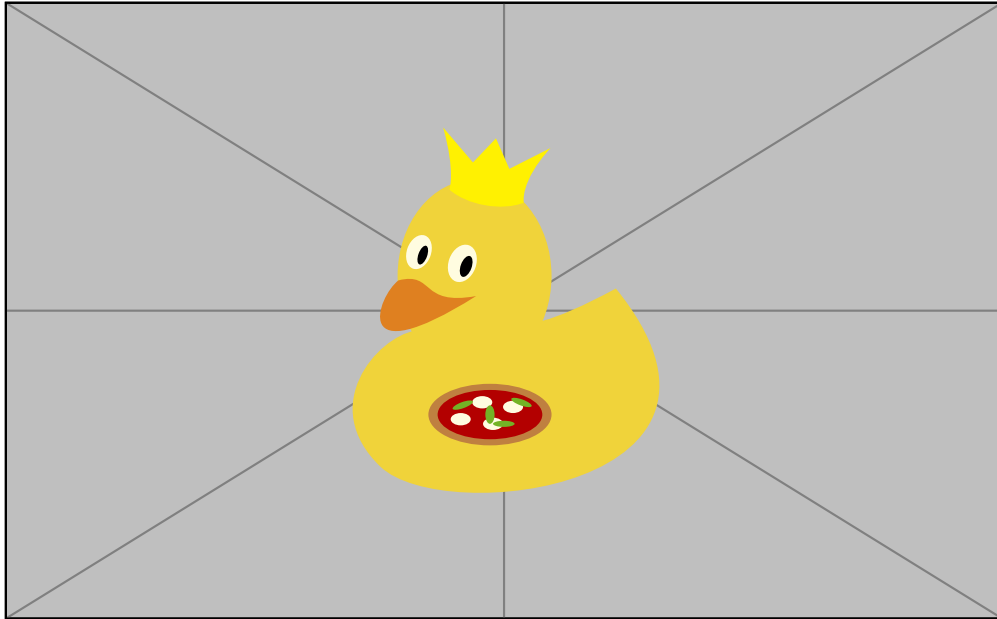


Figure 1.1: Example duck.

## REFERENCES

- [1] Burkhard Ringlein et al. “System Architecture for Network-Attached FPGAs in the Cloud using Partial Reconfiguration”. In: *2019 29th International Conference on Field Programmable Logic and Applications (FPL)*. IEEE, Sept. 2019. DOI: 10.1109/fpl.2019.00054.
- [2] Saket Sathe and Charu C. Aggarwal. “Subspace Outlier Detection in Linear Time with Randomized Hashing”. In: *2016 IEEE 16th International Conference on Data Mining (ICDM)*. IEEE, Dec. 2016. DOI: 10.1109/icdm.2016.0057.
- [3] Kleanthis Malialis, Christos G. Panayiotou, and Marios M. Polycarpou. “Online Learning With Adaptive Rebalancing in Nonstationary Environments”. In: *IEEE Transactions on Neural Networks and Learning Systems* 32.10 (Oct. 2021), pp. 4445–4459. DOI: 10.1109/tnnls.2020.3017863.