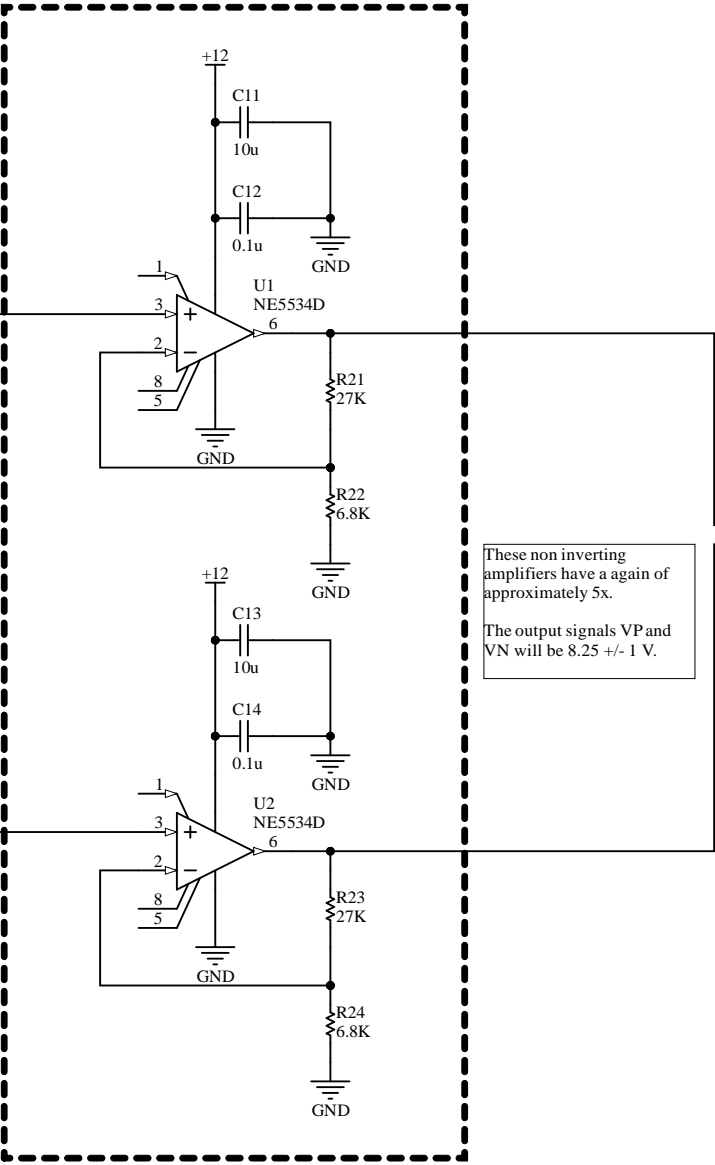


X1
Peizo Tranducer, 750 kHz

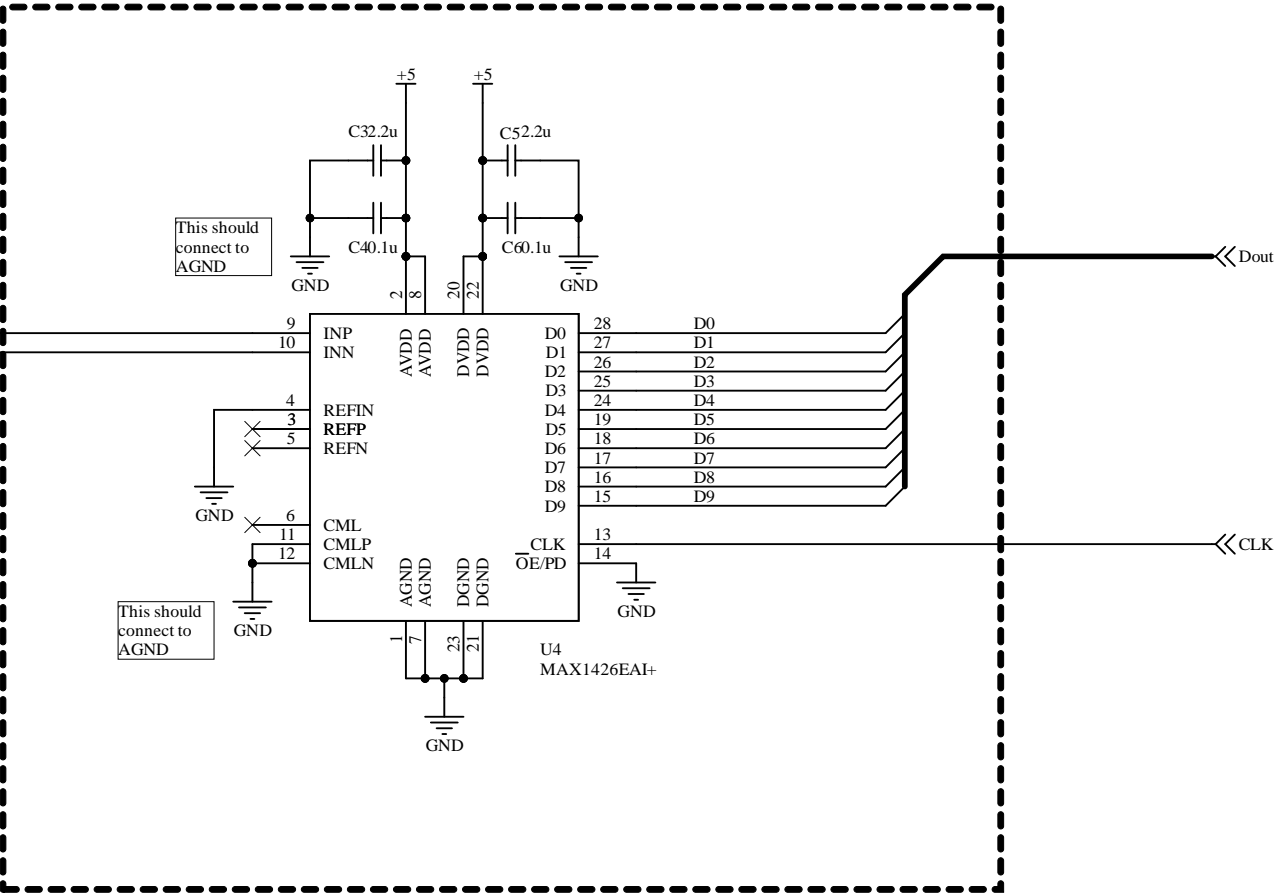
The ouput signals PL
and PR should be
1.65V +/- 0.1V (max)
or +/- 0.01V (min)

Title		
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D		
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File:	\\..\interface.SchDoc	Drawn By:

Amplification

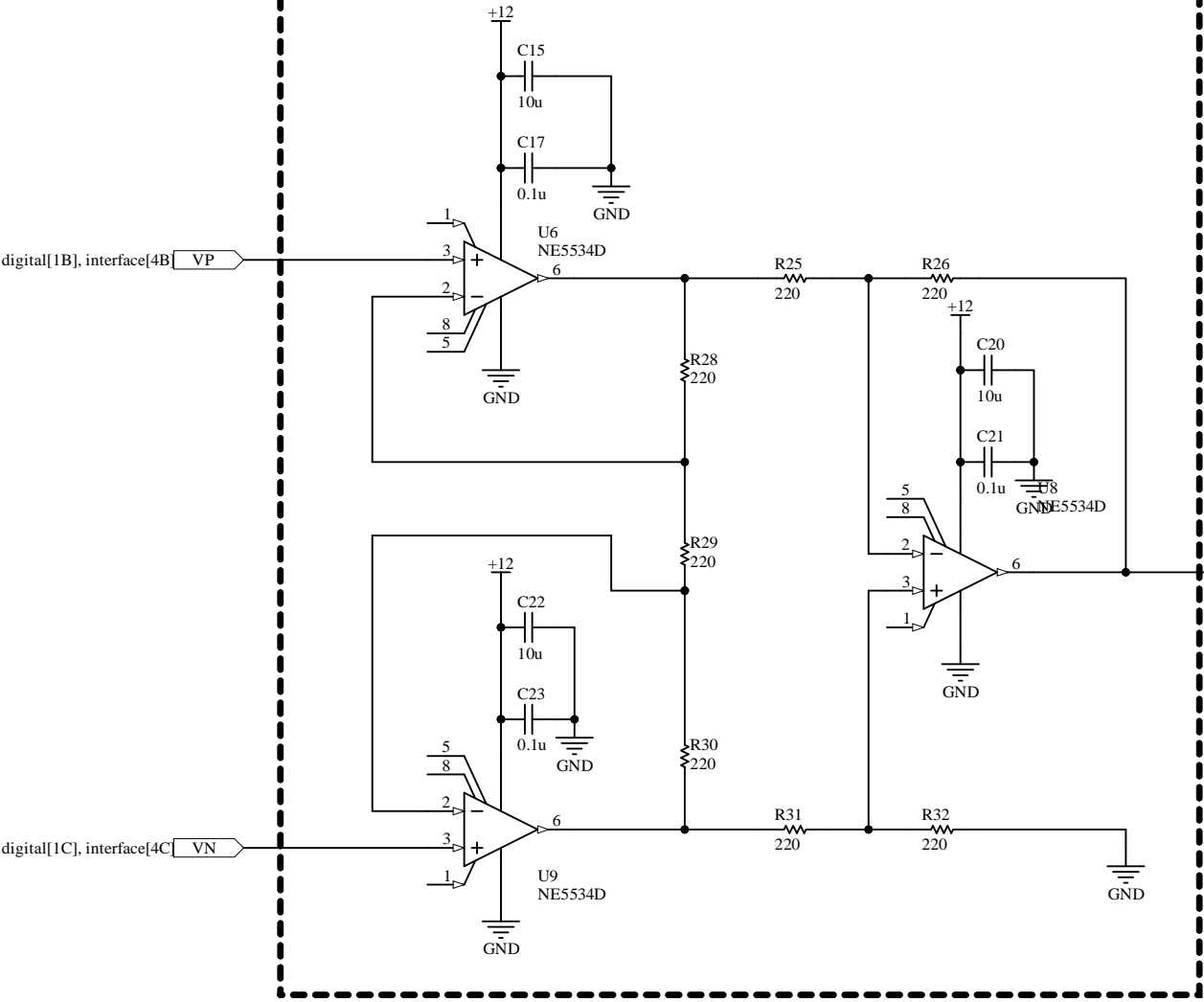


ADC

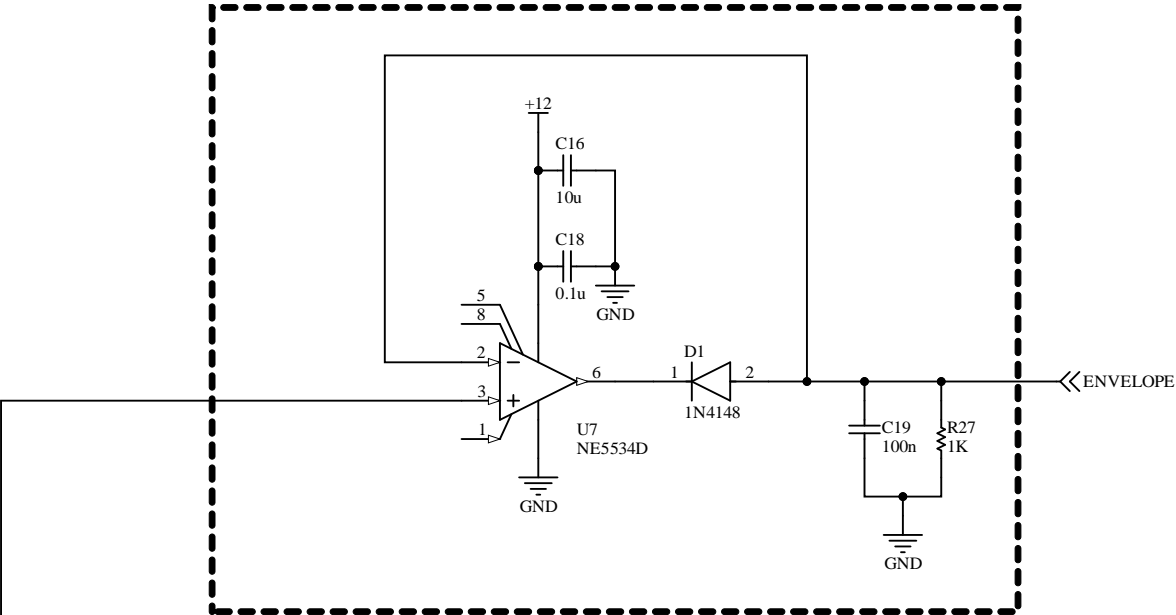


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Differential Amplifier

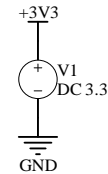
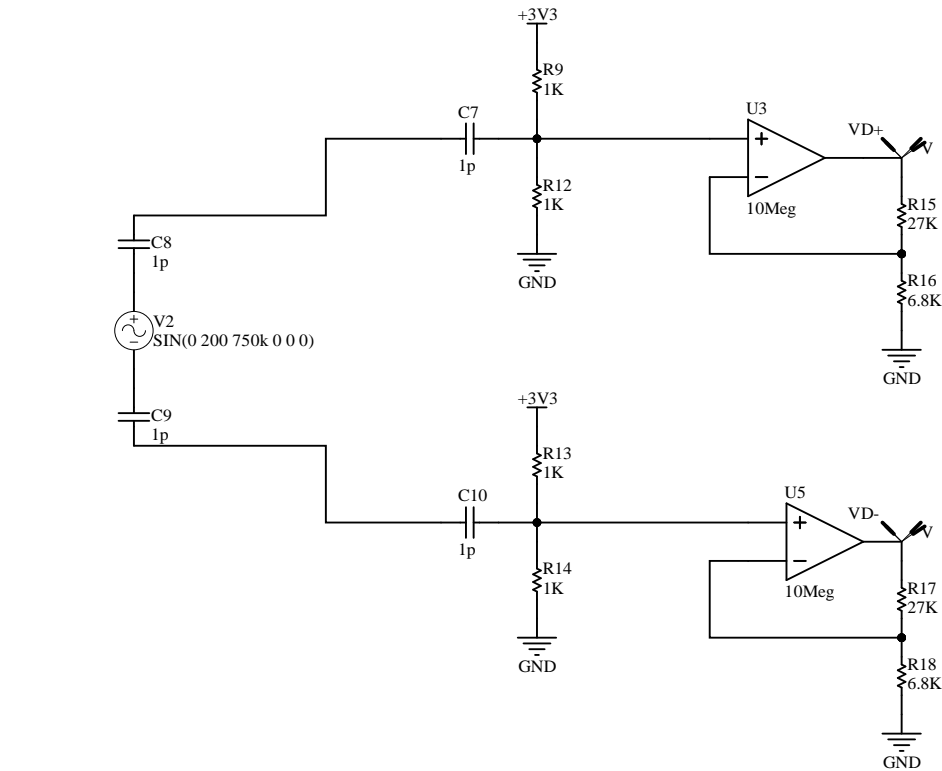


Envelope Detector



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Simulation



Title		
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