

## DEVI BALIKA VIDYALAYA - COLOMBO 08

## First Term Test - May, 2023

## Grade 12

In	formation	&	Communication	Technolog	v - 1
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1 hour

- 1. Consider the following statements regarding the value of information.
  - A The value of information depends on its relevance.
  - B Information has more value at the moment it is created or received.
  - C The value of the information is maximized when the time is closest to zero.
  - D The value of information gradually decreases over time. Eventually they become data again.

Which of the above statements is/are true?

- (1) A and B only
- (2) A and C only
- (3) B and D only

- (4) A, B and C only
- (5) All, A, B, C and D
- 2. Who is the inventor of World Wide Web?
  - (1) Charles Babbage
- (2) Blaise Pascal
- (3) Howard Aiken

- (4) Tim Berners-Lee
- (5) Herman Hollerith
- 3. Which of the following correctly defines Real-time processing?
  - (1) Processing of data collected over a specified period of time as a group.
  - (2) Proper preparation of data before computerization.
  - (3) Correction of errors in computerized data.
  - (4) Processing of data right after it is received.
  - (5) Processing of maximum amount of data within a second.
- 4. Consider the following statements regarding data and information.
  - A The accuracy of the information depends on the accuracy of the input data.
  - B Information obtained after processing of data is always accurate.
  - C In order to make the right decisions, the right data is needed.

Which of the above statements is/are true?

(1) A only

(2) C only

(3) A and C only

- (4) B and C only
- (5) All, A, B and C

5. Which of the following is not	a social issue that can be call	sed by Information Technology?
<ul> <li>(1) Reduction of interpersonal</li> <li>(2) The digital divide</li> <li>(3) People suffering from phys</li> <li>(4) Decreased use of paperwor</li> <li>(5) Discard of e-waste to the e</li> </ul>	relationships	
6. Which of the following statem	ents is false?	
<ol> <li>(1) Privacy is the protection of</li> <li>(2) Software piracy is the make not have the legal right to.</li> <li>(3) Copyright is the author's right</li> <li>(4) Any Software can be reasonable.</li> </ol>	personal information in coming of illegal and unauthorized that to print and sell copies of pied, sold and distributed after	ed copies of software that the user does
(1) Firmware (2) Hardwar	· (0) · · ·	Which of the following is not a main veware (5) Shareware
<ul><li>8. The electronic era began when Select the most appropriate han</li><li>(1) Microprocessor</li><li>(4) Vacuum tube</li></ul>	computers were built using . dware technology to fill the t	(3) Integrated Circuit
9. Who first recognized the compa	iter's ability to perform diffe	rent tasks when different instructions
were provided? (1) Charles Babbage (4) Ada Augusta Lovelace	(2) Blaise Pascal (5) John Mauchly and J. Pa	(3) John von Nous
10. Which generation of computers Blu-ray disks?	first introduced the storage t	echnology which later evolved into
<ul><li>(1) First generation</li><li>(4) Fourth generation</li></ul>	<ul><li>(2) Second generation</li><li>(5) Fifth generation</li></ul>	(3) Third generation
11. In the instruction cycle, Arithme which hardware component?	tic and Logic Unit executes	the decoded commands coming from
(1) Random Access Memory (4) Cache Memory	(2) Control Unit (5) Keyboard	(3) Memory Registers

(1) Machine lang (2) Machine lang (3) Artificial inte (4) Artificial inte (5) Ultra Large S	ital devices as in tware technologication the 'Chatte guage / First ge guage / Fifth ge elligence / Four elligence / Fifth Scale Integration	orogram that simular if they were communities they which is used bot' program belong incration eneration the generation generation in / Fifth generation	to develop the '(	enatbot' program and the
13. "Seismometer" n paper.	neasure the inte	ensity of an earthqua	ake by recording se	ismic waves on to a graph on the technology and the
<ul> <li>(1) Analog / Gen</li> <li>(2) Digital / Gene</li> <li>(3) Analog / Spec</li> <li>(4) Digital / Spec</li> <li>(5) Hybrid / Gene</li> </ul>	eral purpose co cial purpose con cial purpose con	Omputer mputer mputer		and the
C and tran	es the "Read" si	ignal. Then the contr	it needs to access ent of the memory l	in the main memory, the ocation is placed into the
(1) A – Address b	ous	B – Control bus		
(2) A – Data bus	same of the same o	B – Control bus  B – Address bus	C – Data	
(3) A – Data bus		B – Control bus	C – Conti	
(4) A – Control by	us.	B – Address bus	C – Addr	
(5) A – Address b		B – Data bus	C – Data C – Contr	
				or ous
15. What is the equiva (1) 10010111	alent of eight bi (2) 11101000		t of (-23)? (4) 00011000	(5) 00010111
16. If a student has ob	tained 46 <sub>16</sub> , 80	10 and 10110102 mar	ks for the three subj	jects she faced at the
term test, what is t (1) 120 <sub>8</sub>	ne average mar (2) 120 <sub>10</sub>		(4) 240	(5) 5 A
(1) 1208	(2) 12010	(3) 1000110 <sub>2</sub>	$(4) 240_{10}$	(5) 5A <sub>16</sub>

17. Which of the following represents the result of the 4-bit One's compliment addition of  $13_{10} + (-7)_{10}$  in binary?

(1)0110

(2)0101

(3)0100

(4) 1001

(5)1010

18. If the ASCII value of letter F equals to 70, what would be the ASCII value of letter W?

(1) Given data is not sufficient to determine the value of W

(3)81

(5) 124

19. Consider the following statements regarding encoding systems.

A - EDCDIC is a 12-bit encoding system

B - ASCII is a 7-bit encoding system

C - UNICODE encoding system cannot be used to represent the characters of different languages in the world

Which of the above statements is/are correct?

(1) A only

(2) B only

(3) A and B only

(4) A and C only

(5) B and C only

20. What is the equivalent of bitwise 23<sub>10</sub> XOR 31<sub>10</sub> in binary?

(1) 10111

(2)01000

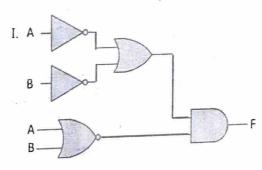
(3) 11111

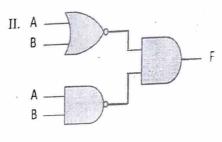
(4) 10110

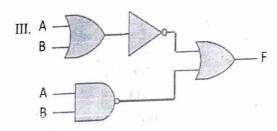
(5)00000

21. Which of the following logic circuits realizes the Boolean expression:

$$F = (A' + B').(A + B)'$$
?

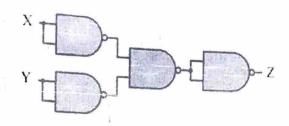






- (1) I only
- (4) II and III only
- (2) II only
- (5) All, I, II and III
- (3) I and II only

22. Consider the following logic circuit.



Which of the following logic gate's function is equivalent to the output "Z" of the logic circuit

- (1) EXOR
- (2) AND
- (3) OR
- (4) NOR
- (5) EXNOR
- 23. What is the Minimum number of 2-input NAND gates required to implement the simplified expression obtained from the following K Map?

F AB				
C	00	01	11	10
0	0	1	1	0
1	0	1	1	0

- (1) No gates required
- (2) 1
- (3) 2
- (4) 3
- (5)4
- 24. Which of the following basic logic gates produces the complement of the input signal?
  - (1) OR
- (2) AND
- (3) NOT
- (4) EXOR
- (5) NOR
- 25. Which of the following represents the least number of basic logic gates required to realize the following Boolean expression F?

$$F = A + B' + A'B'C$$

Note: A gate can only take maximum of 2 inputs.

- (1) 2 NOT, 2 AND, 2 OR gates
- (2) 2 NOT, 1 AND, 1 OR gates
- (3) 1 NOT, 1 OR gates
- (4) 1 NOT, 1 AND gates
- (5) 1 OR gate