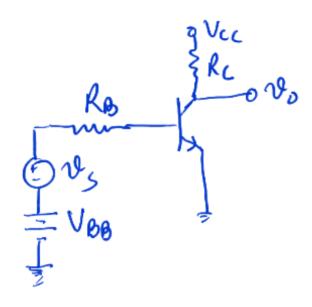
- 1. Determine the bonall-signal parameters  $f_m$ ,  $r_{H}$  and  $r_{S}$  of a transistor with parameters  $\beta = 180$ ,  $V_{A} = 150 \text{V}$  for bias currents of (i)  $C_{Q} = 0.5 \text{mA}$  and (ii)  $C_{Q} = 2 \text{mA}$ . Assume  $V_{T} = 26 \text{mW}$ .
- 2. The transister parameters are  $\beta = 125$  and  $V_{4} = 200 \text{ V}$ . A value of  $g_{m} = 95 \text{ mA/V}$  is desired. Determine the required collector current and then find  $r_{7}$  and  $r_{6}$
- 3. For the given circuit, the transistin parameter are  $\beta = 120$ ,  $V_{AS}(on) = 0.7V$  and  $V_{A} = 80V$ . Other circuit parameters are  $V_{CC} = 3.3V$ ,  $V_{BB} = 1.10V$   $R_{C} = 4k$  and  $R_{B} = 110$  kg. (a) Determine the hybrid 17 parameters, (b) Draw hybrid 17 equivalent chi and obtain the Small-signal gain  $A_{V} = \frac{V_{S}}{V_{S}}$ . (c) If the line varying output signal is given by  $V_{O} = 0.5$  Sin(100t) V, what is  $V_{S}(t)$ ?



Results of the second second

a. Draw hybrid-IT equivalent circuit.

b. determine the quiscent values, Ica Vasa, and find the small-signal parameters

Jm, T, To and obtain the small-signal valtage gain.

C. Also calculate input resistance to base

Rib, input resistance to the

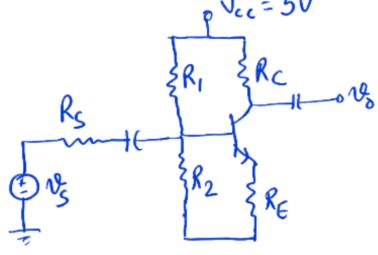
amplifier Ri, output resistance of the

amplifier Ro. Obtain the loading

effect for the circuit.

d. Repeat if B changes by 25%, estimate the change in voltage gain.

Q5.



Design a bias stable circuit for The parameters  $\beta = 100$ ,  $V_{46}(00) = 0.7V$ ,  $V_{4} = 00$ ,  $I_{CA} = 0.5 mA$   $V_{CEA} = 2.5V$ , and  $A_{V} = -8$ .

Draw hybrid-17 equivalent clet for the following circuit. Obtain the small-signal gain for (i) β = 75, (ii)β=100 (iii) β=125. Re2 = 1.1 RE1. Rest other parameters from Q4.

