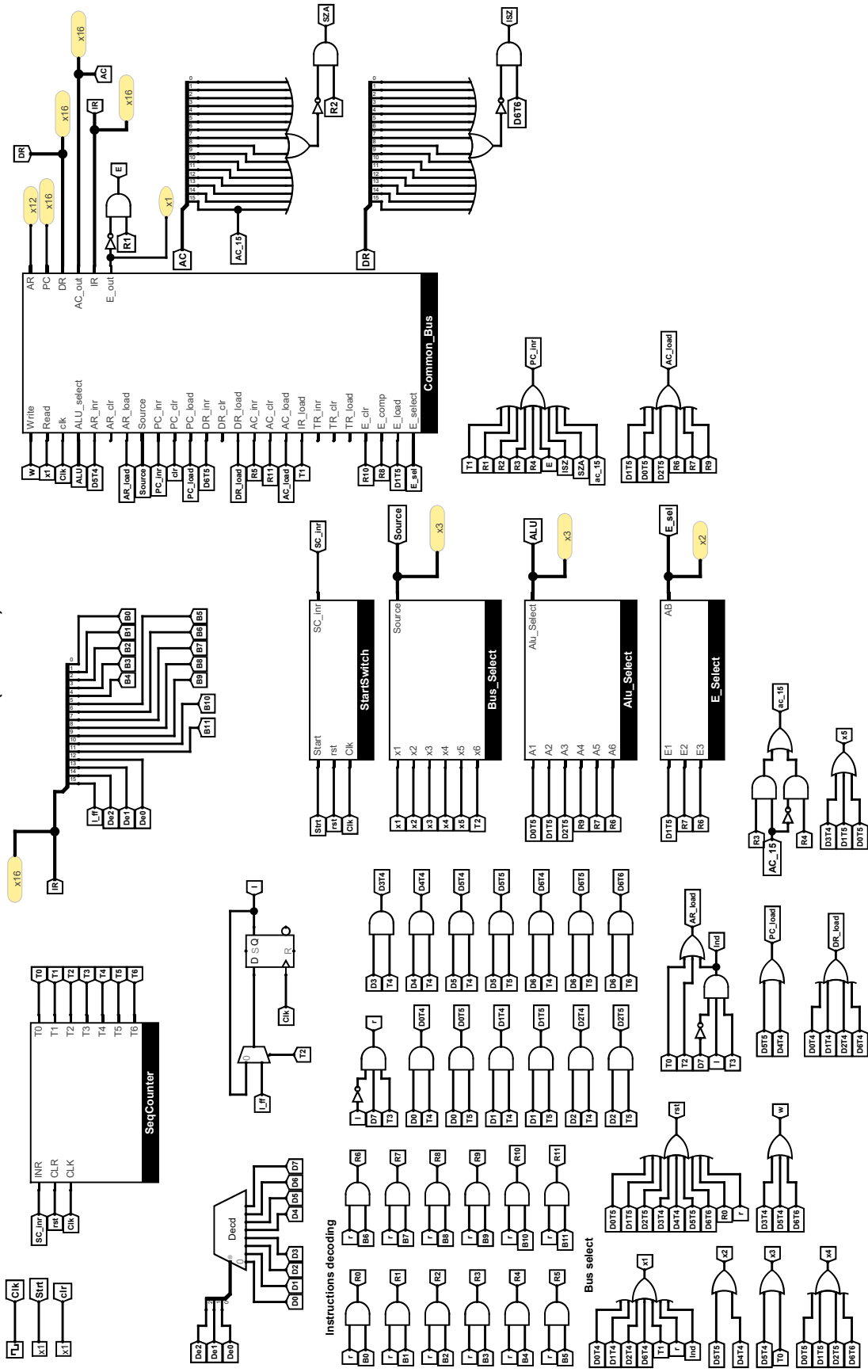
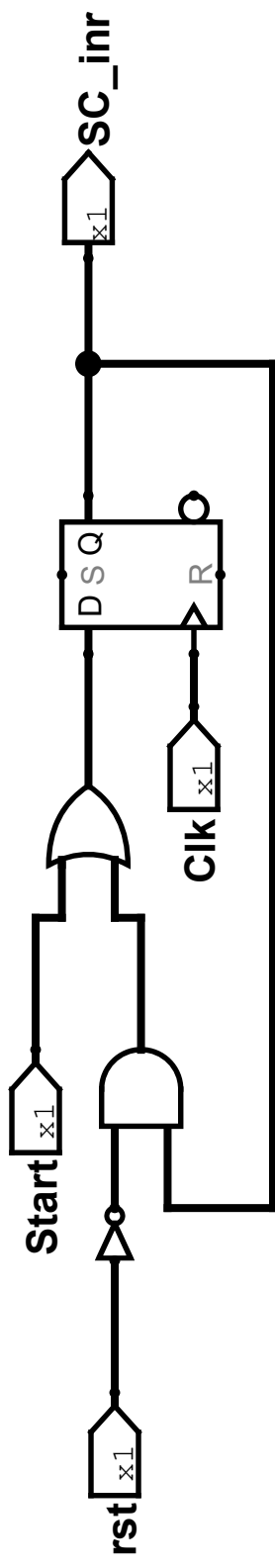


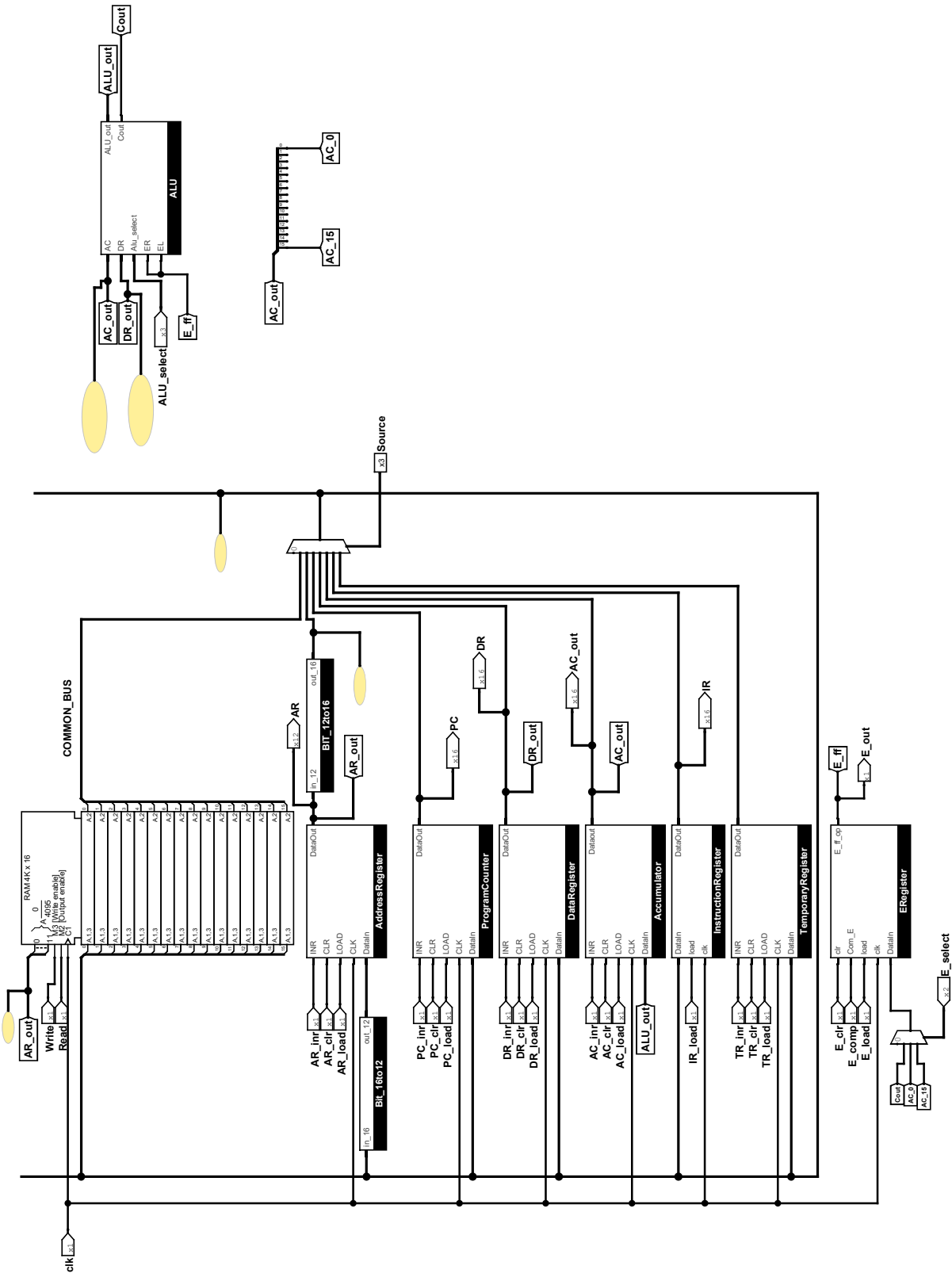
main (1 of 17)



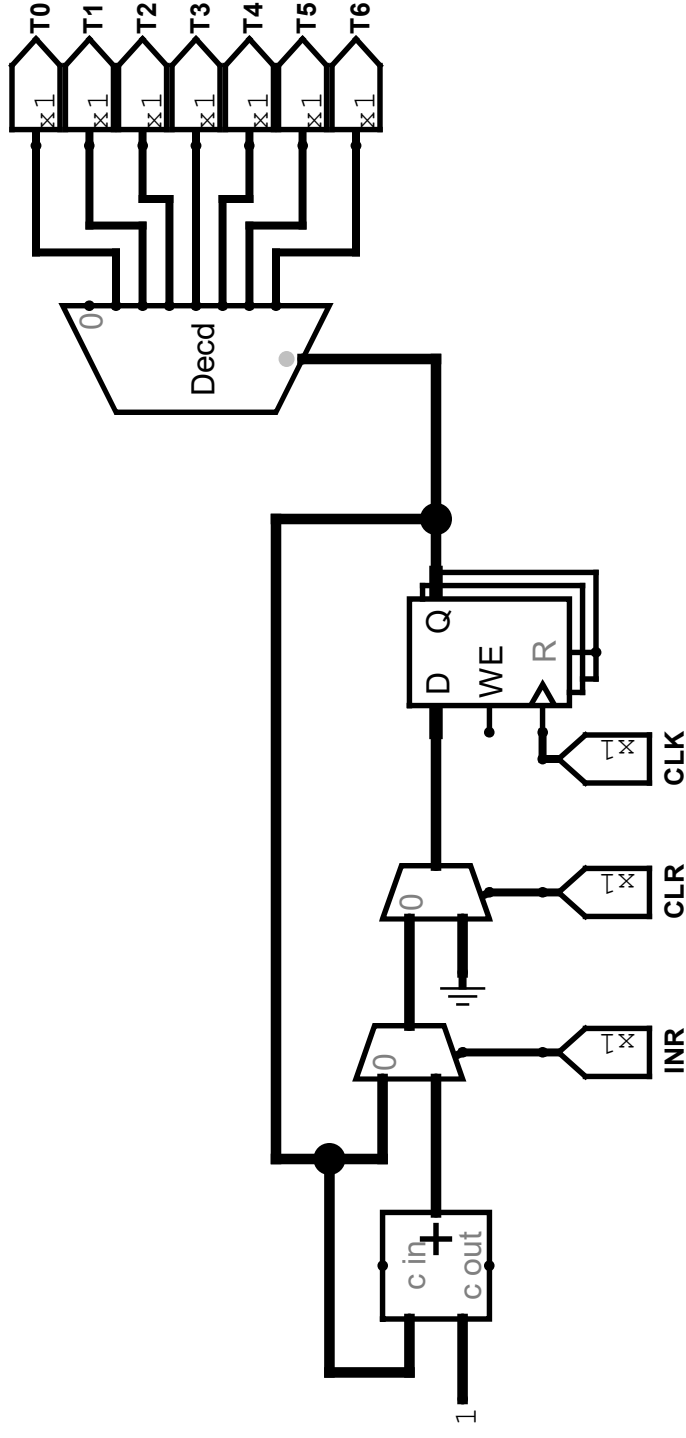
StartSwitch (2 of 17)



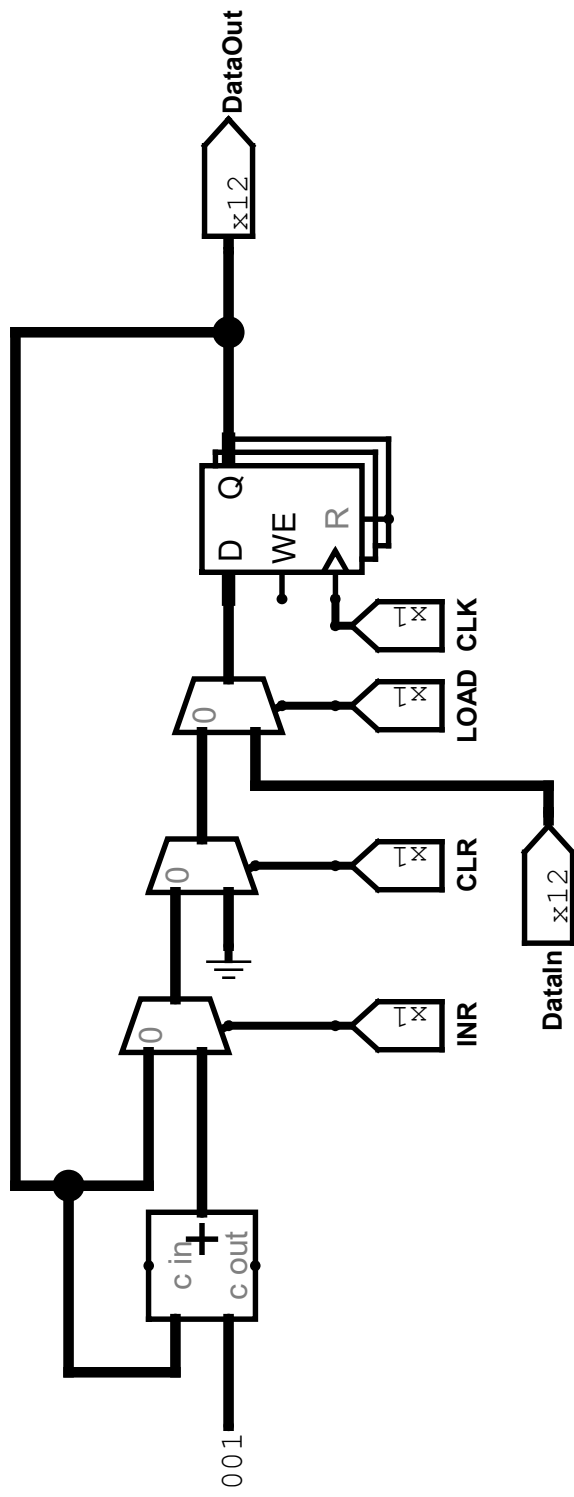
# Common\_Bus (3 of 17)



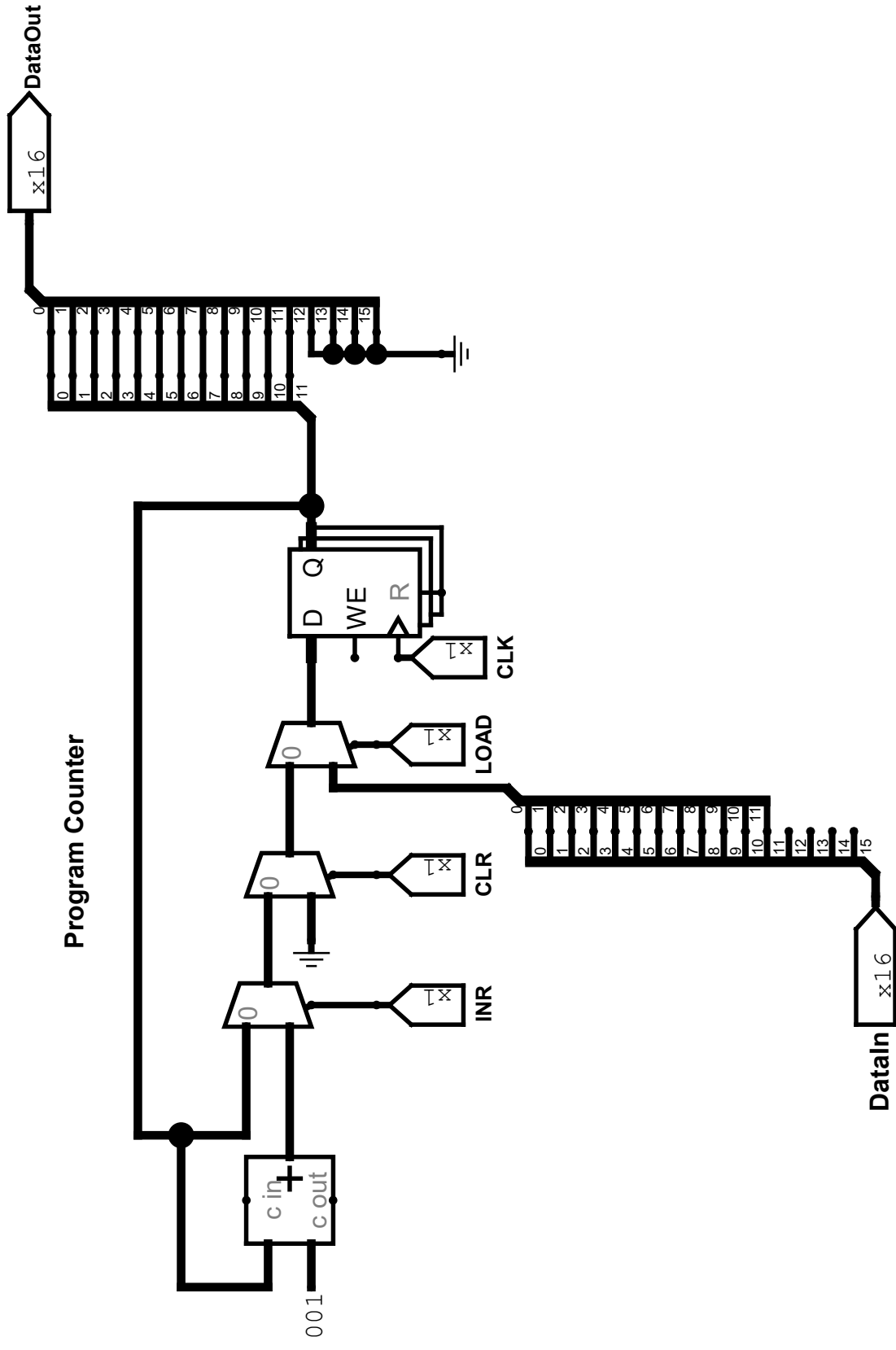
SeqCounter (4 of 17)



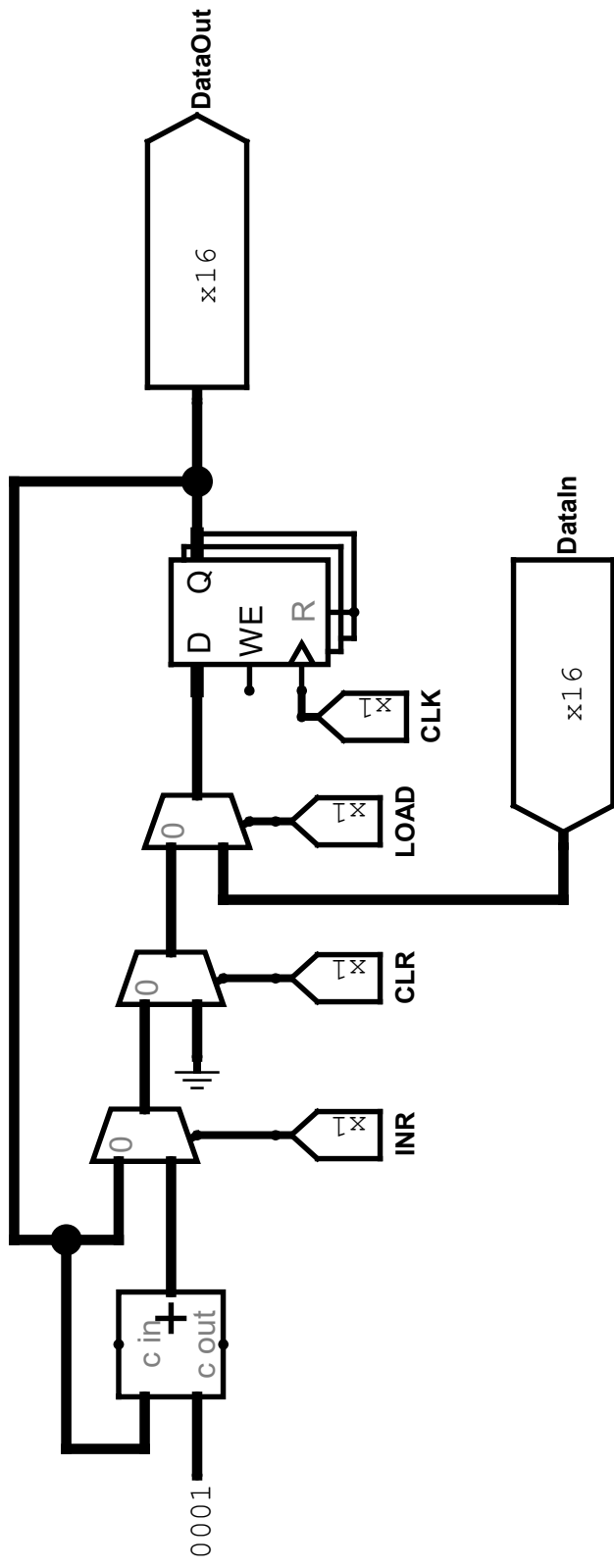
Address Register



ProgramCounter (6 of 17)

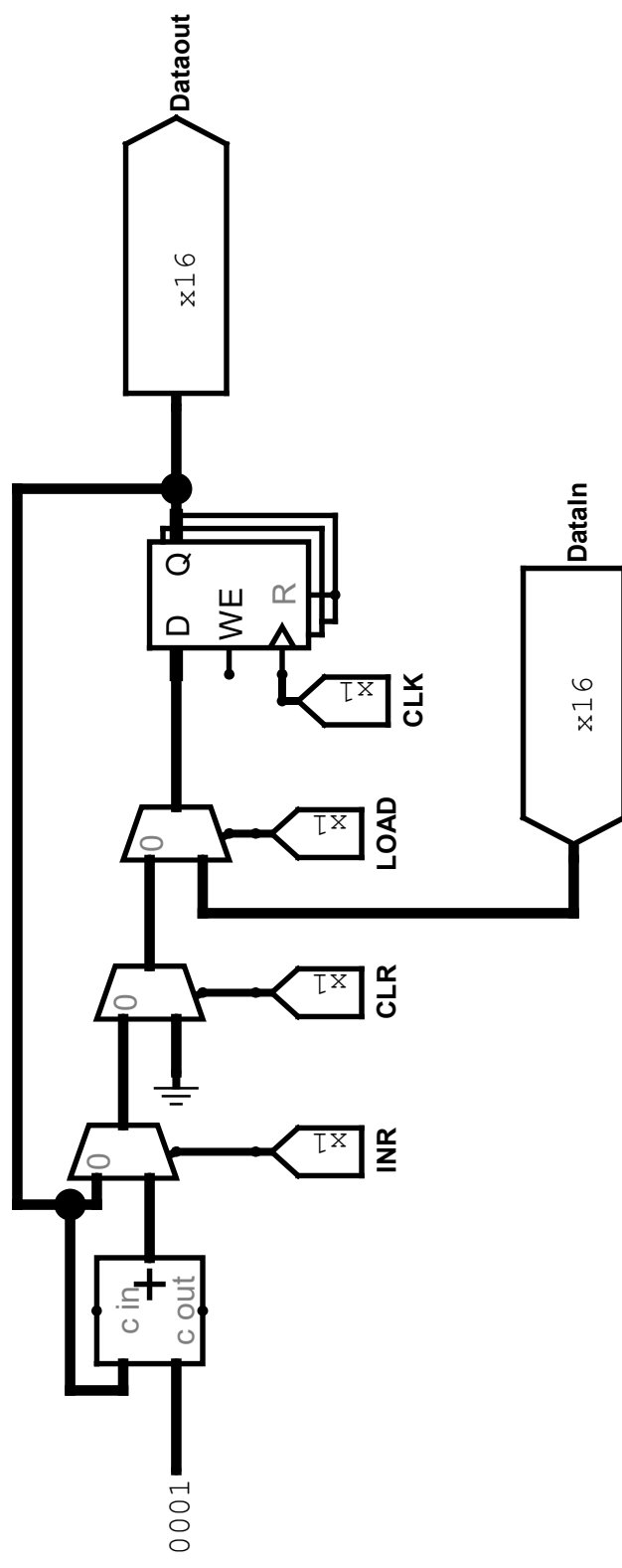


# DATA REGISTER



Accumulator (8 of 17)

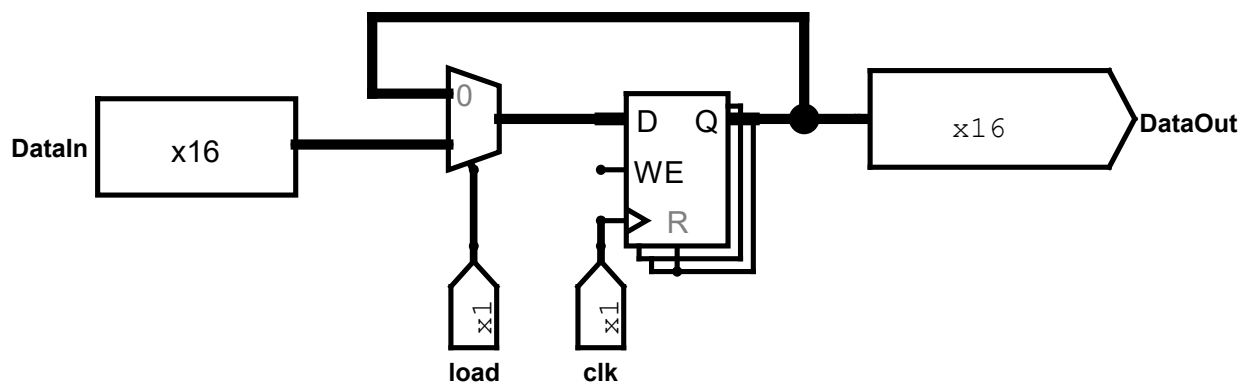
## Accumulator





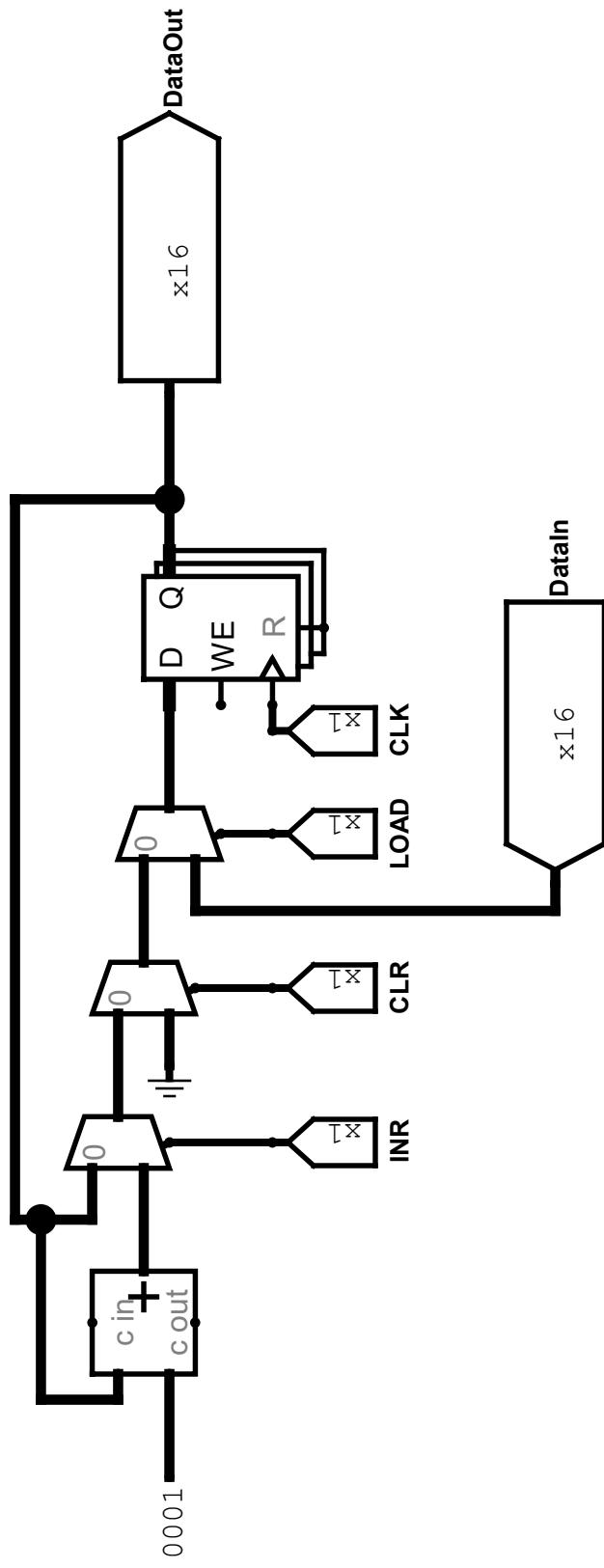
InstructionRegister (9 of 17)

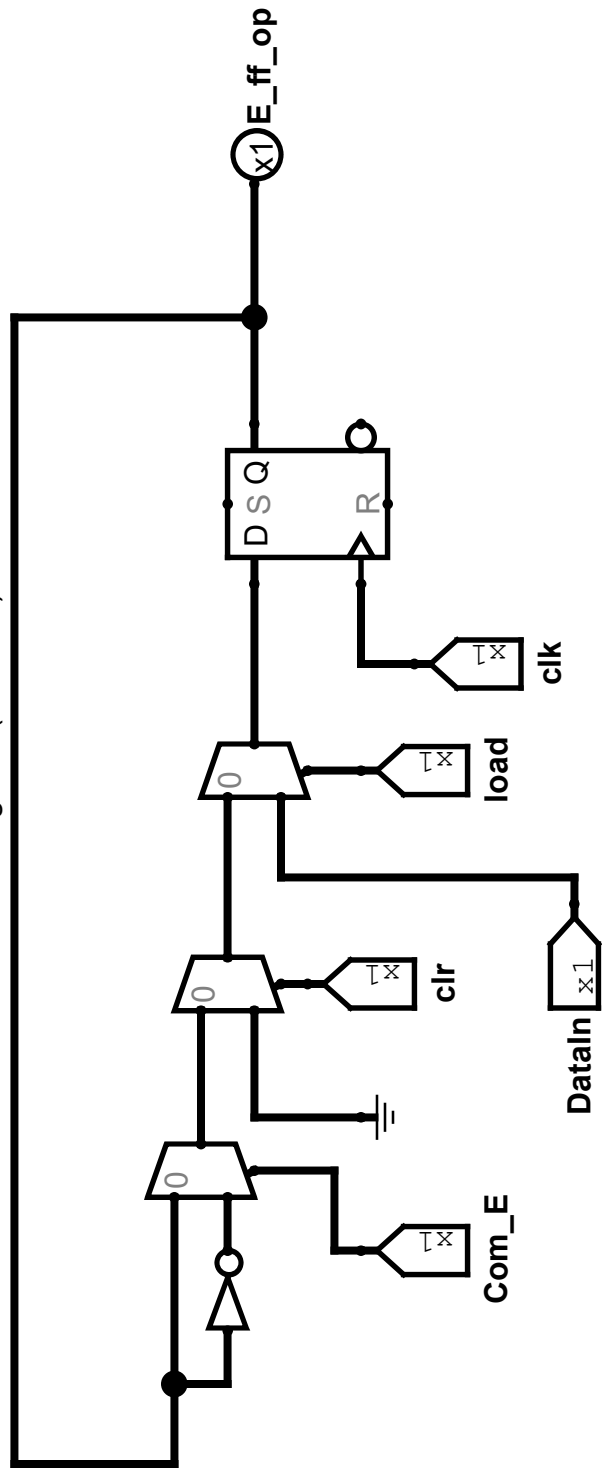
### Instruction Register



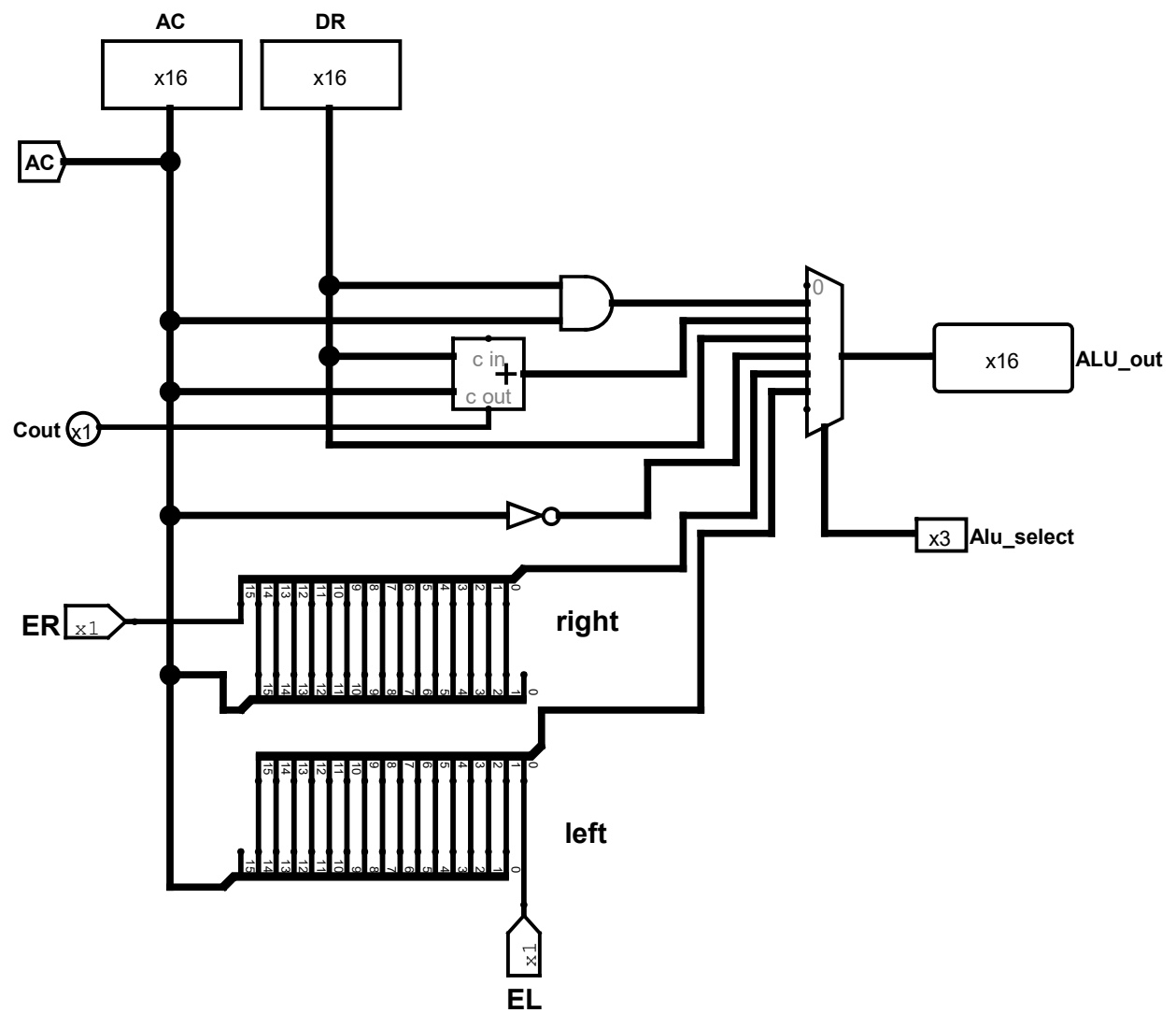
TemporaryRegister (10 of 17)

Temporary Register

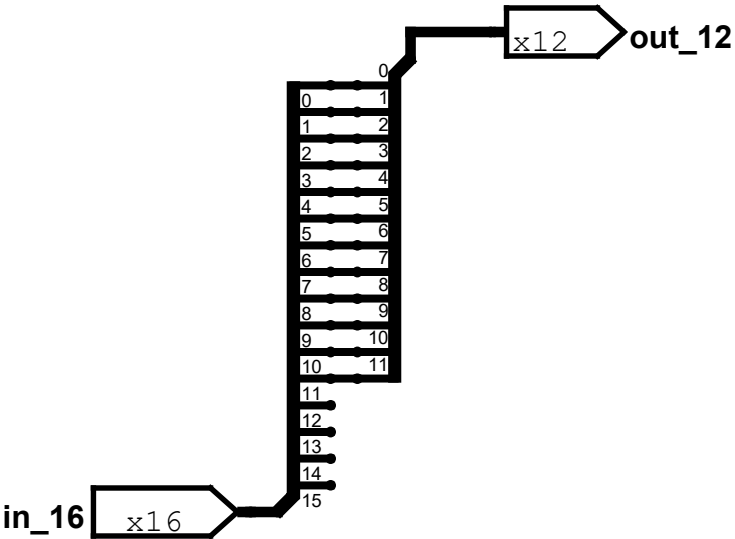




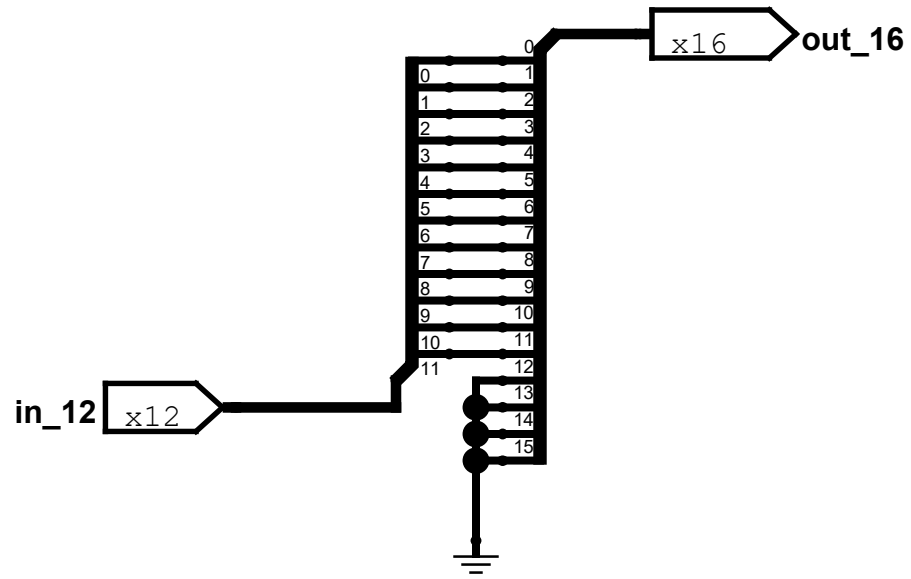
ALU (12 of 17)

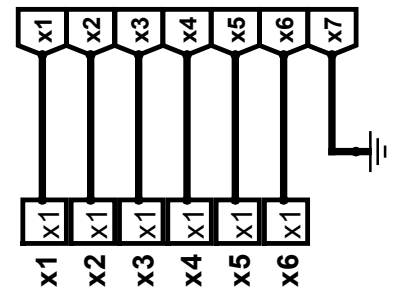
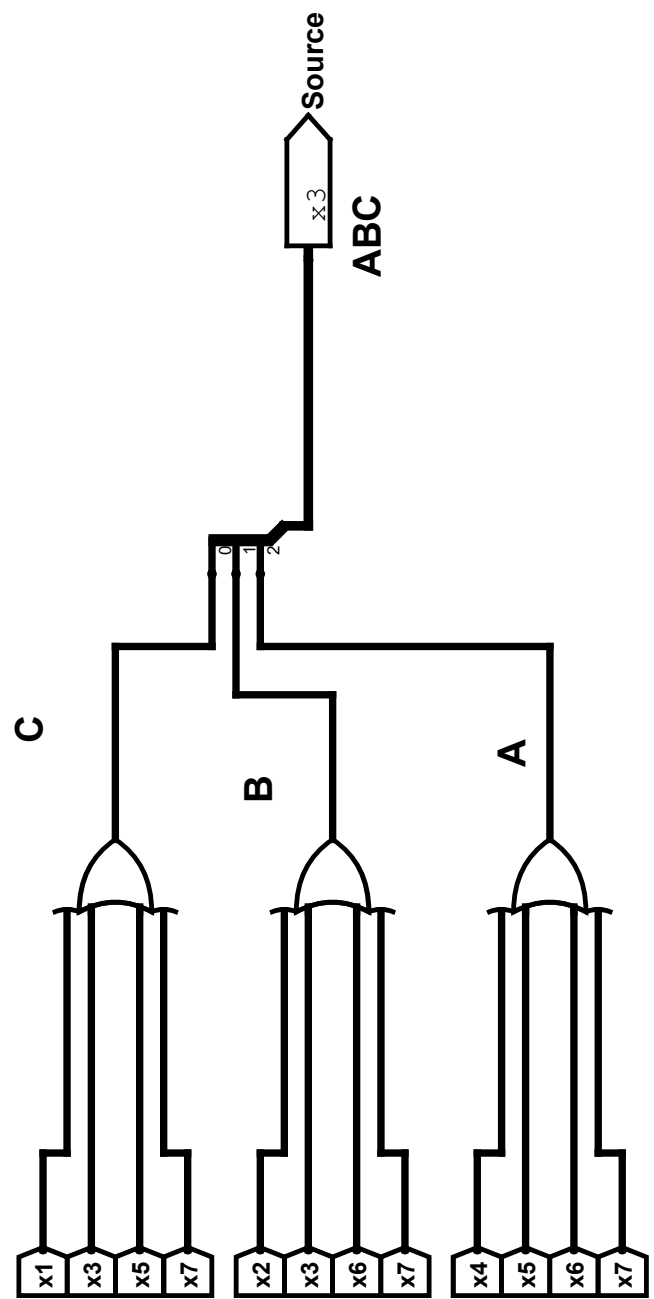


Bit\_16to12 (13 of 17)

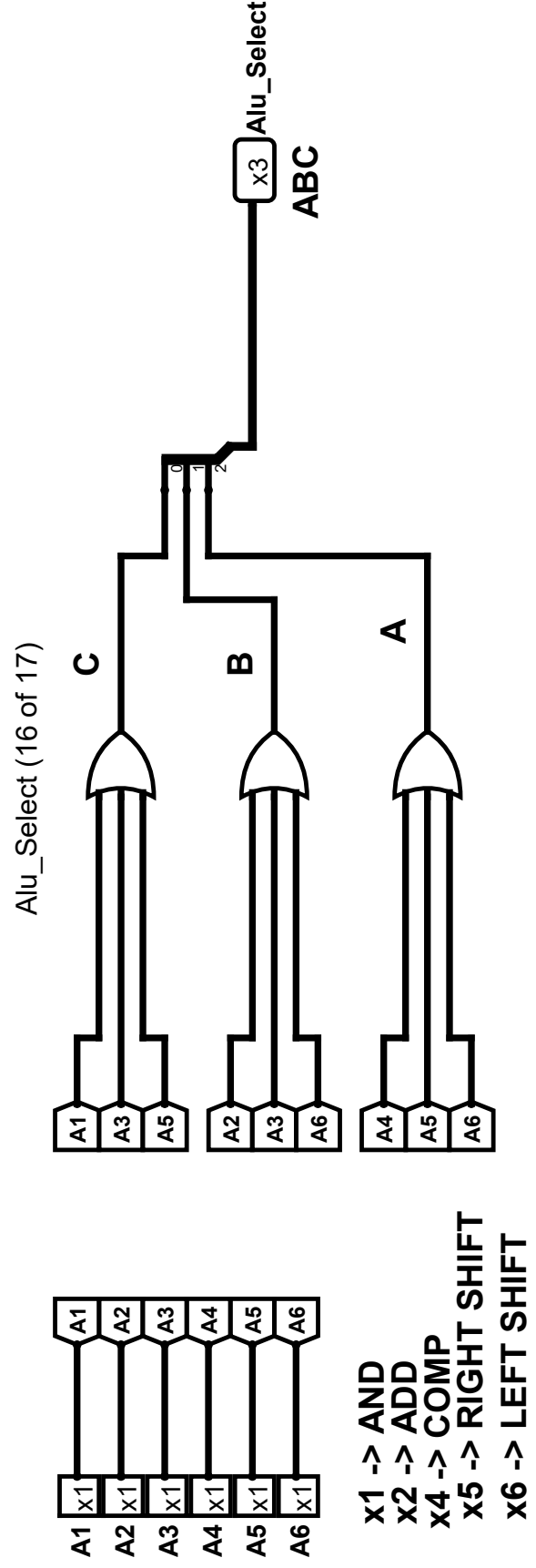


BIT\_12to16 (14 of 17)

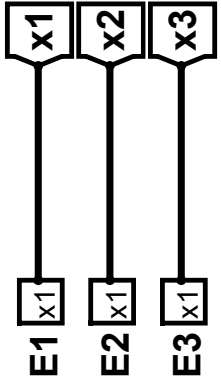




- x3 -> DR
- x4 -> AC
- x5 -> IR
- x6 -> TR
- x7 -> Memory
- x1 -> AR
- x2 -> PC







E\_Select (17 of 17)

