

Kaviyashri K P

Front-End-Developer

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📍 Chennai

Objective

Enthusiastic and self-motivated web designer. Eager to join and bring my skill in frontend development, and create appealing and user-friendly visual design in every project that will be received in the future.

Work Experience

- Front-End development with React.js trainer in *July-2024 -At present*
Besant Technologies,Siruseri,Chennai.
- Assistant Professor (ECE), M.A.M college of Engineering, Trichy *May 2019- Jan 2020*

Education

- Mepco Schlenk Engineering College, Sivakasi. *2019*
 - Master of Engineering, VLSI Design
 - CGPA: 9.33
 - Designed and developed a Project : "Quantum Realization of Testable Reversible Logic Based Computing System" in Quantum Computing domain .
- Kamaraj College of Engineering and Technology, Virudhunagar *2017*
 - Bachelor of Engineering, ECE
 - CGPA: 7.86
 - Designed and developed a project: "High spectral efficiency MU MIMO OFDM system based on DBA" in Wireless Communication.
- Noble Matric Higher Secondary School *2013*
 - HSC
 - Percentage : 88.42 %
- Devangar Girls Higher Secondary School *2011*
 - SSLC
 - Percentage : 95 %

Skills

- HTML
- CSS
- Bootstrap
- JavaScript
- ReactJs
- Python

Projects

● E-Commerce Webpage using ReactJs

- Designed a E-com webpage by applying the skill in Reactjs like JSX (HTML + JavaScript), CSS, Bootstrap, Redux concepts
- In this project I have learnt more about Single Page Application.
- Created components using JSX and used CRUD operation for user detail.
- Used Redux concept for State Management in cart of the webpage.

● Internship Projects:

- Designed a landing page for a Restaurant by applying the skill in HTML CSS and Bootstrap.
- Designed my portfolio page by using the skills in HTML CSS Bootstrap and Javascript.
- Designed a simple calculator using the HTML CSS and Javascript.

Certifications

- Completed Full Stack development course from Besant Technology.
- Obtained Business Vantage qualification Entry level.

Awards Received

- Obtained "OUTSTANDING STUDENT" award in the department of ME VLSI Design for the year 2018 to 2019 at Mepco Schlenk Engineering College.

Publications

- A paper entitled "Realization and Optimization of Quantum Equivalent Circuits of Reversible Combinational Circuits" has been published in the Journal of Computational and Theoretical Nanoscience.
- A paper entitled "Fault Tolerance in Reversible Logic Circuits and Quantum Cost Optimization" has been published in the Journal of Computing and Informatics.
- A paper entitled "Realization of FPGA Architecture for Angle of Arrival using MUSIC Algorithm" has been presented at Panimalar Engineering College and it is published in Springer Lecture Notes on Emerging Trends in Computing and Expert Technology.

Language

- Tamil - First Language
- English

Personal Information

- Marital Status : Married
- DOB : 10/08/1995