

Roll No:

CT - 1

Microprocessor (KCS-403)

Microprocessor (8085-8086)	
CO Number	Course Outcome
CO1	Define the functioning of 8085, 8086 microprocessor and Peripheral devices.
CO2	Describe the functioning of 8085, 8086 microprocessor, Peripheral devices and programming concepts.
CO3	Illustrate interfacing concepts of 8085, 8086 microprocessor.
CO4	Examine the assembly language programming for 8085, 8086 microprocessor.
CO5	Design the various interfacing circuits for 8085, 8086 with peripheral devices and its programming.

M. M. 15

(1X3 = 3 Marks)

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|----|---|-----|
| a) | Why AD ₀ -AD ₇ lines are multiplexed? How they can be separated? | CO1 |
| b) | Find the value of carry flag, zero flag, parity flag and auxiliary flag after the addition of FFH with 01H. | CO2 |
| c) | How many 128 x 8 RAM chips are needed to provide a memory capacity of 2048 bytes? | CO1 |

(2X4 = 8 Marks)

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|------|---|-----|
| a i) | What do you mean by tri state buffer? Draw all possible combination using truth table. | CO3 |
| | Or | |
| ii) | Differentiate between Parallel Data Transfer Scheme and Serial Data Transfer Scheme. | CO3 |
| b i) | Draw the timing diagram for opcode fetch cycle. | CO2 |
| | Or | |
| ii) | Discuss about instruction cycle, machine cycle, T-state and relation among them. | CO2 |
| c i) | Describe the flag register of 8085 microprocessor in detail. | CO2 |
| | Or | |
| ii) | How many types of registers used in 8085 microprocessors? How W and Z register are different from General purpose register? | CO2 |
| d i) | What is difference between memory mapped I/O and peripheral mapped I/O? | CO3 |
| | Or | |
| ii) | Draw the truth table for the 3:8 decoder (active low) by using following block diagram. | CO3 |

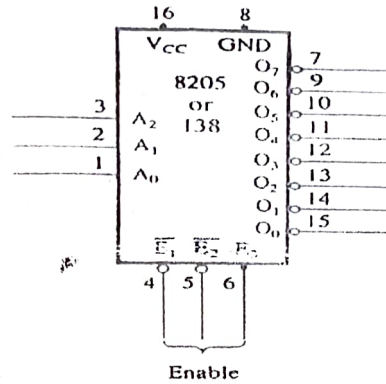


Fig. 1

Section C

(4X1 = 4 Marks)

Q3

i) Draw the pin diagram of 8085 and define all the pins related to timing and control unit.

CO2

Or

ii) Discuss about various block used in 8085 internal architectures with suitable diagram.

CO2