

- · allers data wi witorm skip
- · nald large cache

· if wer tries to read > size of cache, have to MALL MULTIPH Neads on shower Storage

```
Strall extruiti
 ISICK (O, 4114, SEEKSET); howto jump to new addresses
 stral 19
 requested # of bythe to read
  Vs-cd cache
Single-slot cache
                   = to FILE* from fopen() in standardilo
struct io61 file { <
                                                 library
   int fd; - File descriptor
   static constexpr off_t bufsize = 4096; // or whatever
   unsigned char cbuf[bufsize];
                           La can change
   off_t tag; // file offset of first byte in cache (0 when file is opened)
   off_t end_tag; // file offset one past last valid byte in cache
   off t pos tag; // file offset of next char to read in cache
};
          obuf
                         4075
                            unsigned onaracters
     Start 1
                       end
              pos-tag
                                        File
                                        POI- tag
CINCLES
I assuming one byth
    is the byth inside the buther or not
          KEEP Track of byn # of beginning and and
```

```
Facts about cacin representation
    · reads or wrins but not both
    · tag = -end-tag
    · Ind-tag-tag = but size

    Notalways equal be end/start tag can more

    · if tag == end-tag
             calm slot is empty (no valid nata)
       tag = end-tag and pos-tag = end-tag
                           nead
                                                       wwin
                           end-tag
current file pos =
       the next iou-read or iou-win can stans at
       Offict pos-tag.
    · if i >- tag and i == end-tag, clovf [i-tag] = bute
       At Offset i
 Filling cache
void io61 fill(io61 file* f) {
  // Fill the read cache with new data, starting from file offset `end_tag`.
  // Only called for read caches.
  // Check invariants.
  assert(f->tag <= f->pos_tag \&\& f->pos_tag <= f->end_tag);
  assert(f->end_tag - f->pos_tag <= f->bufsize);
  /* ANSWER */
  // Reset the cache to empty.
  f->tag = f->pos_tag = f->end_tag;
  // Read data. read (fire descriptor, butter, better size)
  ssize_t n = read(f->fd, f->cbuf, f->bufsize);
  if $ >= 0) { Trunix read
    f->end_tag = f->tag + n;
  >> n is-1 if failed read
  // Recheck invariants (good practice!).
  assert(f->tag <= f->pos_tag && f->pos_tag <= f->end_tag);
```

assert(f->end_tag - f->pos_tag <= f->bufsize);

Easy read

```
ssize_t io61_read(io61_file* f, char* buf, size_t sz) {
   // Check invariants.
   assert(f->tag <= f->pos_tag && f->pos_tag <= f->end_tag);
   assert(f->end_tag - f->pos_tag <= f->bufsize);
                           assuming hit.
   // The desired data is guaranteed to lie within this cache slot.
   assert(sz <= f->bufsize && f->pos_tag + sz <= f->end_tag);
                                                                   2. Vpdan pos-tag
   /* ANSWER */
 memcpy(buf, &f->cbuf[f->pos_tag - f->tag], sz);
 f->pos_tag += sz;
```

```
1. Calwan indux in
 clout and read
 that index
```

Full R-ead

```
ssize_t io61_read(io61_file* f, char* buf, size_t sz) {
   // Check invariants.
   assert(f->tag <= f->pos_tag && f->pos_tag <= f->end_tag);
                                                             1. loop that reads a
   assert(f->end_tag - f->pos_tag <= f->bufsize);
                                                               byn at a time cbuf->
   /* ANSWER */
   size_t pos = 0;
                                                                buf
   while (pos < sz) {</pre>
       if (f->pos_tag == f->end_tag) {
           io61_fill(f);
           if (f->pos_tag == f->end_tag) {
               break; or retvvn pos;
           }
       }
       // This would be faster if you used `memcpy`!
       buf[pos] = f->cbuf[f->pos_tag - f->tag];
       ++f->pos_tag;
       ++pos;
   }
   return pos;
```



```
ssize_t io61_write(io61_file* f, const char* buf, size_t sz) {
    // Check invariants.
    assert(f\rightarrow tag <= f\rightarrow pos_tag \&\& f\rightarrow pos_tag <= f\rightarrow end_tag);
    assert(f->end_tag - f->pos_tag <= f->bufsize);
    // Write cache invariant.
    assert(f->pos_tag == f->end_tag);
    // The desired data is guaranteed to lie within this cache slot.
    assert(sz \leftarrow f->bufsize && f->pos_tag + sz \leftarrow f->tag + f->bufsize);
    /* ANSWER */
    memcpy(&f->cbuf[f->pos_tag - f->tag], buf, sz);
    f->pos_tag += sz;
    f->end_tag += sz;
    return sz;
```