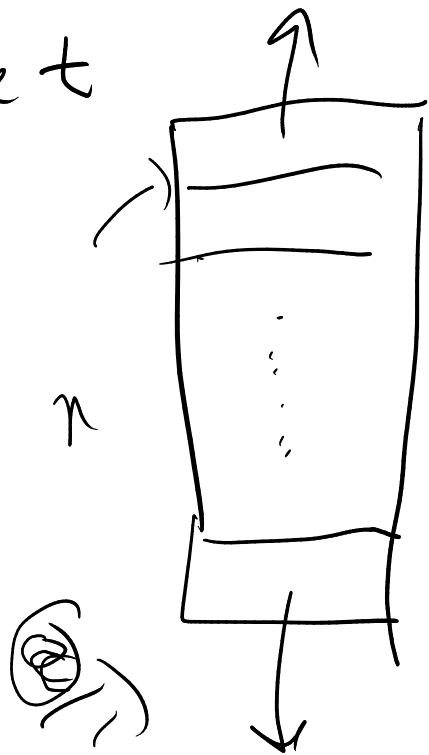
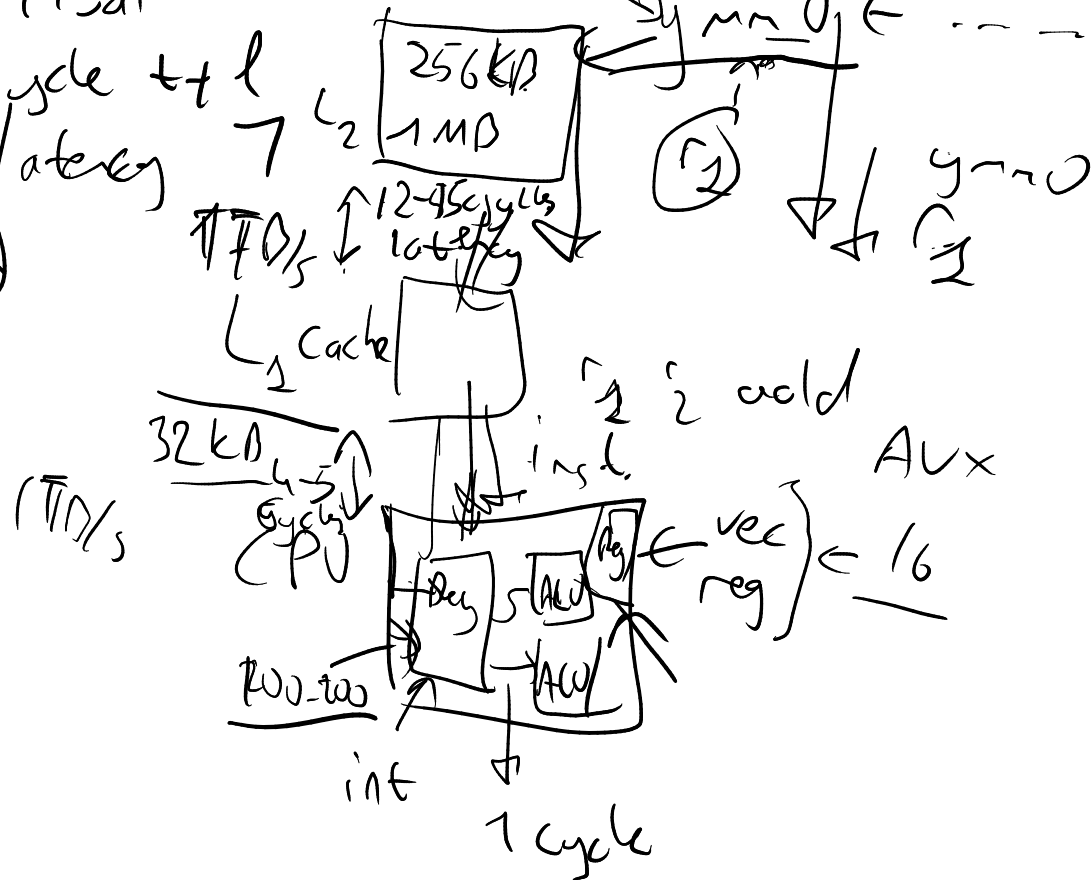


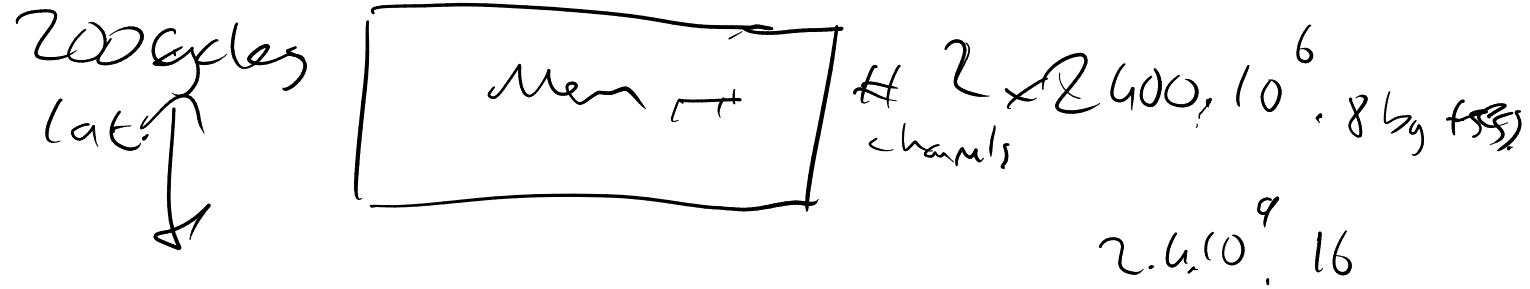
request
cycle t



arrival
cycle $t + l$
latency

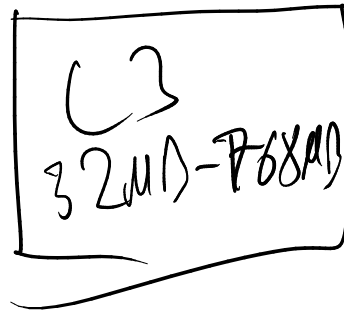


DDR4 2400 MT/s



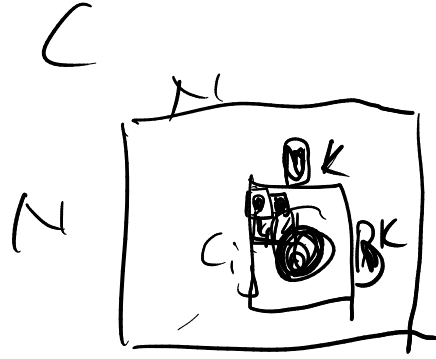
40-50 cycles
latency

300-500 GB/s

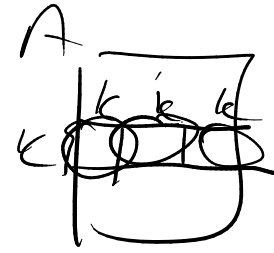
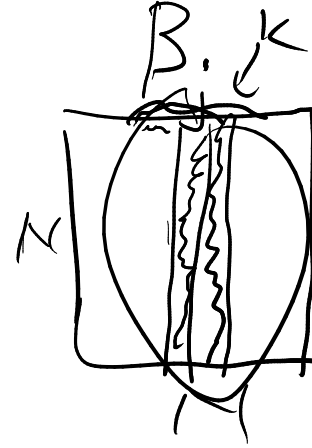
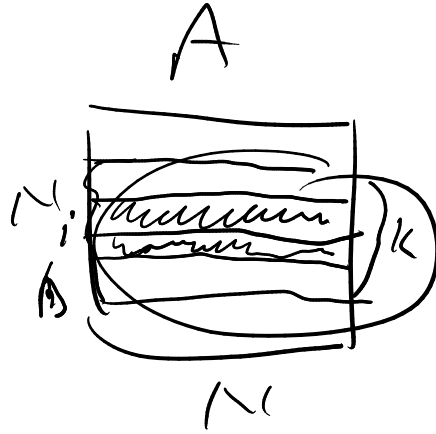


64 GB/s

40-



=



$$3 \times k \times k \downarrow \frac{1}{2}$$

$$C_{i,j} = \sum_{k=0}^{N-1} A_{i,k} \cdot B_{k,j}$$

$$\underline{N \% 2 = 0}$$

