

Communication Networks

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Outline

- 1 Communication networks
- 2 Communication on a ring
- 3 Communication on a hypercube

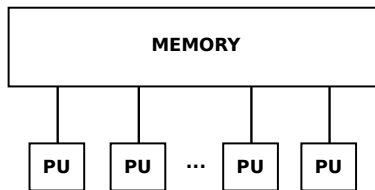
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Objectives

- Introduce the communication cost in to the parallel machine model
- Explore different communication network topologies
- Study communication algorithms in certain networks

Communication in PRAM

In a PRAM machine, PUs are connected by a shared memory



- If data exchange is needed, we can perform read/write to the same memory location.
- Access to each memory case is in constant time.
- Therefore, communication cost is constant for each pair of PUs.
- This is not a realistic model of a real parallel supercomputer.

In a real parallel machine, the PUs are connected by a communication network

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- A photograph showing a long row of black IBM System z mainframe computers in a data center. The units are tall and rectangular, with a distinctive green diagonal stripe on the front panel. The IBM logo is visible on the side of the units. The floor is light-colored and reflective, and the ceiling has a grid pattern.

Network topology

The connectivity graph of PUs form the **network topology**.

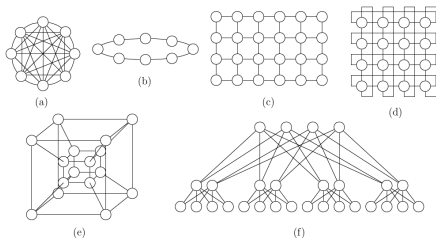


FIGURE 3.1: A few examples of interconnection network topologies: (a) clique; (b) ring; (c) grid; (d) torus; (e) hypercube; (f) fat-tree.

- **Static network:** Links are established beforehand and never change.
- Examples: Clique, ring, mesh, torus, hypercube.
- **Dynamic networks:** Links can be configured in runtime with switches.
- Examples: Fat-tree, butterfly.
- In general, more links = more efficient and cheaper communication.
- More links = more expensive network cost.
- It is a compromise.

Network parameters

The connectivity graph of PUs form the **topology** of the network that we can classify with certain parameters:

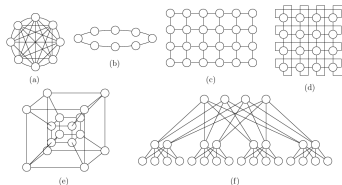


FIGURE 3.1: A few examples of interconnection network topologies: (a) clique; (b) ring; (c) grid; (d) torus; (e) hypercube; (f) fat-tree.

- **Number of nodes/PUs (p):** Number of processors in the network.
- **Degree (k):** Number of links connected to each node/PU. If it is not same for all, we specify (k_{min}, k_{max}) among all nodes.
- **Diameter (D):** Maximum distance among all node pairs.
- **Number of links (N_l):** Total number of links in the network.
- **Bisection width (L_B):** Number of links to remove in order to divide the network into two

Network parameters

Network parameters of certain networks:

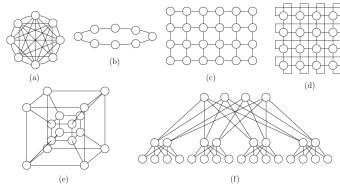


FIGURE 3.1: A few examples of interconnection network topologies:
(a) clique; (b) ring; (c) grid; (d) torus; (e) hypercube; (f) fat-tree.

TABLE 3.1: Main characteristics of classical topologies.

Topology	Num. of proc. p	Degree k	Diameter D	Num. of links N_l	Bisec. Width L_B
Clique	p	$p - 1$	1	$p(p - 1)/2$	$(p/2)^2$
Ring	p	2	$\lfloor p/2 \rfloor$	p	2
2-D Grid	$\sqrt{p}\sqrt{p}$	$2 \rightarrow 4$	$2(\sqrt{p} - 1)$	$2p - 2\sqrt{p}$	\sqrt{p}
2-D Torus	$\sqrt{p}\sqrt{p}$	4	$2\lfloor \sqrt{p}/2 \rfloor$	$2p$	$2\sqrt{p}$
Hypercube	$p = 2^d$	$d = \log(p)$	$d = \log(p)$	$p \log(p)/2$	$p/2$

Communication cost

Sending/receiving a message of m bytes by a link is $L + m/B = L + mb$

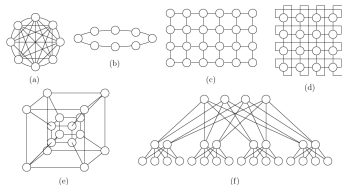


FIGURE 3.1: A few examples of interconnection network topologies: (a) clique; (b) ring; (c) grid; (d) torus; (e) hypercube; (f) fat-tree.

- L : Latency for the preparation and transmission of the message, independent of the message size (in seconds).
- m : Size of the message (in bytes)
- B : Link bandwidth (in bytes/second)
- $b = 1/B$: Inverse link bandwidth (in seconds/byte)
- **Example:** $L = 0.05s$, $B = 10GB/s$ $b = 0.1s/GB$, $m = 1GB$
 - Cost = $L + m/B = 0.05 + 1/10 = 0.15$ seconds.

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Unidirectional ring (cont.)

A topology with P processors, each processor P_i has a link to $P_{(i+1)\%P}$ and is able to execute following routines:

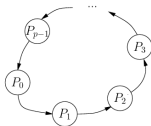


FIGURE 3.7: A unidirectional ring with p processors.

- MY_NUM: Returns the rank/identifier of each processor, $0 \leq \text{MY_NUM} < P$.
- NUM_PROCS: Returns the total number of processors P .
- SEND (*addr*, *m*): Sends m elements starting from the address *addr* to the processor $P_{(i+1)\%P}$.
- RECV (*addr*, *m*): Receives m elements starting from the address *addr* from the processor $P_{(i-1)\%P}$.
- Each SEND must correspond to a RECV (otherwise a bug)

Unidirectional ring (cont.)

A topology with P processors, each processor P_i has a link to $P_{(i+1)\%P}$ and is able to execute following routines:

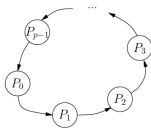


FIGURE 3.7: A unidirectional ring with p processors.

- *addr* is the address of an array in the local memory of the processor.
- We typically suppose that a call to `SEND (addr, m)` does not block the processor (it continues with the rest of the computation while `SEND` is executed in the background).
- `RECV (addr, m)` blocks the processor.

Broadcast on a unidirectional ring

Given the index of a processor k , send a message of size m to all processors

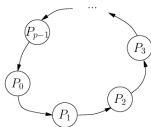


FIGURE 3.7: A unidirectional ring with p processors.

- The idea is to relay the message from left to right while keeping a local copy.

Broadcast on a unidirectional ring (cont.)

Given the index of a processor k , send a message of size m to all processors

```
1 BROADCAST( $k, addr, m$ )
2    $q \leftarrow \text{MY\_NUM}()$ 
3    $p \leftarrow \text{NUM\_PROCS}()$ 
4   if  $q = k$  then
5     SEND( $addr, m$ )
6   else
7     if  $q = k - 1 \bmod p$  then
8       RECEIVE( $addr, m$ )
9     else
10      RECEIVE( $addr, m$ )
11      SEND( $addr, m$ )
```

- The idea is to relay the message from left to right while keeping a local copy.
- Root processor performs one SEND and no RECV.
- Processor before root performs one RECV and no SEND.
- All others perform a RECV followed by a SEND.
- Complexity: $(p - 1)(L + mb)$

Scatter on a unidirectional ring

Given a processor index k , send the message at the address $addr[i]$ of size m to each processor $0 \leq l < p$

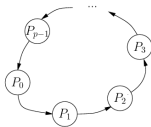


FIGURE 3.7: A unidirectional ring with p processors.

- Idea is still relaying messages from left to right.
- We should try to avoid transferring all messages each time! (slow)
- Send the message from the root in the reverse processor index order.
- The message of $P_{(k-1)\%p}$, then $P_{(k-2)\%p}$, etc.
- **Warning:** Each processor performs a different number of sends and receives!

Scatter on a unidirectional ring (cont.)

Given a processor index k , send the message at the address $addr[i]$ of size m to each processor $0 \leq l < p$

```
1 SCATTER( $k, msg, addr, m$ )
2    $q \leftarrow MY\_NUM()$ 
3    $p \leftarrow NUM\_PROCS()$ 
4   if  $q = k$  then
5     for  $i = 1$  to  $p - 1$  do
6       SEND( $addr[k + p - i \bmod p], m$ )
7      $msg \leftarrow addr[k]$ 
8   else
9     RECEIVE( $tempR, m$ )
10    for  $i = 1$  to  $k - 1 - q \bmod p$  do
11       $tempS \leftarrow tempR$ 
12      SEND( $tempS, m$ ) || RECEIVE( $tempR, m$ )
13     $msg \leftarrow tempR$ 
```

- || indicates parallel execution.
- Complexity: $(p - 1)(L + mb)$.
- Same as BROADCAST despite different message sent to each processor. Why?
 - Because the network is better utilized.
 - Can we do better BROADCAST then using the same idea? We will see in the exercise session.

All-to-all on a unidirectional network

This time, each processor k has a message to address $my_message$ of size m to send to all processors, to put in $addr[k]$ of each processor.

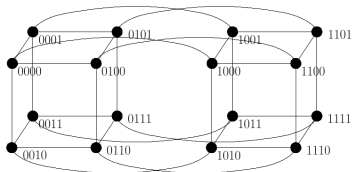
```
1 ALL_To_All(my_message, addr, m)  
2    $q \leftarrow MY\_NUM()$   
3    $p \leftarrow NUM\_PROCS()$   
4    $addr[q] \leftarrow my\_message$   
5   for  $i = 1$  to  $p - 1$  do  
6     SEND( $addr[q - i + 1 \bmod p], m$ ) ||  
     RECEIVE( $addr[q - i \bmod p], m$ )
```

- Equivalent to p BROADCAST s, but we can do better.
- Complexity: $(p - 1)(L + mb)$.
- Same as BROADCAST and SCATTER despite transferring more/different messages. Why?
 - Because the network is better utilized.

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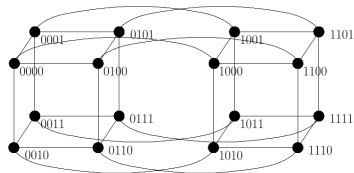
Hypercube

A d -dimensional hypercube, called d -cube, consists of



- $p = 2^d$ nodes and $p \log_2 p = 2^d d$ links,
- two $(d - 1)$ -cubes whose each pair of corresponding nodes i ($0 \leq i < 2^{d-1}$) connected with a link.
- Nodes having exactly $d = \log_2 p$ connections.
- It suffices to flip the bit i ($0 \leq i < d$) in the binary representation of the rank of a node to find its i .th neighbor.
- **Example:** $\text{Neighbors}(0000) = \{0001, 0010, 0100, 1000\}$.

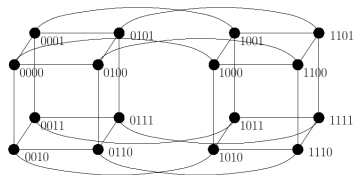
Message transmission on a d -cube:



- Send a message to the neighbor i , SEND (i , addr, m)
- Receive a message from neighbor i , RECV (i , addr, m)
- **Cost** : $L + m/B = L + mb$
- Possible to send a message to any node in at most $d = \log_2 p$ steps maximum.

Broadcast on a hypercube

Idea: Recursive doubling of the message



- Suppose that we broadcast from P_0 .
- Initially, BROADCAST is already done on a 0-cube.
- In phase t , BROADCAST will be done on a t -cube.
 - by using messages in the $(t - 1)$ -cube
 - each node sends its message to its neighbor $(t - 1)$
- In phase d , entire BROADCAST will be done.

Broadcast on a hypercube

Idea: Recursive doubling of the message

```
1 BROADCAST(k, addr, m)
2   q ← MY_NUM()
3   n ← log(TOT_PROC_NUM())
4   { Update pos to work as if P0 was the root of the broadcast }
5   pos ← q XOR k
6   { Find the rightmost 1 }
7   first1 ← 0
8   while ((BIT(pos, first1) = 0) And (first1 < n)) do
9     first1 ← first1 + 1
10  { Core of the algorithm }
11  for phase = n - 1 to 0 do
12    if (phase = first1) then RECEIVE(phase, addr, m)
13    else if (phase < first1) then SEND(phase, addr, m)
```

- If *root* is not 0, we can XOR all ranks by *k*.
- Find the first left bit 1, then RECV the message in that phase.
- Then, in all successive phases, SEND the message
- **Cost:** $d(L + mb) = \log_2 p(L + mb)$.

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