Senester 6 TimeTable

Monday Schedule:

- 8:45 to 9:45: Digital signal Processing (DSP) lecture
- 9:45 to 10:45: Data Communication and Computer Networks (DCCN) lecture
- 10:45 to 11:00: 15-minute break
- 11:00 to 13:00: Practical sessions:
- A1 batch: Data Communication and Computer Networks (DCCN)
- A2 batch: SCILAB
- A3 batch: Digital Communication (DCOM)
- 13:00 to 13:30: 30-minute break
- 13:30 to 15:30: Practical sessions:
- A1 batch: Technical Writing (TW)
- A2 batch: Advance power electronics
- A3 batch: Digital signal Processing (DSP)
- 15:30 to 16:30: Data Communication and Computer Networks (DCCN) lecture Tuesday Schedule:
- 8:45 to 9:45: Digital Communication (DCOM) lecture
- 9:45 to 10:45: Digital signal Processing (DSP) lecture
- 10:45 to 11:00: 15-minute break
- 11:00 to 13:00: Practical sessions:
- A1 batch: Digital Communication (DCOM)
- A2 batch: Data Communication and Computer Networks (DCCN)
- A3 batch: SCILAB
- 13:00 to 13:30: 30-minute break
- -1:30 to 2:30 Advance power electronics lecture (APE)
- 2:30 to 3:30 Data Communication and Computer Networks (DCCN) lecture
- 3:30 to 4:30 project 2 practical

Wednesday Schedule:

- 8:45 to 9:45: Industrial management and quality control (IMQC) lecture
- 9:45 to 10:45: Digital signal Processing (DSP) lecture
- 10:45 to 11:00: 15-minute break
- 11:00 to 13:00: Practical sessions:
- A1 batch: SCILAB
- A2 batch: Digital Communication (DCOM)
- A3 batch: Data Communication and Computer Networks (DCCN)
- 13:00 to 13:30: 30-minute break
- -1:30 to 2:30 Digital Communication (DCOM) Lecture
- 2:30 to 3:30 Advance power electronics (APE) Lecture
- 3:30 to 4:30 project 2 practical

Thursday Schedule:

- 8:45 to 10:45: Practical sessions:
- A1 batch: Digital signal Processing (DSP)
- A2 batch: Technical Writing (TW)
- A3 batch: Advance power electronics (APE)
- 10:45 to 11:00: 15-minute break
- 11:00 to 12:00: Industrial management and quality control (IMQC) lecture
- 12:00 to 1:00: Digital Communication (DCOM) Lecture
- 13:00 to 13:30: 30-minute break
- -1:30 to 2:30 Advance power electronics (APE) Lecture
- 2:30 to 3:30 Industrial management and quality control (IMQC) lecture
- 3:30 to 4:30 project 2 practical

Friday Schedule:

- 8:45 to 10:45: Practical sessions:

- A1 batch: Advance power electronics (APE)
- A2 batch: Digital signal Processing (DSP)
- A3 batch: Technical Writing (TW)
- 10:45 to 11:00: 15-minute break
- 11:00 to 12:00: Digital Communication (DCOM)
- 12:00 to 1:00: Advance power electronics (APÉ) Lecture
- 13:00 to 13:30: 30-minute break
- -1:30 to 2:30 Digital signal Processing (DSP)
- 2:30 to 3:30 Data Communication and Computer Networks (DCCN)
- 3:30 to 4:30 project 2 practical

Batch Information:

A1 batch is from 2103070 to 2103052 (26 students)

A2 batch is from 2103058 to 2103167 (28 students)

A3 batch is from 2103173 to 2103209 (24 students)