Automatically Verifying Replicated Data Types

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Joint work with Vimala Soundarapandian, Aseem Rastogi and Kartik Nagar

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Will appear at OOPSLA 2025





Collaborative Applications

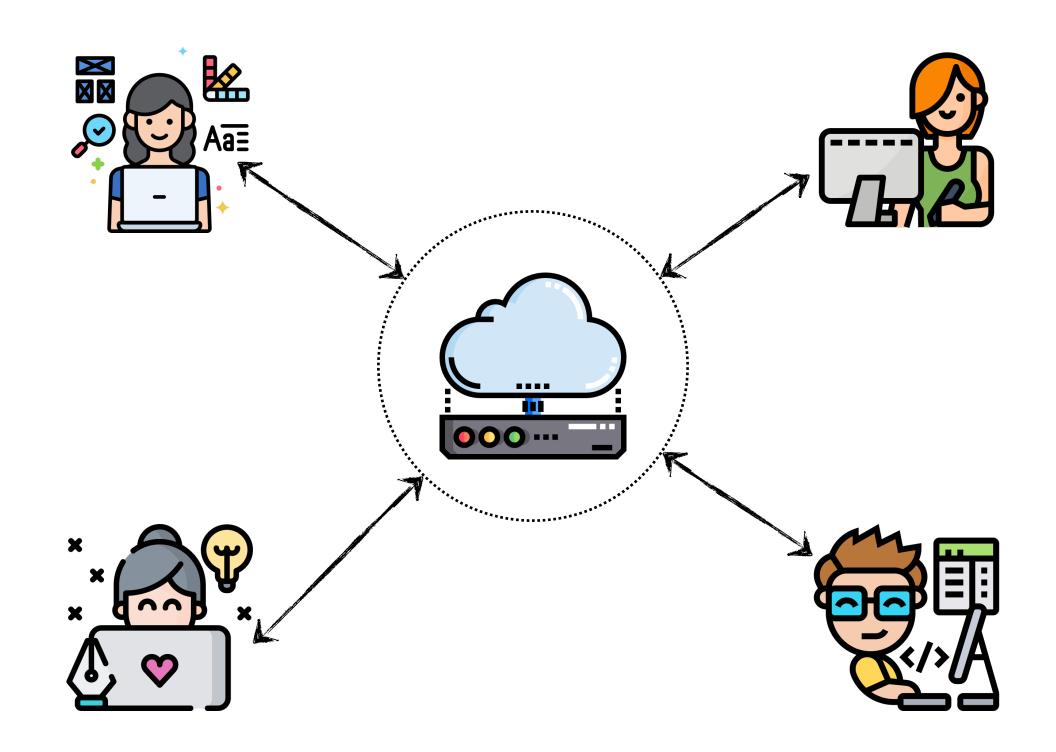












Collaborative Applications

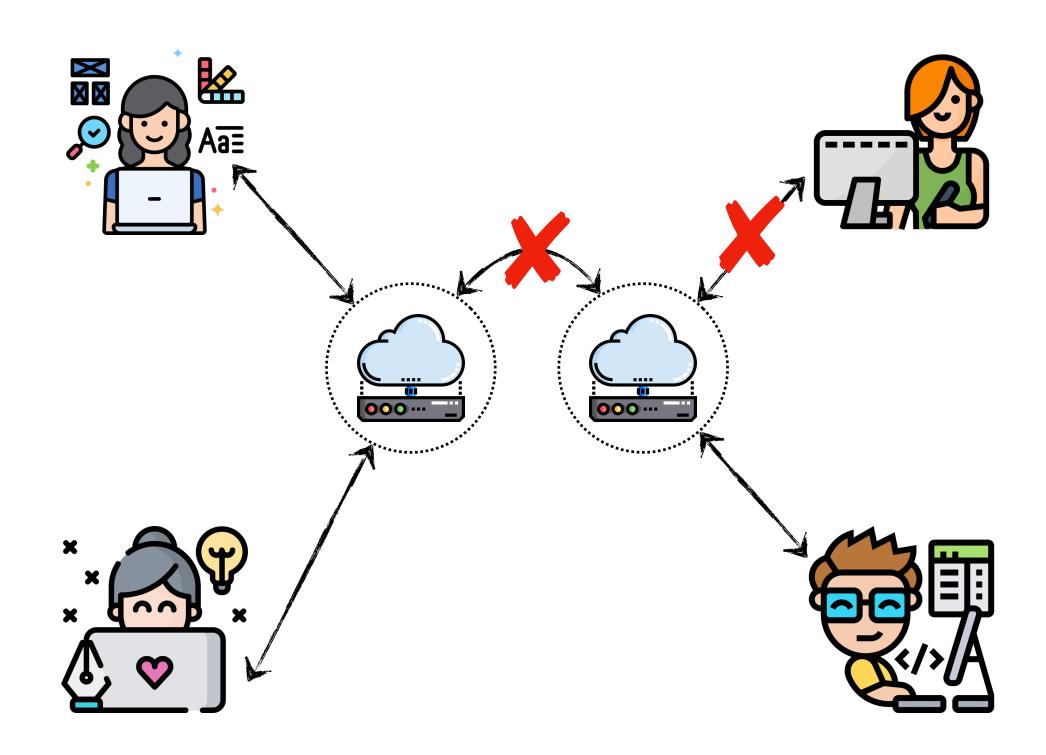






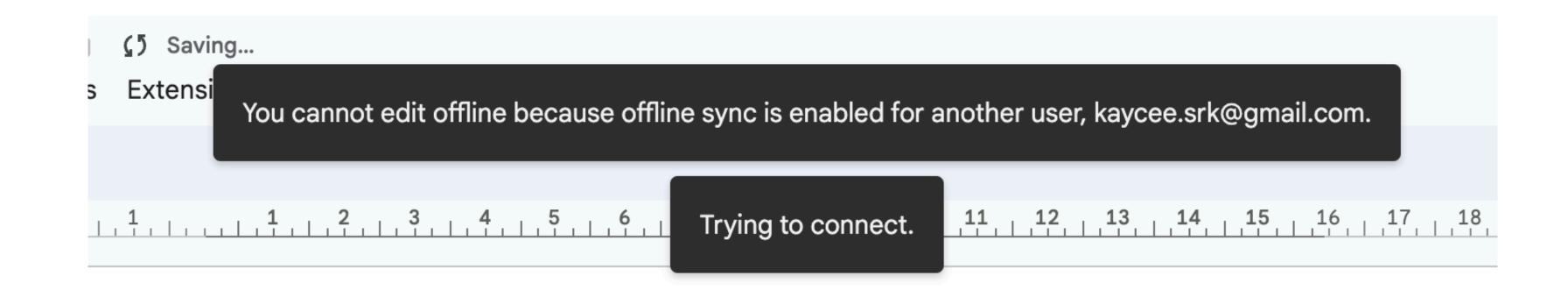






Network Partitions

Centralised Apps provide limited support for offline editing



Enabling offline sync for one account prevents other accounts from working offline

Local-first software

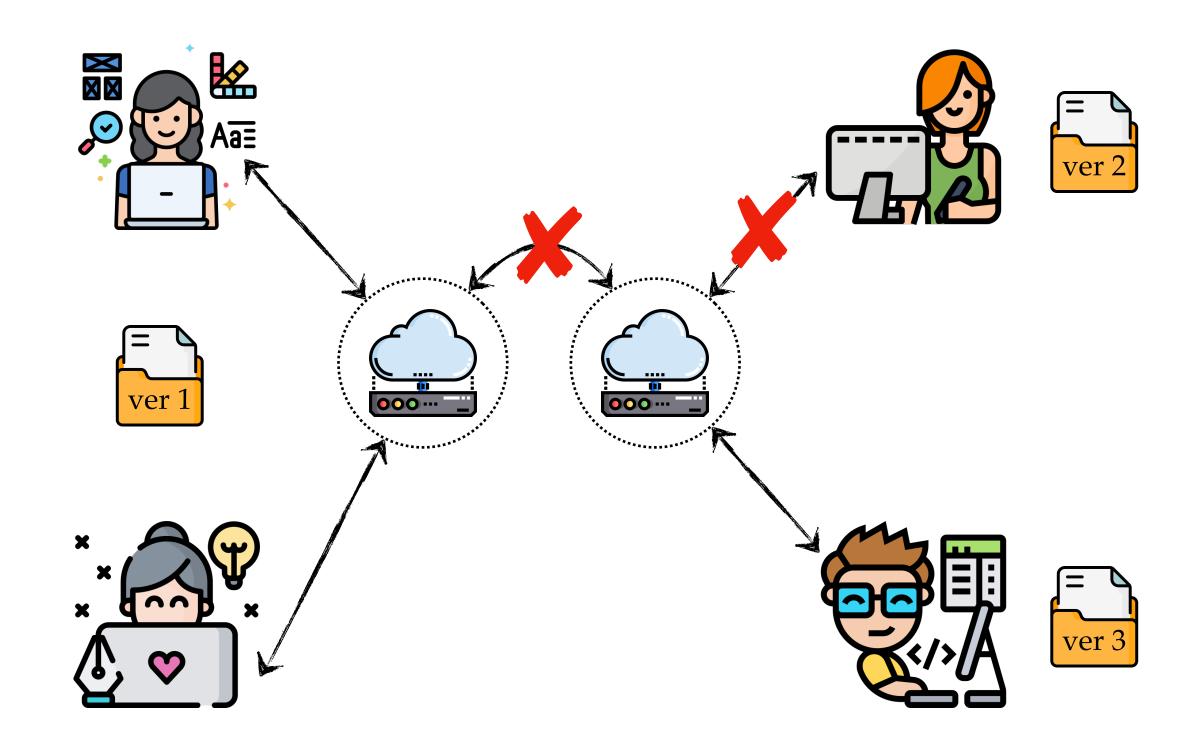












Local-first software

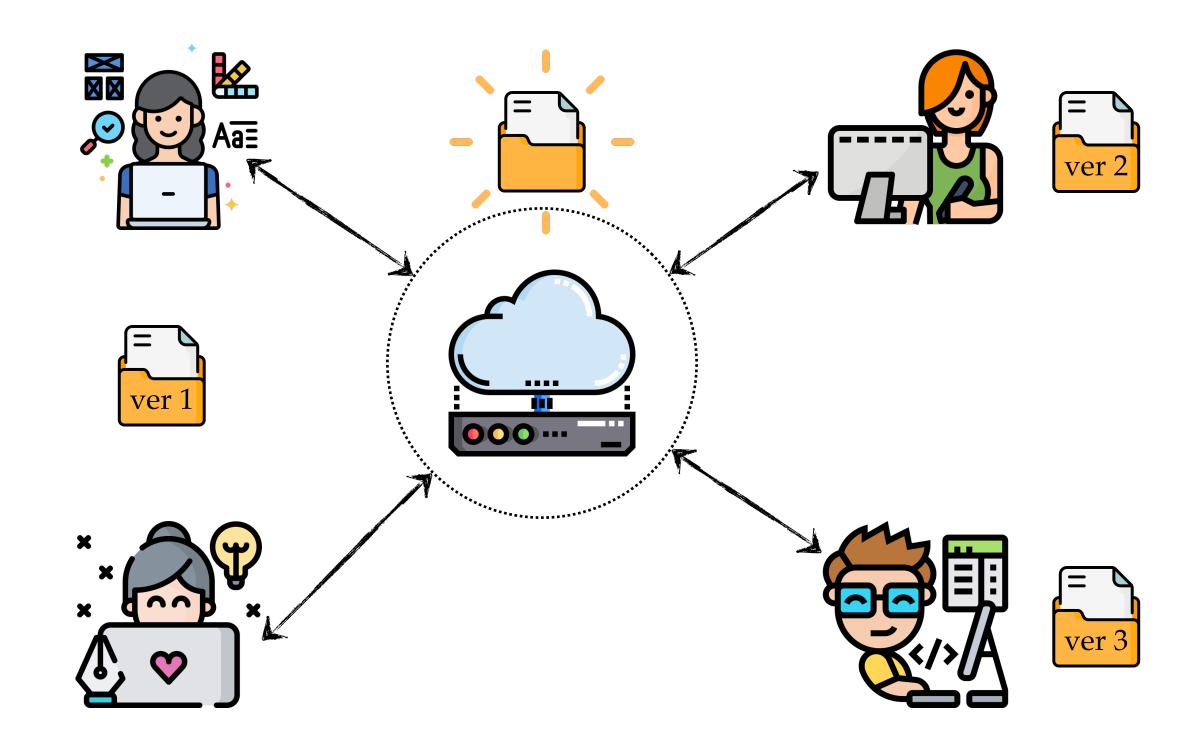










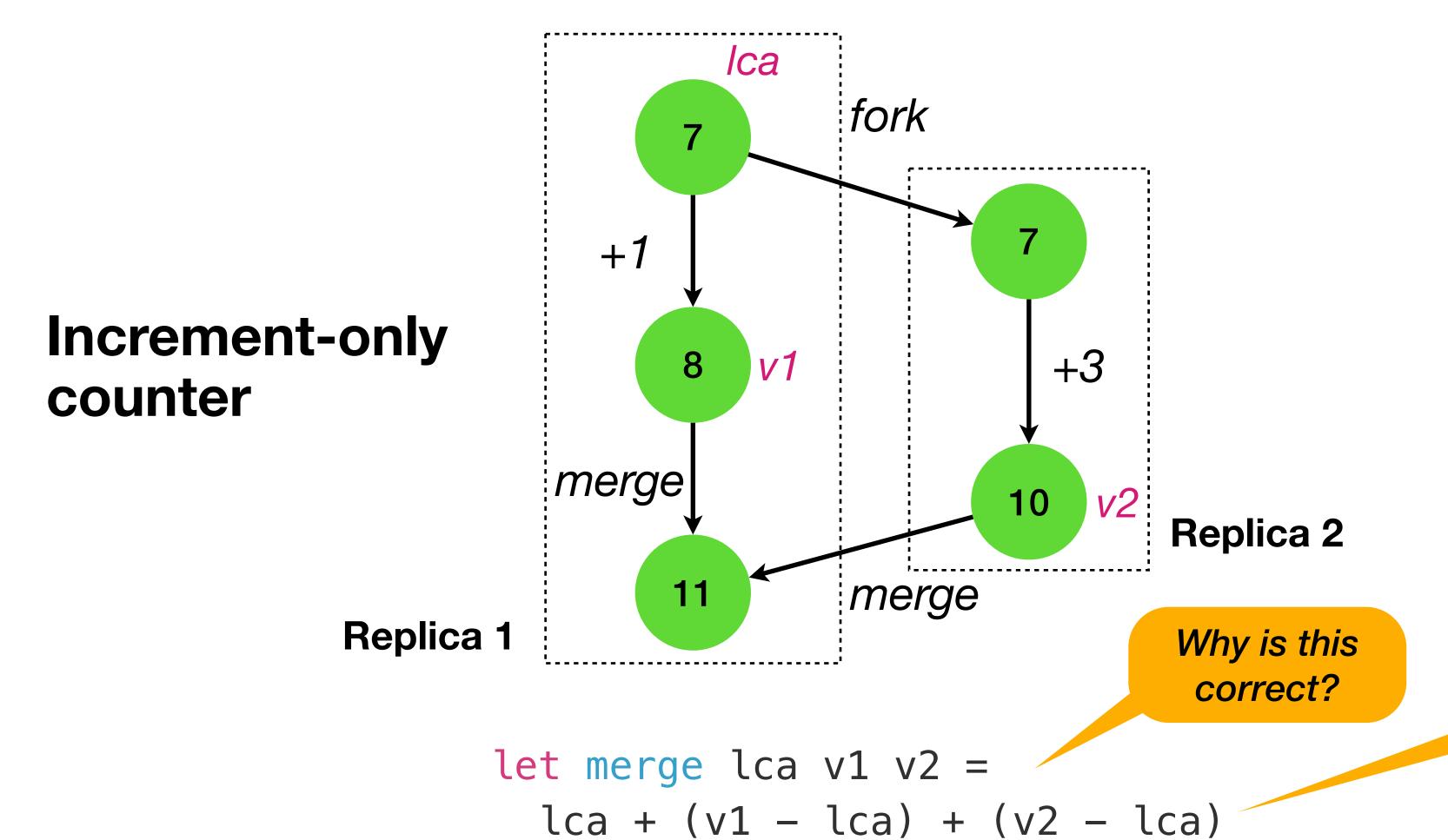


How do we build such applications?

Embed the notion of replication into the data types

Mergeable Replicated Data Types (MRDTs)

MRDTs = Sequential data types + 3-way merge function à la Git



How do we automatically verify it?

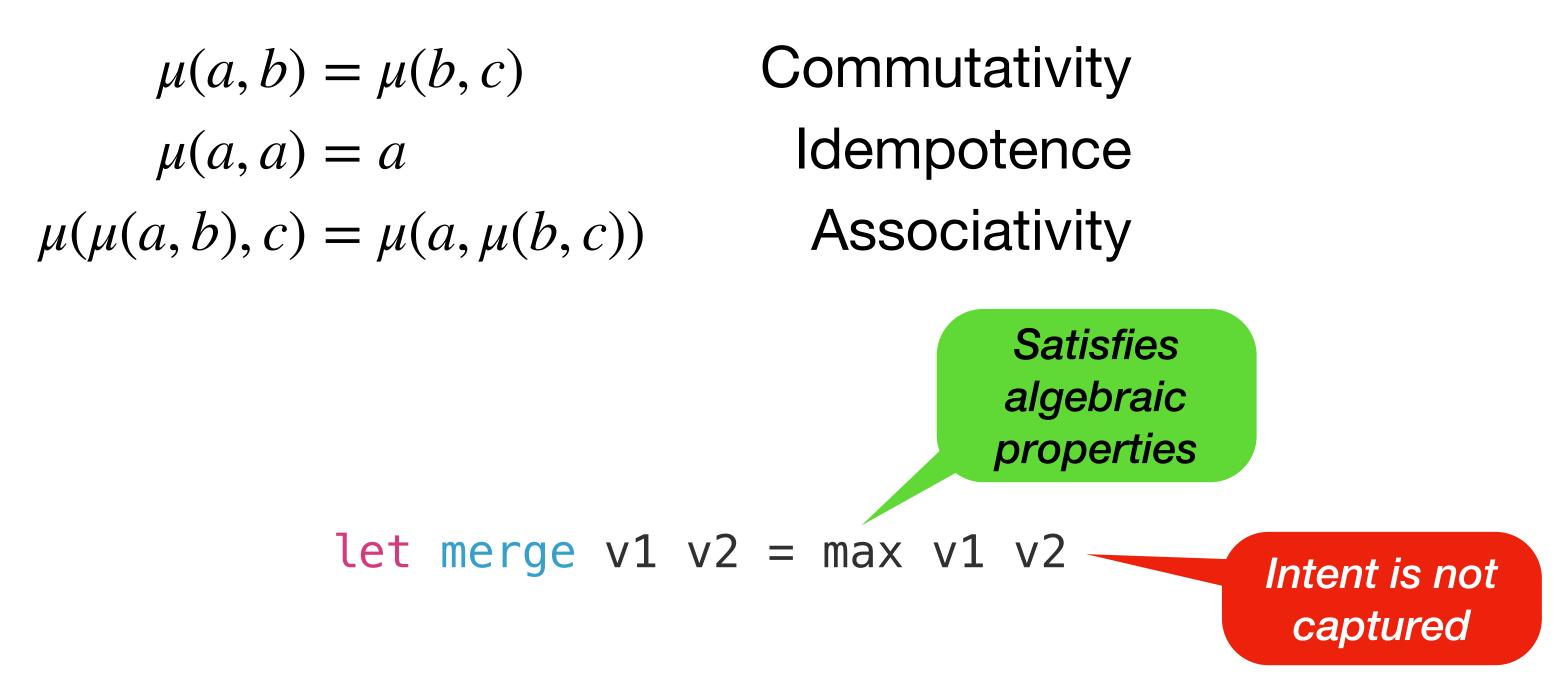
Verification using Algebraic Properties

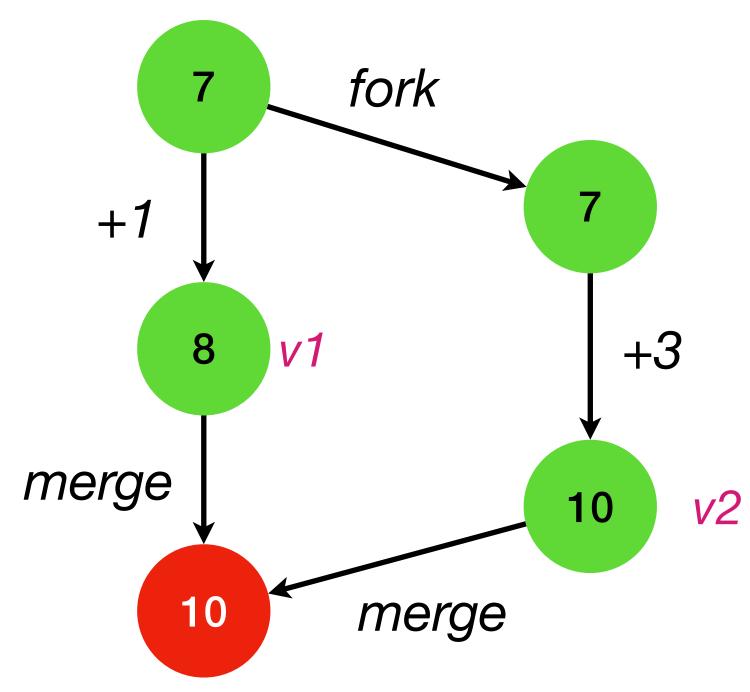
- State-based Convergent Replicated Data Types (CRDTs)
 - Merge is 2-way $-\mu(v_1, v_2)$

Verify program correctness automatically and seamlessly.

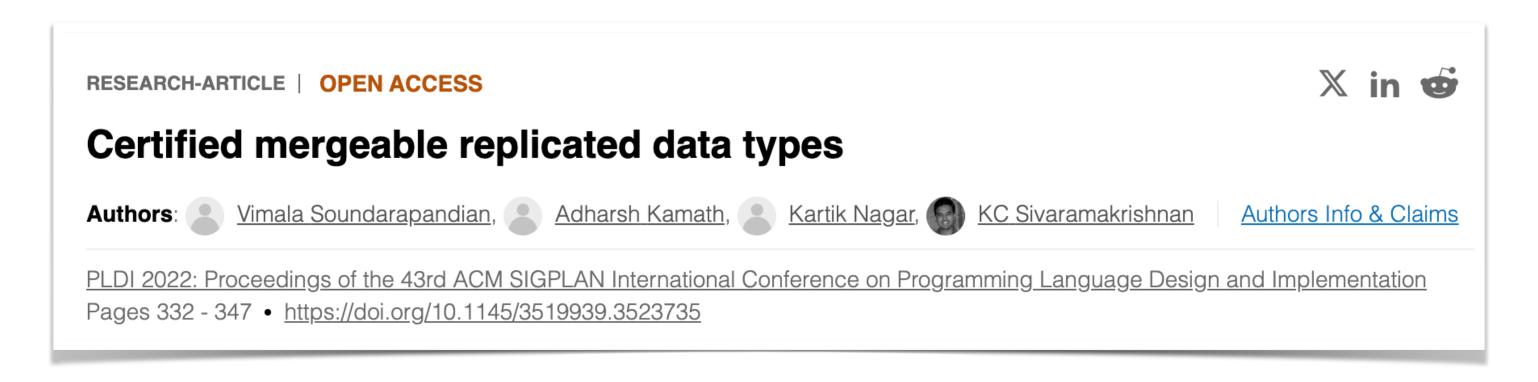
Define the correctness properties and off you go ?

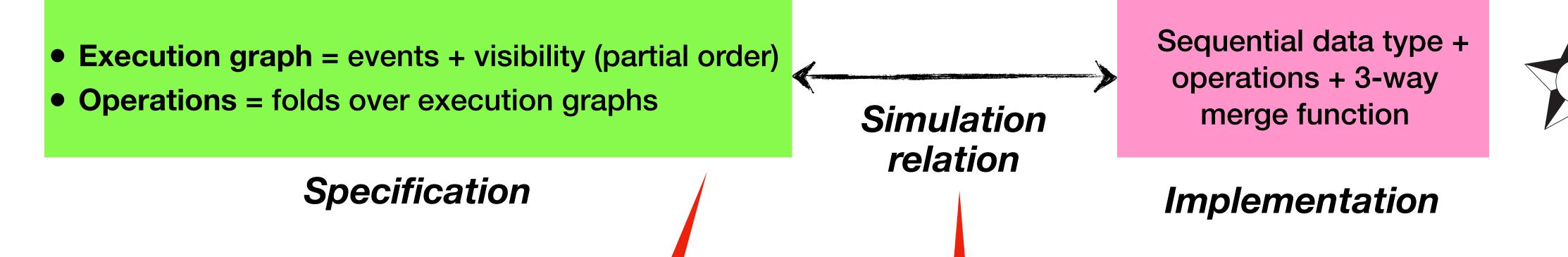
Verify algebraic properties of merge for strong eventual consistency





Capturing Intent through Axiomatic Spec

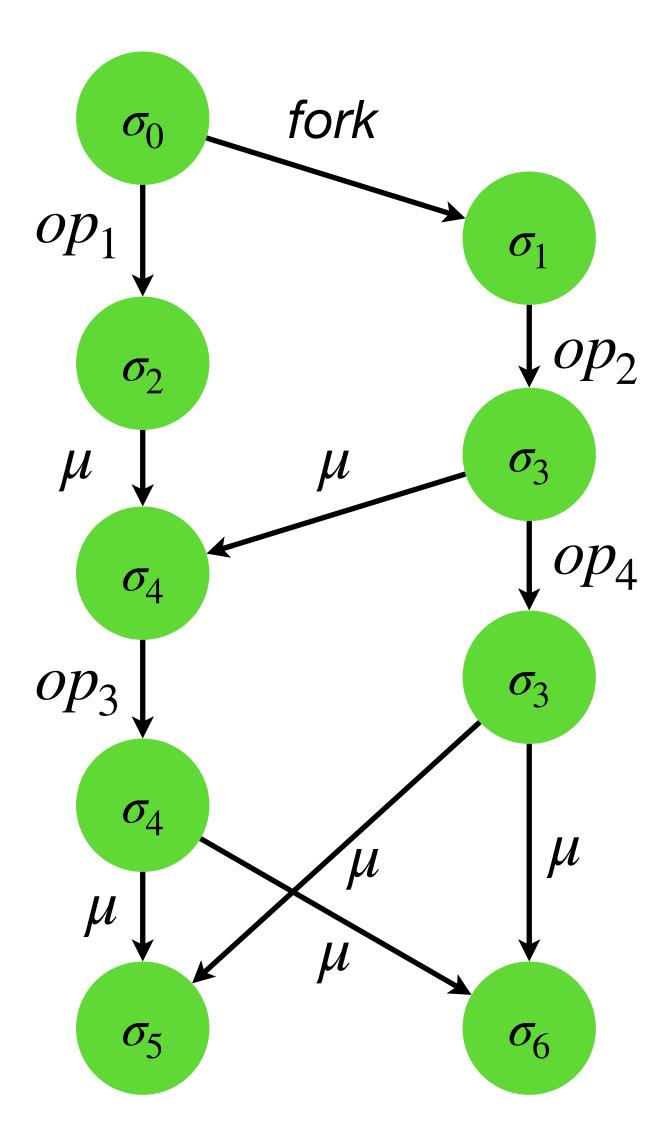




Complex

Manual

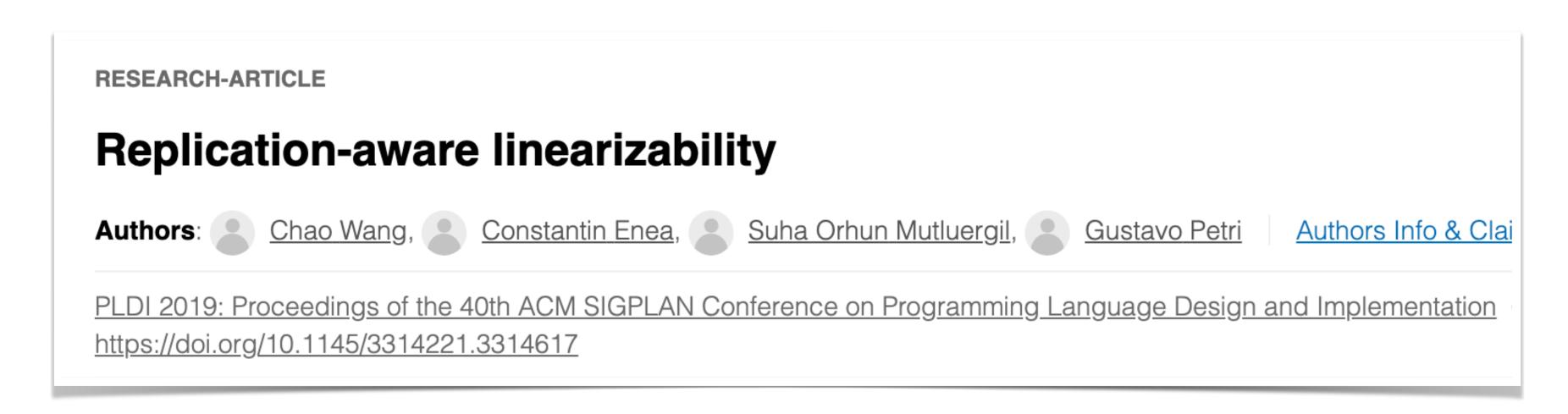
Is there a more natural spec?



 $\sigma_5 = \sigma_6 = linearization(\{op_1, op_2, op_3, op_4\}) \sigma_0$

Replication-aware Linearizability

- Replica states should be a linearisation of observed update operations
 - Use commutativity and asynchrony → Replication-aware (RA) linearizability



 All replicas should converge to the same state — Strong Eventual Consistency

Neem — Automatic verification of RDTs

- What's in the box?
 - Definition of RA-linearizability for MRDTs
 - A novel induction scheme for MRDTs and state-based CRDTs to automatically verify RA-linearizability
 - Implemented in F*

Resolving conflicts

- Not all operations commute
 - Add-wins set add(a) and rem(a) do not commute
 - Specify ordering using the Conflict Resolution relation $rc = \{(rem_a, add_a) \mid a \in \mathbb{E}\}$
- Neem developers provide
 - MRDT = Sequential Data Type + 3-way merge
 - Conflict Resolution rc relation

Increment-only Counter

```
State 1: \Sigma = \mathbb{N} Unique timestamp

Updates 2: O = \{ \text{inc} \}
Queries 3: Q = \{ \text{rd} \}
Replica ID

Update behaviour 5: \text{do}(\sigma, \_, \_, \text{inc}) = \sigma + 1

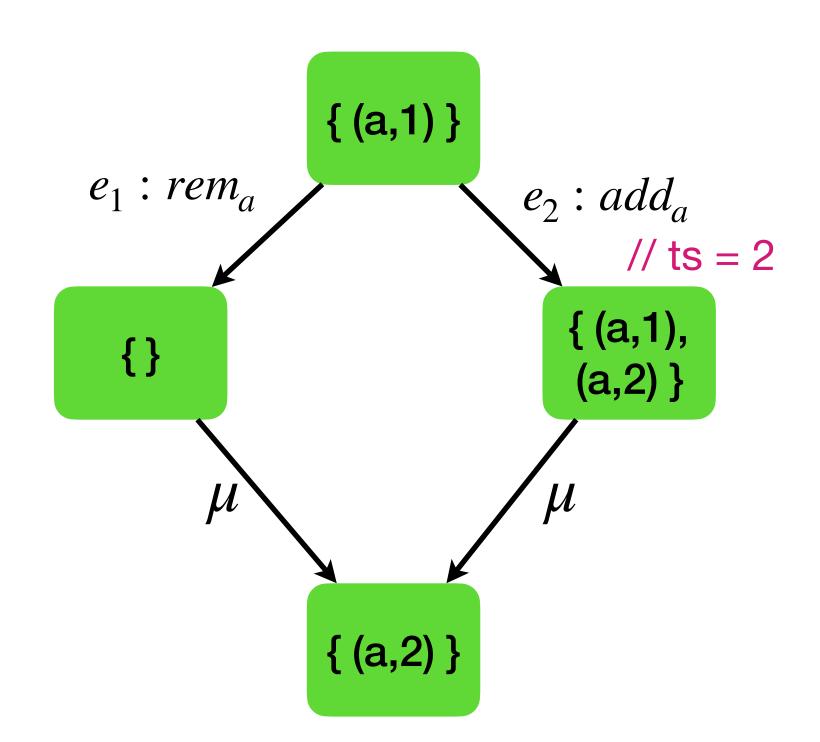
Merge 6: \text{merge}(\sigma_\top, \sigma_1, \sigma_2) = \sigma_\top + (\sigma_1 - \sigma_\top) + (\sigma_2 - \sigma_\top)

Query behaviour 7: \text{query}(\sigma, rd) = \sigma

Resolve conflict 8: \text{rc} = \emptyset
```

Add-wins Set

```
State 1: \Sigma = \mathcal{P}(\mathbb{E} \times \mathcal{T})
             Updates 2: O = \{add_a, rem_a \mid a \in \mathbb{E}\}
              Queries 3: Q = \{rd\}
            Init State 4: \sigma_0 = \{\}
               Update 5: do(\sigma, t, \_, add_a) = \sigma \cup \{(a, t)\}
           behaviour
                               6: do(\sigma, \_, \_, rem_a) = \sigma \setminus \{(a, i) \mid (a, i) \in \sigma\}
                               7: merge(\sigma_{\mathsf{T}}, \sigma_1, \sigma_2) =
                 Merge
                                         (\sigma_{\mathsf{T}} \cap \sigma_1 \cap \sigma_2) \cup (\sigma_1 \backslash \sigma_{\mathsf{T}}) \cup (\sigma_2 \backslash \sigma_{\mathsf{T}})
Query behaviour 8: query (\sigma, rd) = \{a \mid (a, \_) \in \sigma\}
Resolve conflict 9: rc = \{(rem_a, add_a) \mid a \in \mathbb{E}\}
```



$$\{(a,2)\} = add_a(rem_a\{(a,1)\})$$

RA-Linearizability Challenge

- Should the linearisation total order be consistent with
 - Conflict Resolution ordering for concurrent events? $(rc \cap ||) \subseteq lo$
 - And, Visibility? $vis \subseteq lo$

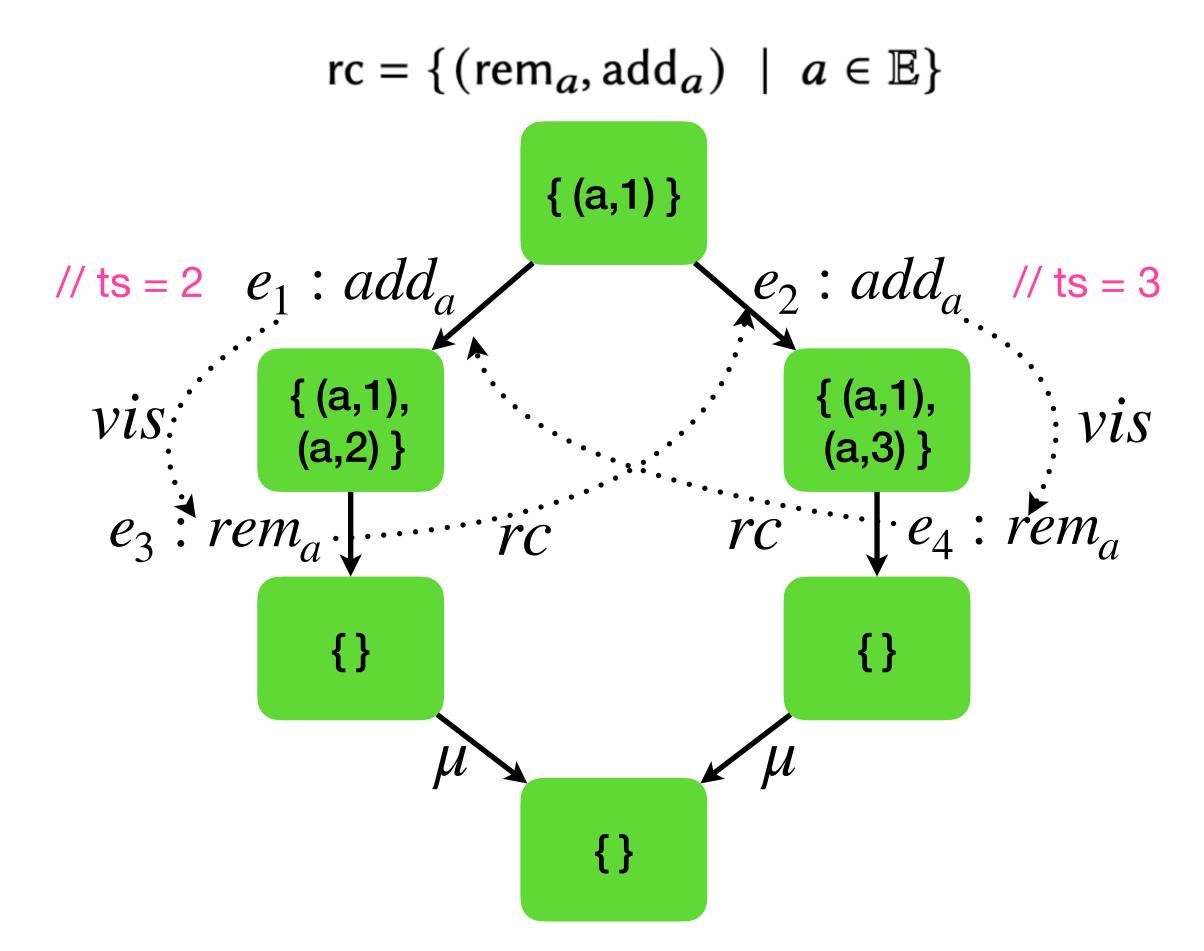
$$e_{1} \xrightarrow{vis} e_{3}$$

$$e_{3} \parallel e_{2} \wedge e_{3} \xrightarrow{rc} e_{2}$$

$$e_{2} \xrightarrow{vis} e_{4}$$

$$e_{4} \parallel e_{1} \wedge e_{4} \xrightarrow{rc} e_{1}$$

lo cannot be total order since $(rc \cap ||) \cup vis$ is not irreflexive



RA-Linearizability Challenge

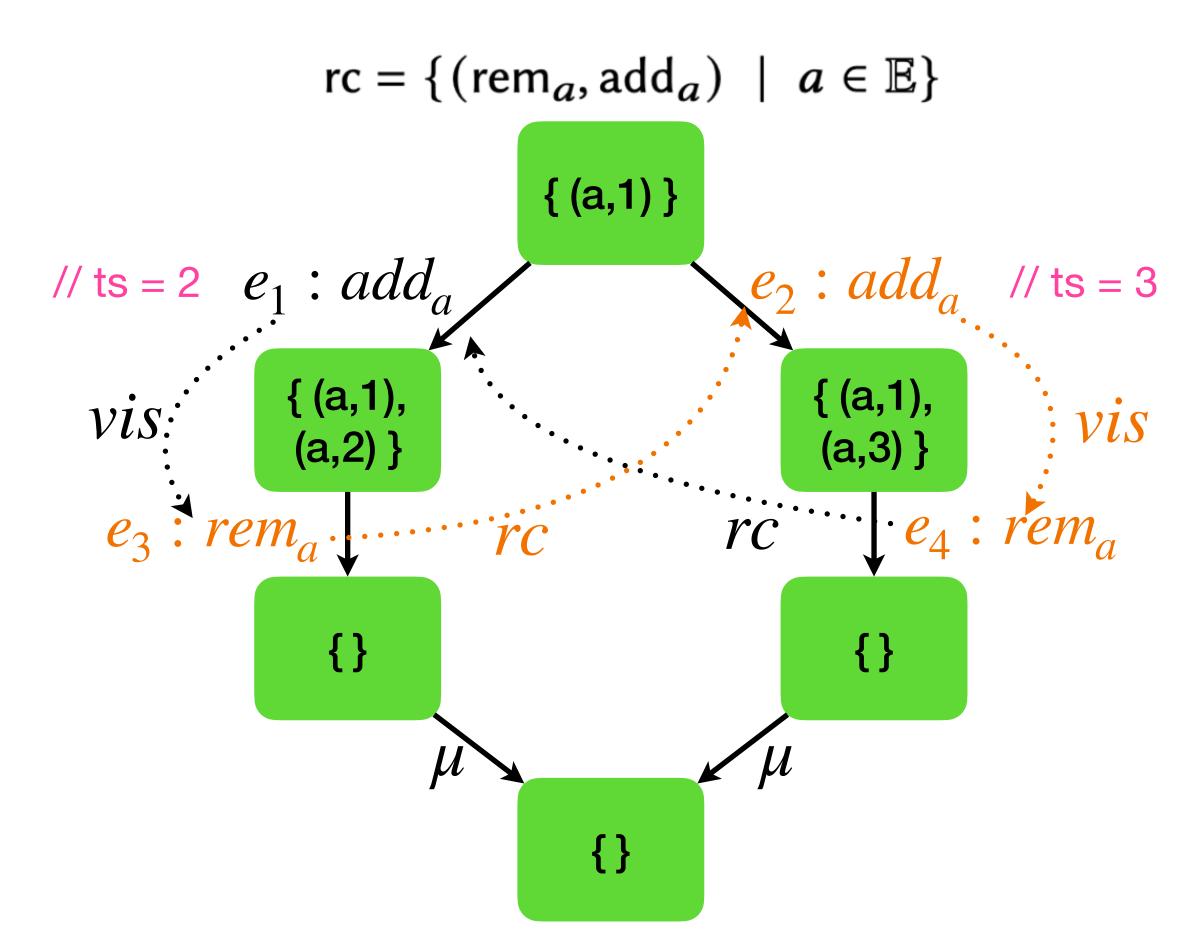
- Should the linearisation total order be consistent with
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$$e_{1} \xrightarrow{vis} e_{3}$$

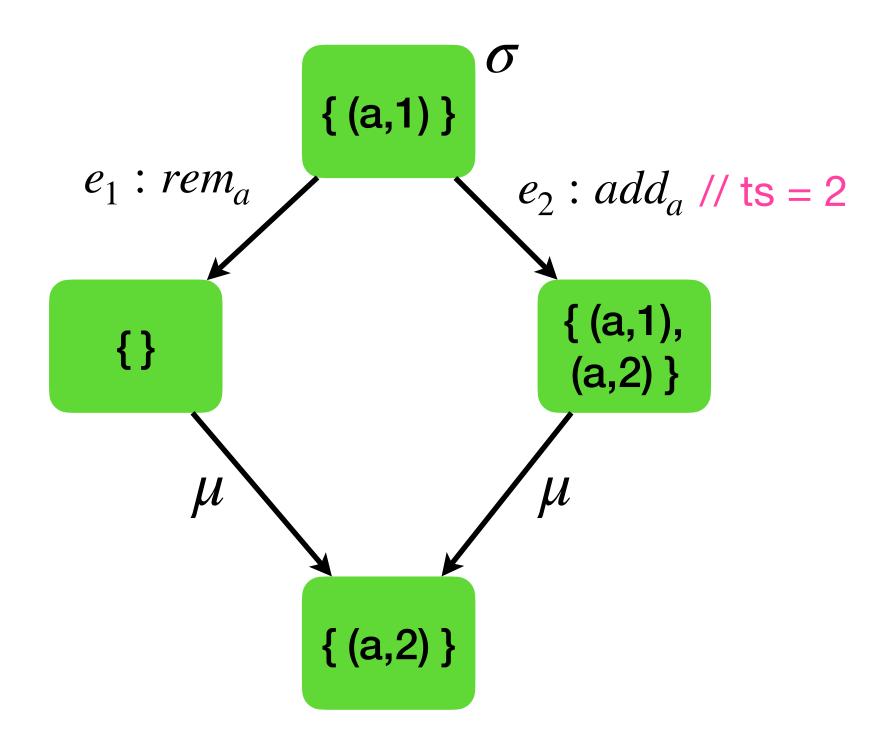
$$e_{2} \xrightarrow{vis} e_{4}$$

$$e_{4} \parallel e_{1} \wedge e_{4} \xrightarrow{rc} e_{1}$$

 e_3 conditionally commutes wrt e_2 due to e_4



$$rc = \{(rem_a, add_a) \mid a \in \mathbb{E}\}$$



To show

$$\mu(\sigma, e_1(\sigma), e_2(\sigma)) = e_2(e_1(\sigma))$$

[Воттом Up-2-OP]

$$\frac{e_1 \neq e_2 \quad e_1 \xrightarrow{\text{rc}} e_2 \lor e_1 \rightleftarrows e_2}{\mu(l, e_1(a), e_2(b)) = e_2(\mu(l, e_1(a), b))}$$

[BottomUp-1-OP]

$$\frac{(e_{\top} \neq \epsilon \land e_{1} \neq e_{\top}) \lor (e_{\top} = \epsilon \land l = b)}{\mu(e_{\top}(l), e_{1}(a), e_{\top}(b)) = e_{1}(\mu(e_{\top}(l), a, e_{\top}(b)))}$$

[BOTTOMUP-0-OP]

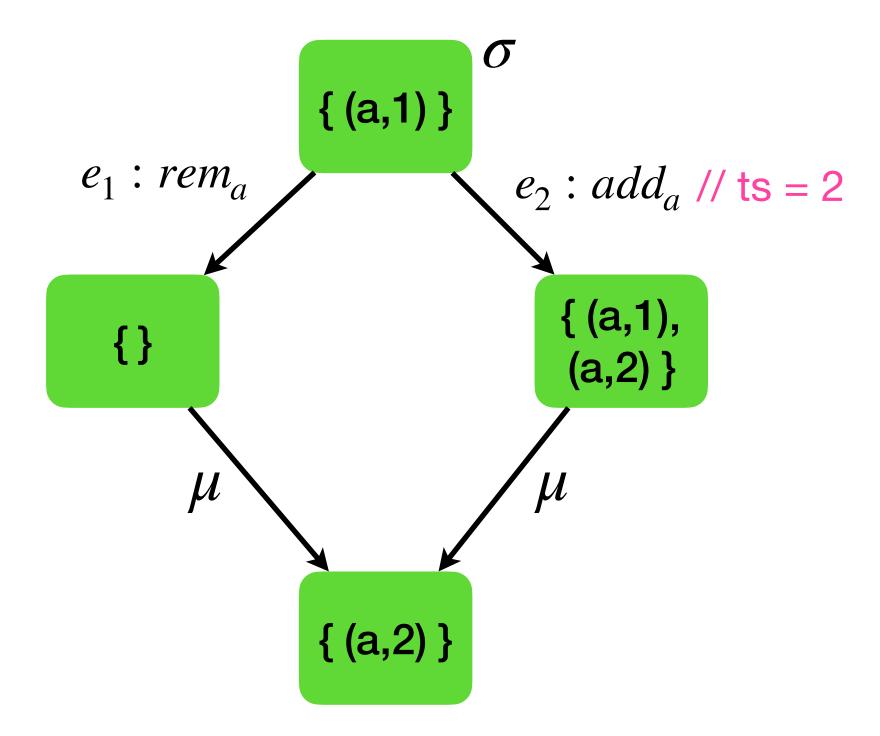
$$\mu(e_{\top}(l), e_{\top}(a), e_{\top}(b)) = e_{\top}(\mu(l, a, b))$$

[MergeIdempotence]

$$\mu(a,a,a)=a$$

$$\mu(l, a, b) = \mu(l, b, a)$$

$$rc = \{(rem_a, add_a) \mid a \in \mathbb{E}\}$$



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$$\mu(\sigma, e_1(\sigma), e_2(\sigma)) = e_2(e_1(\sigma))$$

[BOTTOMUP-2-OP]

$$\frac{e_1 \neq e_2}{\mu(l, e_1(a), e_2(b)) = e_2(\mu(l, e_1(a), b))}$$

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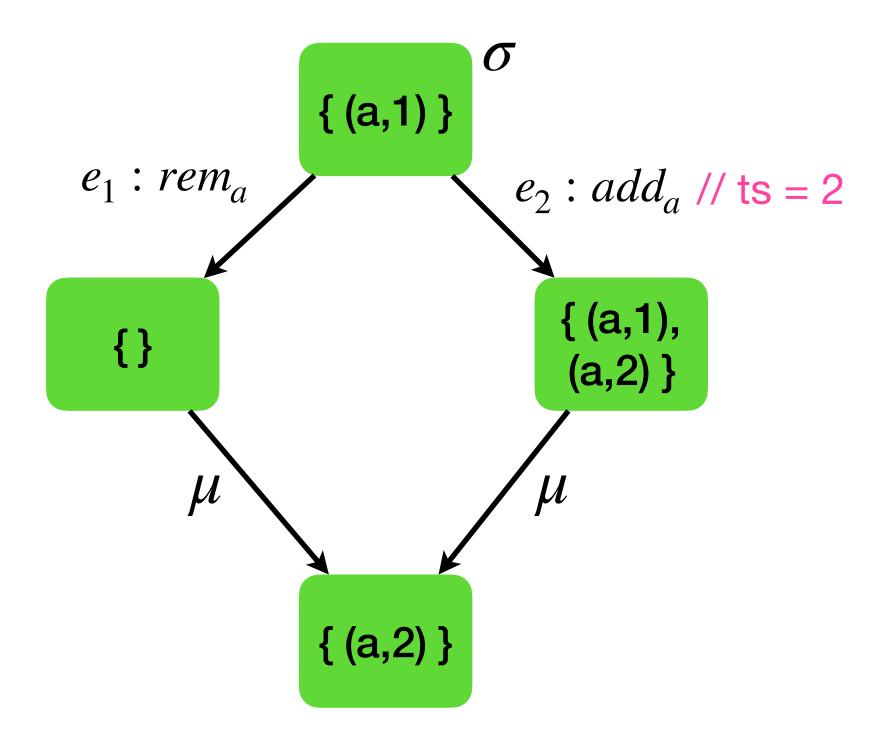
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$$e_2(\mu(\sigma, e_1(\sigma), \sigma)) = e_2(e_1(\sigma))$$

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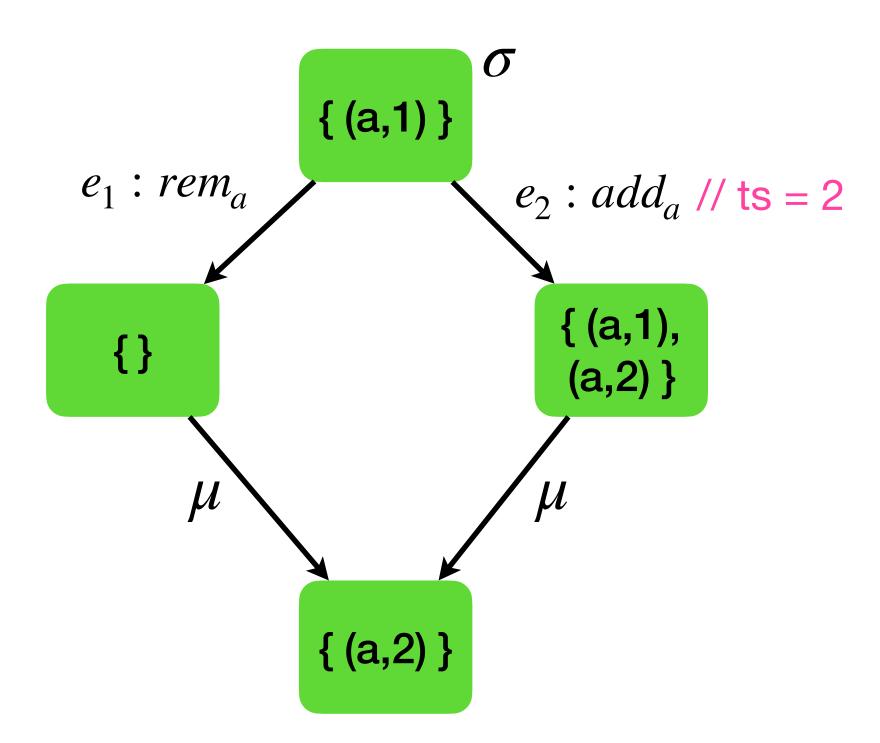
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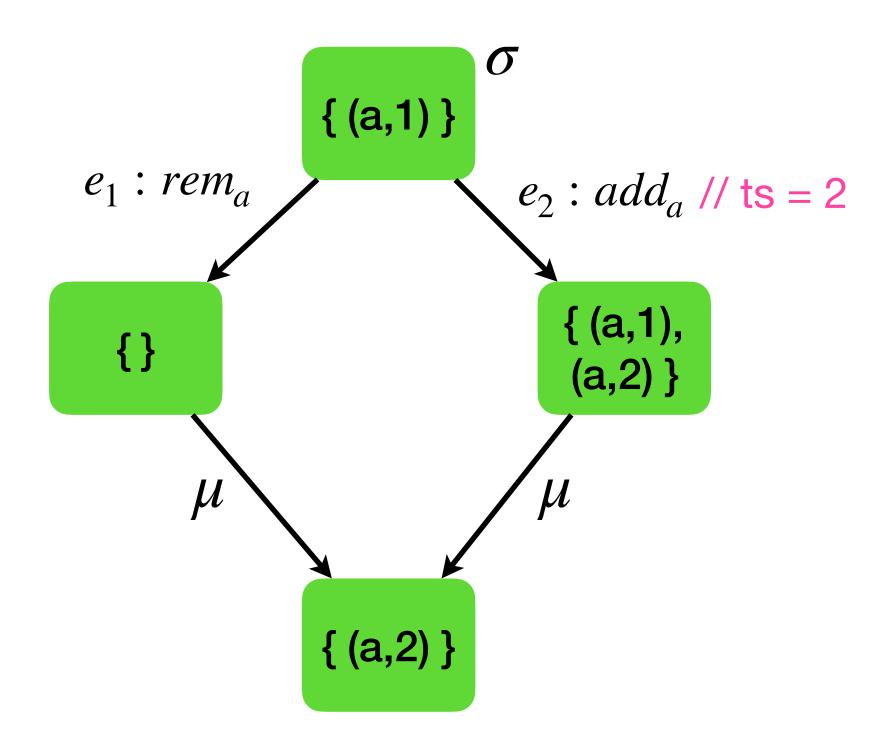
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To show

$$e_2(e_1(\mu(\sigma,\sigma,\sigma))) = e_2(e_1(\sigma))$$

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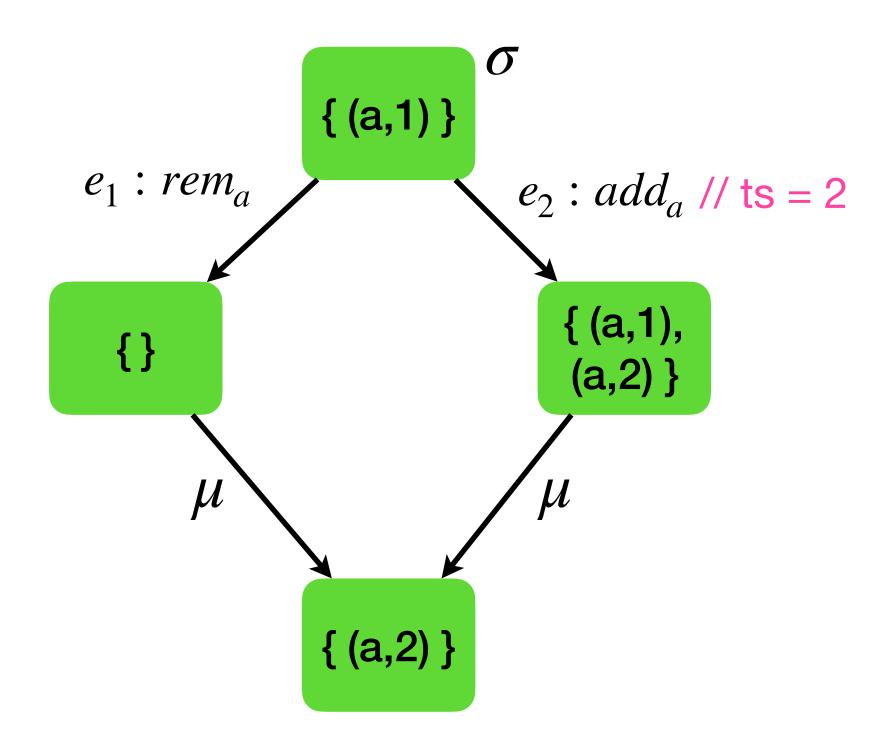
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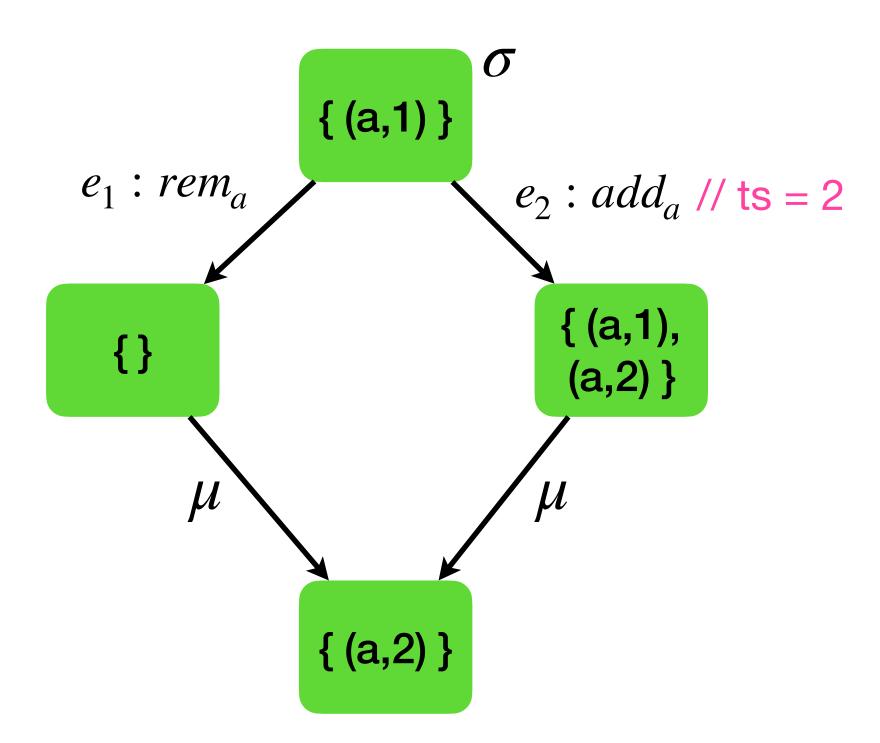
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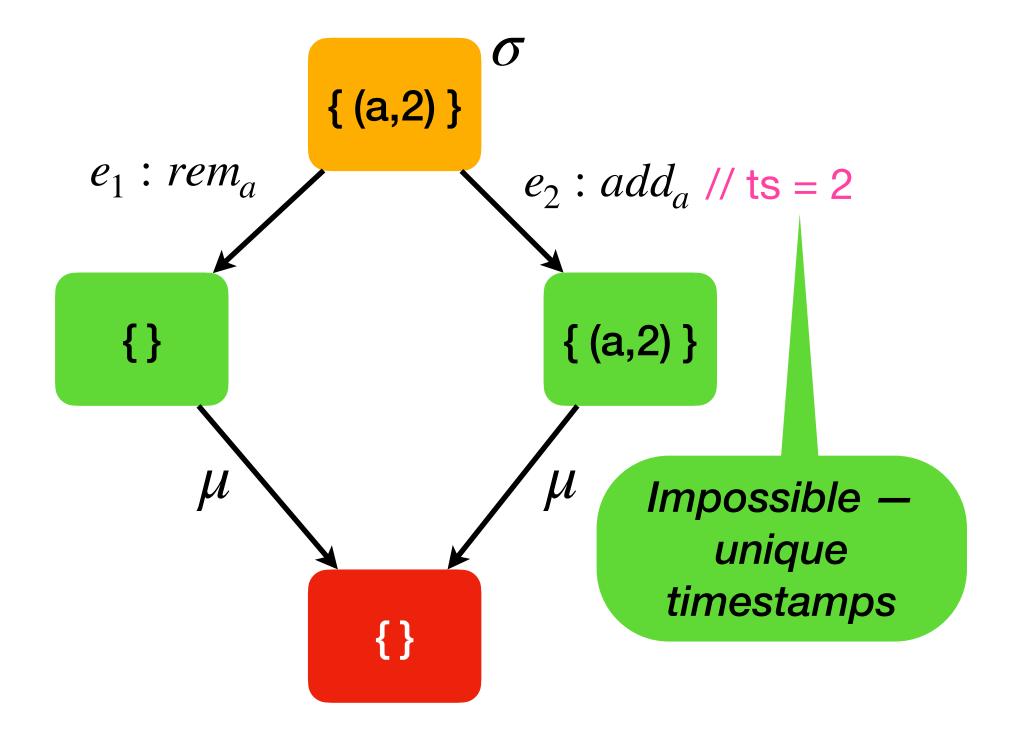
[MergeIdempotence]

$$\mu(a, a, a) = a$$

$$\mu(l, a, b) = \mu(l, b, a)$$

Making a good VC

$$rc = \{(rem_a, add_a) \mid a \in \mathbb{E}\}$$



[ВоттомUр-2-ОР]

$$e_1 \neq e_2 \quad e_1 \xrightarrow{\text{rc}} e_2 \lor e_1 \rightleftarrows e_2$$

$$\mu(l, e_1(a), e_2(b)) = e_2(\mu(l, e_1(a), b))$$

Cannot prove for an arbitrary \boldsymbol{l}

l must be a *feasible state*, obtained by application of updates on the initial state

 μ and e_2 may

not commute in

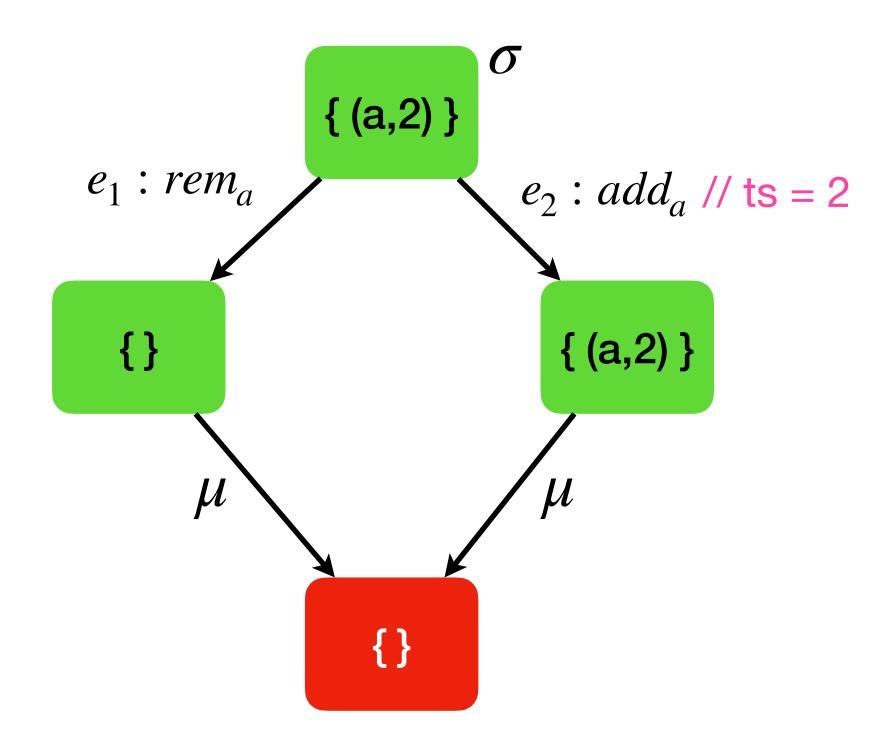
general

To show

$$\mu(\sigma, e_1(\sigma), e_2(\sigma)) = e_2(e_1(\sigma))$$

Induction over event sequences



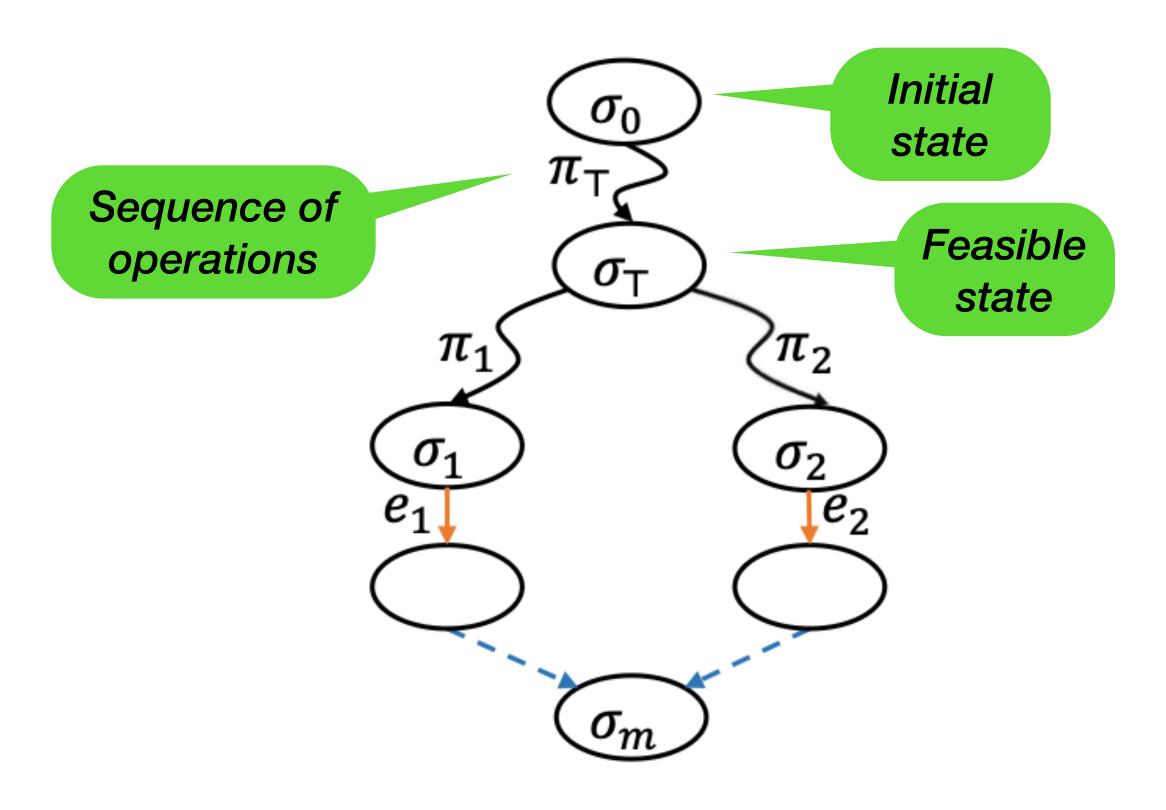


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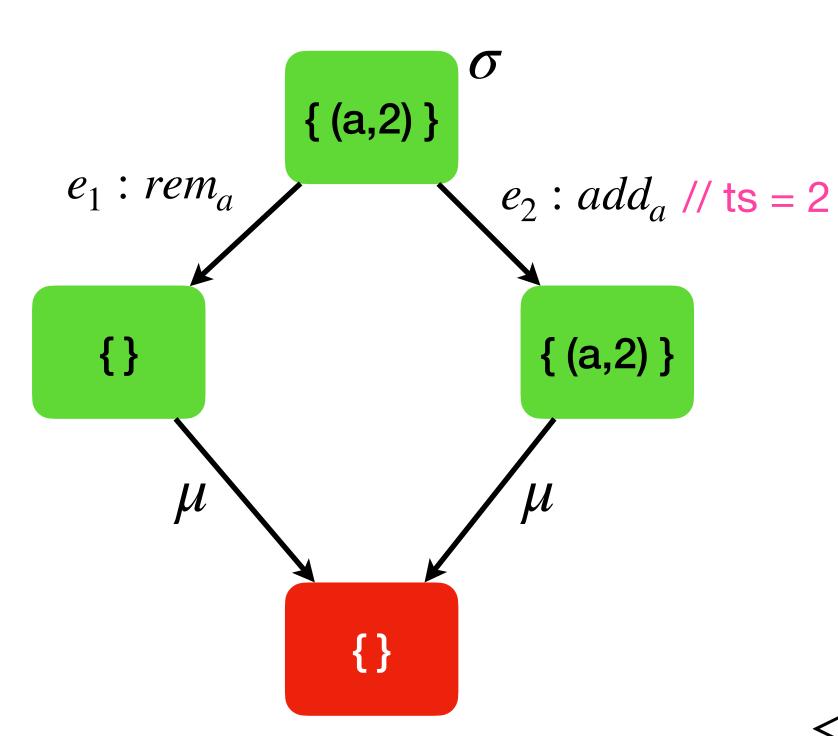
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Induction over event sequences





[BottomUp-2-OP]

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Induction on π_{\top}

$$\frac{}{\mu(\sigma_0, e_1(\sigma_0), e_2(\sigma_0)) = e_2(\mu(\sigma_0, e_1(\sigma_0), \sigma_0))}$$

Base case

 $(a,2) \notin \sigma_0$

Inductive case

To show

$$\mu(\sigma, e_1(\sigma), e_2(\sigma)) = e_2(e_1(\sigma))$$

Timestamps are unique

Linearizable MRDTs

Theorem 4.7. If an MRDT \mathcal{D} satisfies the VCs ψ^* (BottomUp-2-OP), ψ^* (BottomUp-0-OP), MergeIdempotence and MergeCommutativity, then \mathcal{D} is linearizable.

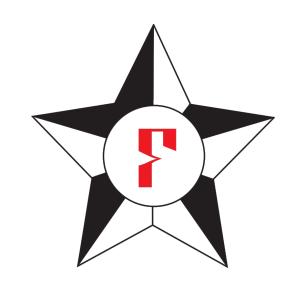
An MRDT that satisfies the algebraic properties is RA-linearizable

LEMMA 3.10. If MRDT \mathcal{D} is RA-linearizable, then for all executions $\tau \in [S_{\mathcal{D}}]$, for all transitions $C \xrightarrow{query(r,q,a)} C'$ in τ where $C = \langle N, H, L, G, vis \rangle$, there exists a sequence π consisting of all events in L(H(r)) such that $lo(C)_{|L(H(r))} \subseteq \pi$ and $a = query(\pi(\sigma_0), q)$.

RA-linearizable MRDT query results match those obtained on the linearised updates applied to the initial state

Verified MRDTs

MRDT	rc Policy	#LOC	Verification Time (s)
Increment-only counter [12]	none	6	0.72
PN counter [23]	none	10	1.64
Enable-wins flag*	disable \xrightarrow{rc} enable	30	29.80
Disable-wins flag*	enable \xrightarrow{rc} disable	30	37.91
Grows-only set [12]	none	6	0.45
Grows-only map [23]	none	11	4.65
OR-set [23]	$rem_a \xrightarrow{rc} add_a$	20	4.53
OR-set (efficient)*	$rem_a \xrightarrow{rc} add_a$	34	660.00
Remove-wins set*	$add_a \xrightarrow{rc} rem_a$	22	9.60
Set-wins map*	$del_{k} \xrightarrow{rc} set_{k}$	20	5.06
Replicated Growable Array [1]	none	13	1.51
Optional register*	unset \xrightarrow{rc} set	35	200.00
Multi-valued Register*	none	7	0.65
JSON-style MRDT*	Fig. 13	26	148.84



Neem also supports verification of RA-linearizability of state-based CRDTs https://github.com/prismlab/neem

Future work (we could do better)

- Automated verification returns yes / no / _(ツ)_/
 - Not pleasant for engineering
 - No counterexamples!
- Current work: model checking MRDTs against RA-linearizability
 - Fixed inputs & unrestricted concurrency
 - QuickCheck?