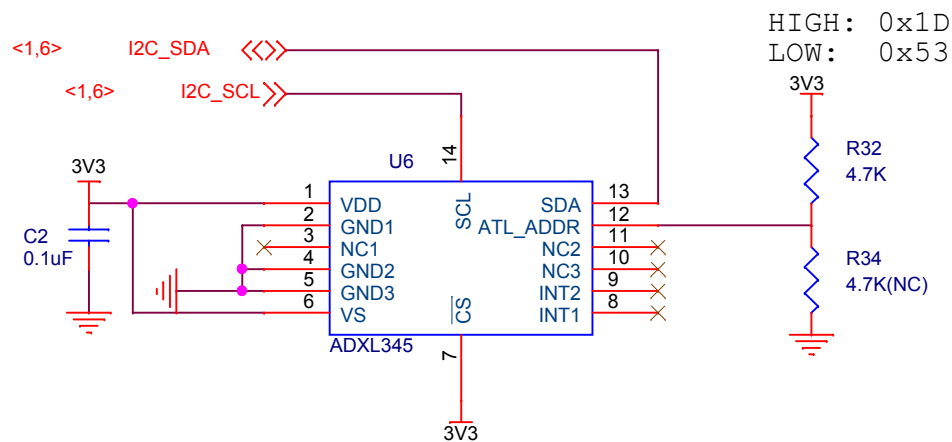
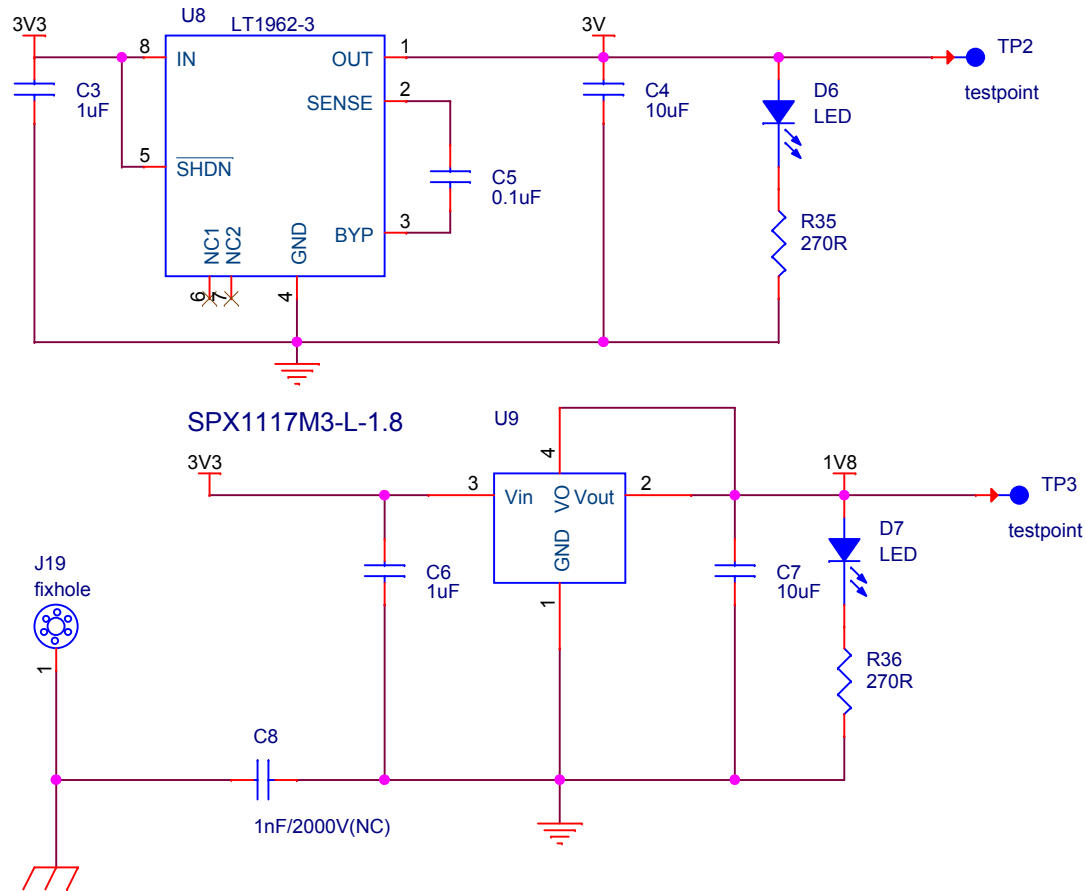


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I2C mode is enabled if the CS
pin is tied high to VDD I/O.

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