Dayananda Sagar University
SCHOOL OF ENGINEERING
KUDLU GATE, BANGALORE-560068

2nd Semester BCA – IA Test I
Computer Organization -II (16CA109)

Date: 20-02-2019		Time10.00 -11.30 am.
Max Marks: 50		Duration: 1 ½ hrs
Note:	1. Answer any FIVE full Questions 2. Each question carries 10 marks	
1.	Draw and Explain the architecture of 8086 microprocessor.	10 Marks
2. A.	Explain 8086 flag registers in detail.	5 Marks
2. B.	What is Bus? Explain the different types of bus in Microprocessor architecture.	5 Marks
3.	Explain the addressing mode in 8086 Microprocessor and write two instructions for each.	10 Marks
4.	Determine the starting address, ending address and the memory location address for the given segment and offset addresses. SS = 2300H DS = 1000H CS = 2000H DI = 2000h IP = 2000H BP = 3200H	10 Marks
5.	Write an ALP program to perform linear search?	10 Marks
6.	Explain the following. • Opcode • Operand • Segment address • Offset address • Segment Override	10 Marks