

課題9-6(1)

プログラム

circuit.v

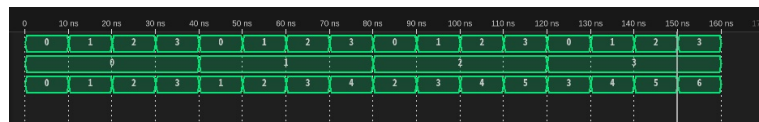
```
module kadai09_6 (  
    input [1:0] a,  
    input [1:0] b,  
    output [3:0] z  
);  
  
    assign z = {2'b00, a} + {2'b00, b};  
  
endmodule
```

出力

VCD info: dumpfile kadai09_6_test.vcd opened for output.

```
0: a = 00, b = 00, z = 0000  
10: a = 01, b = 00, z = 0001  
20: a = 10, b = 00, z = 0010  
30: a = 11, b = 00, z = 0011  
40: a = 00, b = 01, z = 0001  
50: a = 01, b = 01, z = 0010  
60: a = 10, b = 01, z = 0011  
70: a = 11, b = 01, z = 0100  
80: a = 00, b = 10, z = 0010  
90: a = 01, b = 10, z = 0011  
100: a = 10, b = 10, z = 0100  
110: a = 11, b = 10, z = 0101  
120: a = 00, b = 11, z = 0011  
130: a = 01, b = 11, z = 0100  
140: a = 10, b = 11, z = 0101  
150: a = 11, b = 11, z = 0110
```

タイムチャート



波形