EEE1001	Basic Electrical and Electronics Engineering	g LTPJC						
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Pre-requisite	NIL	Syllabus version						
	1	v. 1.0						
Course Objective								
1. To understand the various laws and theorems applied to solve electric circuits and networks								
2. To provide the students with an overview of the most important concepts in Electrical and								
Electronics Engineering which is the basic need for every engineer								
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Expected Course	Outcome:							
Solve basic electrical circuit problems using various laws and theorems								
2. Analyze AC power circuits and networks, its measurement and safety concerns								
3. Classify and compare various types of electrical machines								
4. Design and implement various digital circuits								
5. Analyze the characteristics of semiconductor devices and comprehend the various modulation								
techniques in communication engineering								
6. Design and conduct experiments to analyze and interpret data								
Student Learning	g Outcomes (SLO): 1,2,9							
	ty to apply mathematics and science in engineering appli	ications						
_	understanding of the subject related concepts and of cont							
	n solving ability- solving social issues and engineering pr							
	rircuits	5 hours						
Basic circuit elem	ents and sources, Ohms law, Kirchhoff's laws, series and	d parallel connection of						
	Node voltage analysis, Mesh current analysis, Thevenin's							
transfer theorem		•						
	rircuits	6 hours						
Alternating voltag	es and currents, AC values, Single Phase RL, RC, RLC	Series circuits, Power						
	wer Factor- Three Phase Systems – Star and Delta C							
Power Measureme	ent – Electrical Safety –Fuses and Earthing, Residential v	wiring						
M 1 1 2 E1 4	. 134 1:							
	trical Machines	7 hours						
	rking Principle and applications of DC Machines, Tra							
and Three-phase Induction motors, Special Machines-Stepper motor, Servo Motor and BLDC								
motor								
Madulant Digit	al Crystoms							
	ral Systems	5 hours						
	t concepts, Representation of Numerical Data in Binar	y roim- Comomational						
logic circuits, Syn	thesis of logic circuits							
Module:5 Semi	conductor devices and Circuits	7 hours						
Conduction in Semiconductor materials, PN junction diodes, Zener diodes, BJTs, MOSFETs, Rectifiers, Feedback Amplifiers using transistors. Communication Engineering: Modulation and								
Demodulation - Amplitude and Frequency Modulation								
	,							

Total Lecture hours: 30 hours

Text Book(s)							
1.	1. John Bird, 'Electrical circuit theory and technology', Newnes publications, 4 t h Edition, 2010.						
Reference Books							
1.	Allan R. Hambley, 'Electrical Engineering -Principles & Applications' Pearson Education, First Impression, 6/e, 2013						
2.	Simon Haykin, 'Communication Systems', John Wiley & Sons, 5 t h Edition, 2009.						
3.	Charles K Alexander, Mathew N O Sadiku, 'Fundamentals of Electric Circuits', Tata McGraw Hill, 2012.						
4.	Batarseh, 'Power Electronics Circuits', Wiley, 2003						
5.	H. Hayt, J.E. Kemmerly and S. M. Durbin, 'Engineering Circuit Analysis', 6/e, Tata McGraw Hill, New Delhi, 2011.						
7.	Fitzgerald, Higgabogan, Grabel, 'Basic Electrical Engineering', 5t h edn, McGraw Hill, 2009.						
8.	S.L.Uppal, 'Electrical Wiring Estimating and Costing', Khanna publishers, NewDelhi, 2008.						
Mode of Evaluation: CAT / Assignment / Quiz / FAT / Project / Seminar							
List of Challenging Experiments (Indicative)							
1.	Thevenin's and Maximum Power Transfer Theorems – Impedance matching of source and load				2 hours		
2.	Sinusoidal steady state Response	2 hours					
3.	Three phase power measurement	2 hours					
4.	Staircase wiring circuit layout for	2 hours					
5.	Fabricate and test a PCB layout for	2 hours					
6.	Half and full adder circuits.	2 hours					
7.	Full wave Rectifier circuits used in characteristics of the semiconduct	2 hours					
8.	Regulated power supply using zer Zener diode used	2 hours					
9.	Lamp dimmer circuit (Darlington Study the characteristics of the tra	2 hours					
10.	Characteristics of MOSFET	2 hours					
	20 hours						
Mode of assessment: Assignment / FAT							
Recommended by Board of Studies 29/05/2015							
Approved by Academic Council 37 th AC Date 16/06/2015							