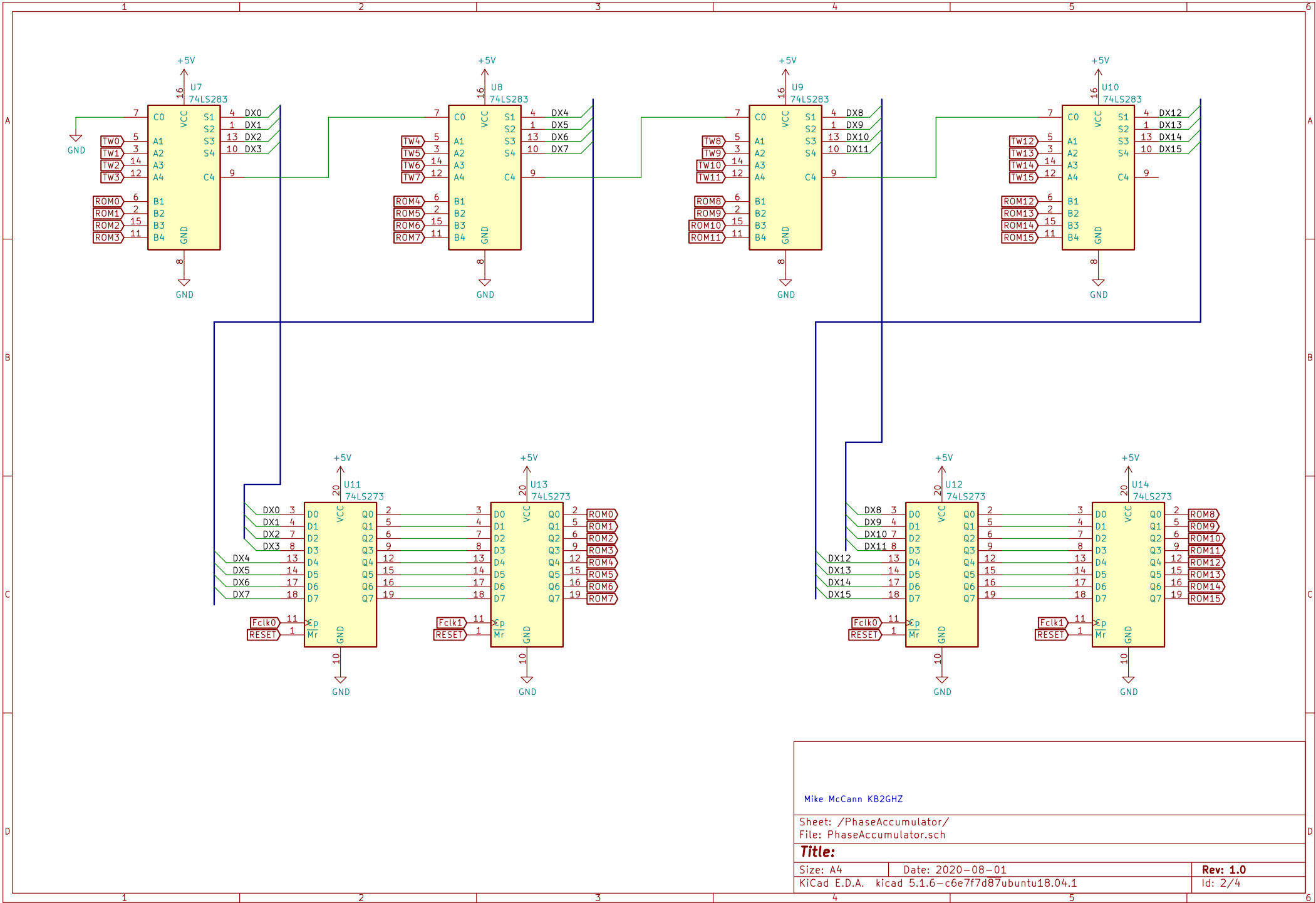
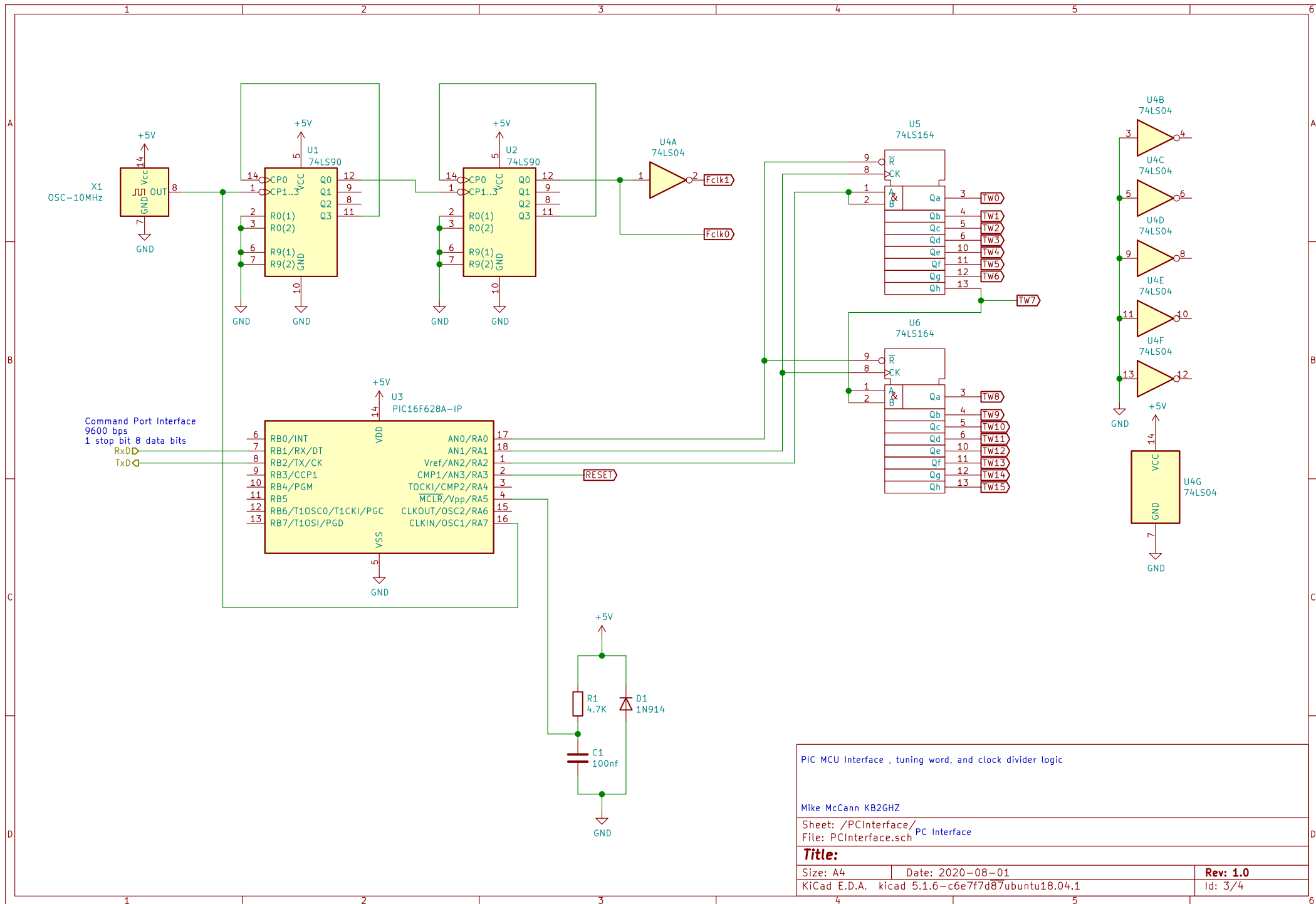


	1	2	3	4	5	6
A	<div>Sheet: PhaseAccumulator</div> <div>File: PhaseAccumulator.sch</div>					<div>Sheet: PCInterface</div> <div>File: PCInterface.sch</div>
B	<div>Sheet: OutputSection</div> <div>File: OutputSection.sch</div>					
C						
D						<div>Mike McCann KB2GHZ</div> <div>Sheet: / File: DDS.sch</div> <div><b>Title:</b> <span>TTL Logic Based Direct Digital Synthesizer (DDS)</span></div> <div><div>Size: A4</div><div>Date: 2020-08-01</div><div>Rev: 1.0</div></div> <div><div>KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1</div><div>Id: 1/4</div></div>
	1	2	3	4	5	6





PIC MCU Interface , tuning word, and clock divider logic

Mike McCann KB2GHZ

Sheet: /PCInterface/  
File: PCInterface.sch PC Interface

**Title:**

Size: A4 Date: 2020-08-01

KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1

**Rev: 1.0**

Id: 3/4

Id: 4/4