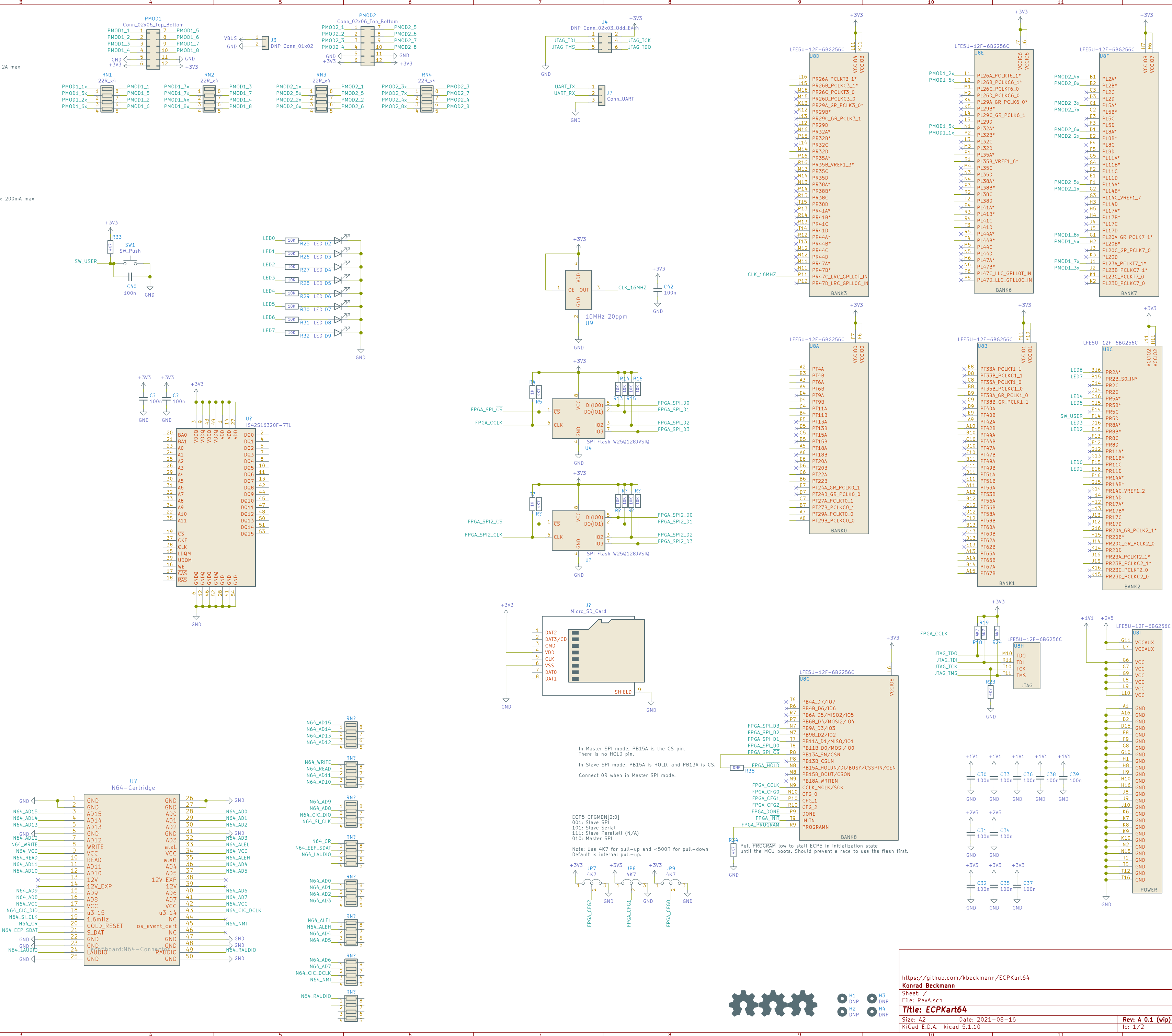


Errata

RevA 0.1:

TODO

RevA 0.1:



In Master SPI mode, PB15A is the CS pin. There is no HOLD pin.

In Slave SPI mode, PB15A is HOLD, and PB13A is CS. Connect OR when in Master SPI mode.

ECPS CFMON[2:0]
001: Slave SPI
101: Slave Serial
111: Slave Parallel (N/A)
010: Master SPI

Note: Use 4K7 for pull-up and <500R for pull-down. Default is internal pull-up.

Pull PROGRAM low to stall ECPS in initialization state until the MCU boots. Should prevent a race to use the flash first.



H1 DNP
H2 DNP
H3 DNP
H4 DNP

<https://github.com/kbeckmann/ECPCart64>

Konrad Beckmann

Sheet: /
File: RevA.sch

Title: **ECPCart64**

Size: A2 Date: 2021-08-16 Rev: **A 0.1 (wip)**

KiCad E.D.A. kicad 5.1.10 Id: 1/2

